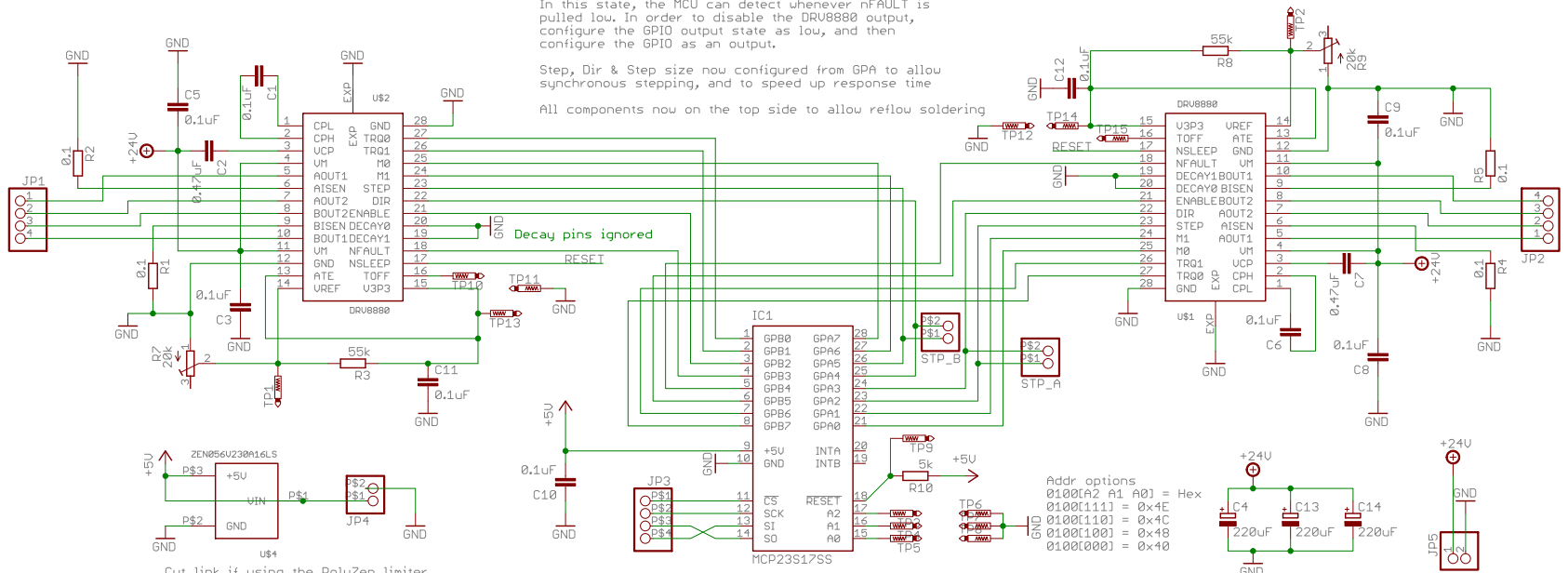


A microcontroller can retain control of the ENABLE pin while in retry mode if it is operated like an open-drain output. Many microcontrollers support this. When the DRV8880 is operating normally, configure the MCU GPIO as an input. In this state, the MCU can detect whenever nFAULT is pulled low. In order to disable the DRV8880 output, configure the GPIO output state as low, and then configure the GPIO as an output.

Step, Dir & Step size now configured from GPA to allow synchronous stepping, and to speed up response time

All components now on the top side to allow reflow soldering



Decay pins ignored

Cut link if using the PolyZen limiter

25mA output source/sink, set to input for 'z' or high impedance

Addr options  
 0100(A2 A1 A0) = Hex  
 0100(111) = 0x4E  
 0100(110) = 0x4C  
 0100(100) = 0x48  
 0100(000) = 0x40

I2C/SPI Dual stepper Controller, rev. A5  
 1/16 step, 45V, 1.4A/phase  
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