



MEMORY MAP

RAM1 0X0000 - 0X7FFF
RAM2 0X8000 - 0XFFFF

I/O MAP

UART 0X0000 - 0X01FF
DSK 0X0200 - 0X02FF
PIO 0X0400 - 0X07FF

74HC299 FUNCTION

----- S1 S0 OE

ILLEGAL => 0 0 X

WRITE DATA BUS => 0 1 0

ILLEGAL => 1 0 X

READ DATA BUS => 0 1 1

DATA BUS HI-Z => 1 1 1

Write bus sequence:

- 1) S1=1, OE=1 => Hi-Z
- 2) S1=0, SPI Write data
- 3) S1=0, OE=0 => preset data to bus
- 4) S1=1, OE=1 => Hi-Z

Read bus sequence:

- 1) S1=1, OE=1 => Hi-Z
- 2) SPI dummy write
- 3) S1=0, OE=1 => read SPI from bus
- 4) S1=1, OE=1 => Hi-Z