



Rev 4: Alpha board  
 Rev 3: Updated concept [posted 2019-02-27] + 32 pins RAM socket  
 Rev 2: Added glitch suppression for /IOSEL  
 Rev 1: Proposal jwolfoam [posted 2019-01-11]

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Sheet: /  
 File: Expansion.sch

**Title: SPI expansion proposal [DRAFT]**

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KiCad E.D.A. kicad 4.0.6

Rev: 4

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**TODO / OPEN ISSUES:**  
 Mappings from bus lines to/from signals for ease of programming  
 Add blinkenlight breakouts  
 Add raw bus breakouts (Q? mimic RC2014 assignment?)

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