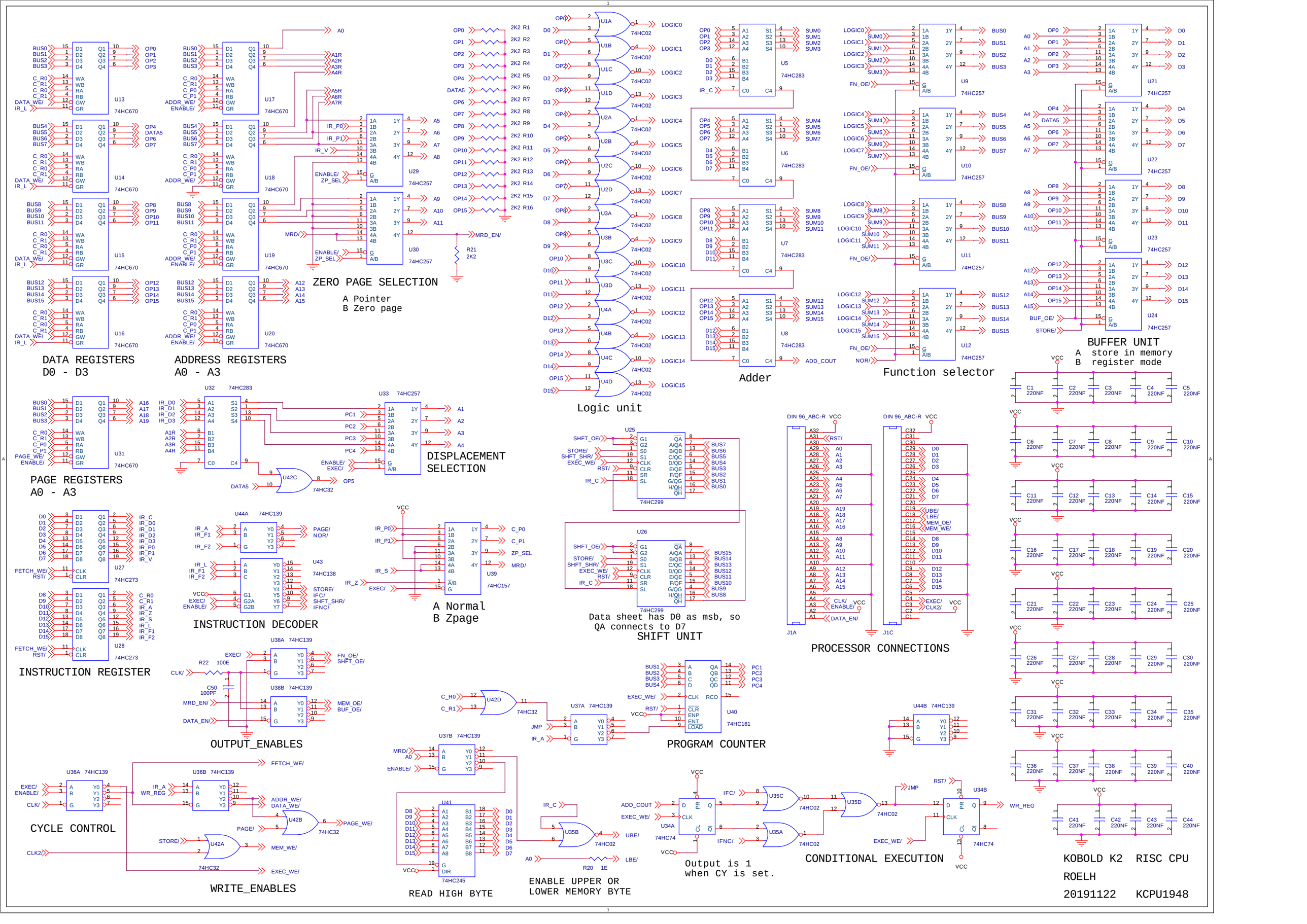
DATA REGISTERS
D0 - D3
ADDRESS REGISTERS
A0 - A3
ZERO PAGE SELECTION
A Pointer
B Zero page
Logic unit
Adder
Function selector
BUFFER UNIT
A store in memory
B register mode
PAGE REGISTERS
A0 - A3
DISPLACEMENT
SELECTION
INSTRUCTION DECODER
A Normal
B Zpage
INSTRUCTION REGISTER
Data sheet has D0 as msb, so
QA connects to D7
SHIFT UNIT
PROCESSOR CONNECTIONS
OUTPUT_ENABLES
PROGRAM COUNTER
CYCLE CONTROL
WRITE_ENABLES
READ HIGH BYTE
ENABLE UPPER OR
LOWER MEMORY BYTE
Output is 1
when CY is set.
CONDITIONAL EXECUTION
KOBOLD K2 RISC CPU
ROELH
20191122 KCPU1948