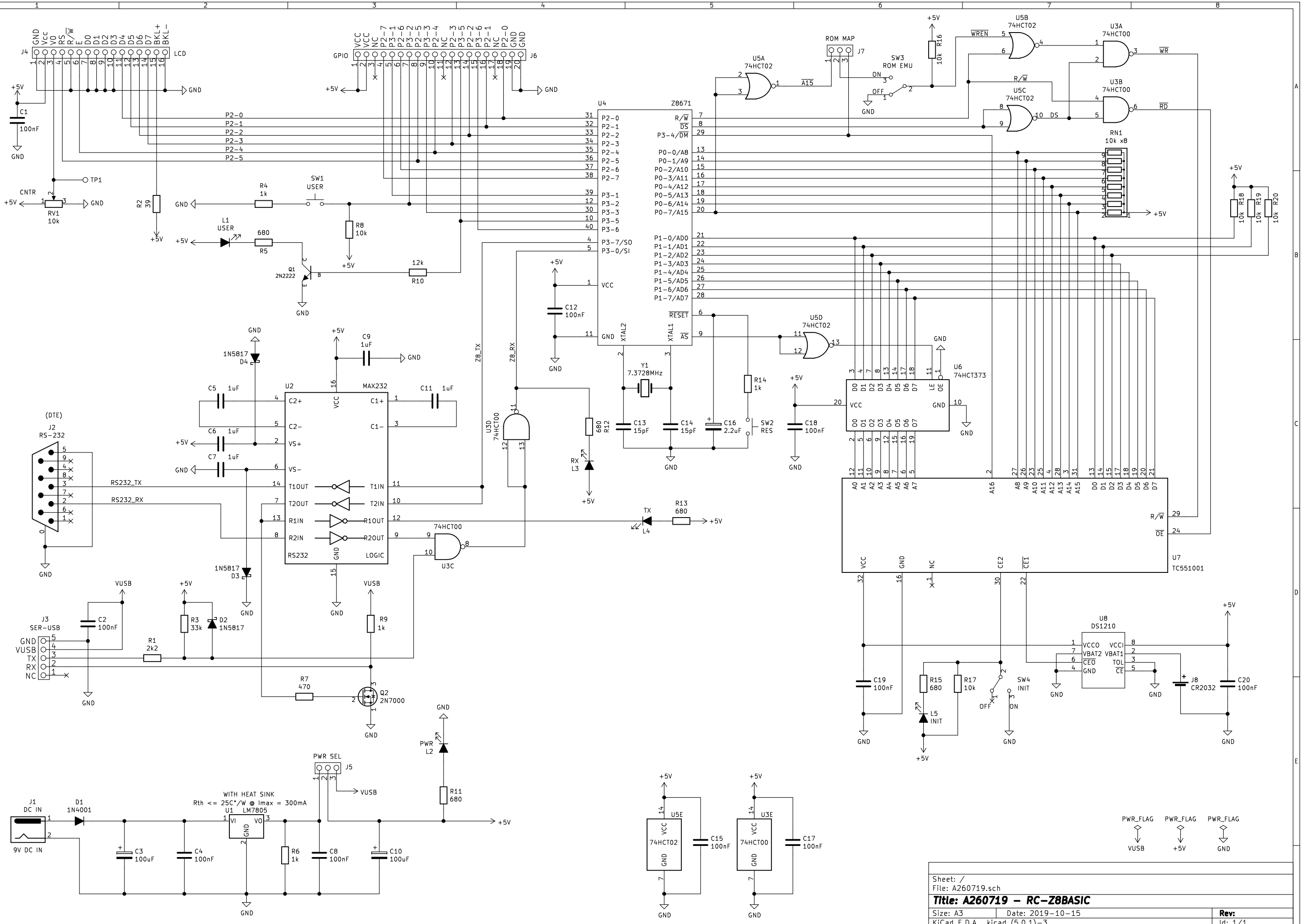
J4 LCD
GND Vcc VO RS R/W E D0 D1 D2 D3 D4 D5 D6 D7 BKL+ BKL−
J6 GPIO
VCC VCC NC P2−7 P3−1 P2−6 P3−2 P2−5 P3−3 P2−4 NC NC P2−3 P3−5 P2−2 P3−6 P2−1 NC P2−0 GND GND
C1 100nF
TP1
CNTR
RV1 10k
R2 39
R4 1k
SW1 USER
L1 USER
R5 680
Q1 2N2222
R8 10k
R10 12k
U4 Z8671
P2−0 P2−1 P2−2 P2−3 P2−4 P2−5 P2−6 P2−7
P3−1 P3−2 P3−3 P3−5 P3−6 P3−7/SO P3−0/SI
R/W DS P3−4/DM
P0−0/A8 P0−1/A9 P0−2/A10 P0−3/A11 P0−4/A12 P0−5/A13 P0−6/A14 P0−7/A15
P1−0/AD0 P1−1/AD1 P1−2/AD2 P1−3/AD3 P1−4/AD4 P1−5/AD5 P1−6/AD6 P1−7/AD7
VCC GND XTAL2 XTAL1 RESET AS
C12 100nF
Y1 7.3728MHz
C13 15pF
C14 15pF
C16 2.2uF
SW2 RES
R14 1k
C18 100nF
U5A 74HCT02
U5B 74HCT02
U5C 74HCT02
U5D 74HCT02
ROM MAP
J7
SW3 ROM EMU
R16 10k
WREN
A15
U3A 74HCT00
U3B 74HCT00
WR
RD
DS
RN1 10k x8
R18 10k
R19 10k
R20 10k
U6 74HCT373
D0 D1 D2 D3 D4 D5 D6 D7 LE OE VCC GND
U7 TC551001
A0 A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16
D0 D1 D2 D3 D4 D5 D6 D7
R/W OE CE2 CE1 VCC GND NC
C19 100nF
U8 DS1210
VCCO VCCI VBAT2 VBAT1 CEO TOL GND CE
J8 CR2032
C20 100nF
R15 680
R17 10k
SW4 INIT
L5 INIT
(DTE) J2 RS−232
RS232_TX
RS232_RX
U2 MAX232
C1+ C1− C2+ C2− VS+ VS− T1IN T2IN R1IN R2IN T1OUT T2OUT R1OUT R2OUT RS232 GND LOGIC VCC
C5 1uF C6 1uF C7 1uF C9 1uF C11 1uF
D3 1N5817
D4 1N5817
Z8_TX Z8_RX
U3C 74HCT00
U3D 74HCT00
TX L4
RX L3
R12 680
R13 680
J3 SER−USB
GND VUSB TX RX NC
C2 100nF
R1 2k2
R3 33k
D2 1N5817
R9 1k
R7 470
Q2 2N7000
PWR L2
R11 680
PWR SEL J5
J1 DC IN
9V DC IN
D1 1N4001
U1 LM7805
VI VO GND
WITH HEAT SINK
Rth <= 25C°/W @ Imax = 300mA
C3 100uF
C4 100nF
R6 1k
C8 100nF
C10 100uF
U5E 74HCT02
U3E 74HCT00
C15 100nF
C17 100nF
PWR_FLAG PWR_FLAG PWR_FLAG
VUSB +5V GND
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File: A260719.sch
Title: A260719 − RC−Z8BASIC
Size: A3
Date: 2019−10−15
Rev:
KiCad E.D.A. kicad (5.0.1)−3
Id: 1/1