# Revision History

<table>
<thead>
<tr>
<th>Date</th>
<th>Pages</th>
<th>Revision</th>
</tr>
</thead>
<tbody>
<tr>
<td>Oct-08-2019</td>
<td>N/A</td>
<td>Design forked from SCP-PI-RS485-D</td>
</tr>
<tr>
<td>Oct-12-2019</td>
<td>4</td>
<td>Added latched power control with tactile switch button, P MOS and two complementary BJT</td>
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<tr>
<td>Oct-12-2019</td>
<td>4</td>
<td>Added power supply minimum input voltage enable. Moved electrolytic to power control area.</td>
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<tr>
<td>Oct-12-2019</td>
<td>4</td>
<td>Removed 7V power supply and RS485 Interface</td>
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<tr>
<td>Oct-12-2019</td>
<td>N/A</td>
<td>Release A</td>
</tr>
<tr>
<td>Nov-15-2019</td>
<td>4</td>
<td>Changed capacitor On-Off button from 100 nF to 200 nF. Changed series resitor from 400 KΩ to 200 KΩ, Increased releability on voltages above 20V.</td>
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<tr>
<td>Nov-15-2019</td>
<td>N/A</td>
<td>Reannotate schematics</td>
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<tr>
<td>Nov-15-2019</td>
<td>N/A</td>
<td>Release B</td>
</tr>
</tbody>
</table>
BLOCK DIAGRAM

Back Converter & Hat Connector

- SHUTDOWN
- POWEROFF
- RUN
- FEEDBACK

SuperCap UPS

- SHUTDOWN
- POWEROFF
- RUN
- FEEDBACK

Local Fiducials
- FID1
- FID2
- FID3

Revision: B

Title: Block Diagram

Sheet: 3 of 5  Author: acortellini
Date: Nov-17-2019  License: CC BY-NC-SA 4.0
Most Programmers have a supply voltage of +5V which will damage the GPIO pins.

DESIGN NOTE:
Do not program the ATTiny25 with the Raspberry Pi connected.

Most Programmers have a supply voltage of +5V which will damage the GPIO pins.