

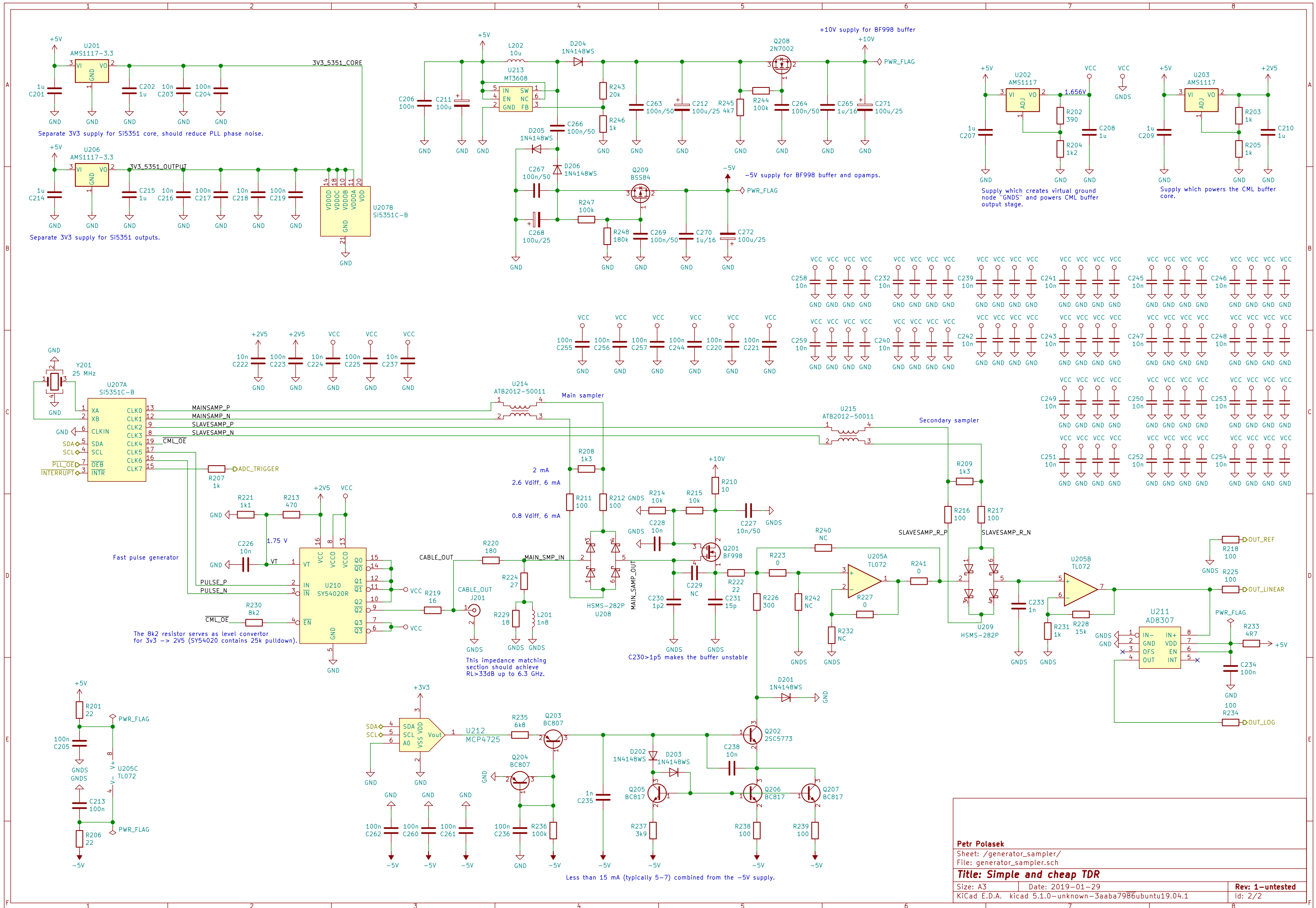
Petr Polasek

Sheet: /
File: TDR_5351_core.sch

Title: Simple and cheap TDR

Size: A4 Date: 2019-01-29
KiCad E.D.A. kicad 5.1.0-unknown-3aaba7986ubuntu19.04.1

Rev: 1-untested
Id: 1/2



Petr Polasek
 Sheet: /generator_sampler/
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Title: Simple and cheap TDR
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Less than 15 mA (typically 5-7) combined from the -5V supply.

This impedance matching section should achieve $RL > 33dB$ up to 6.3 GHz.

The 8k2 resistor serves as level converter for 3v3 -> 2V5 (SY54020 contains 25k pull-down).

Fast pulse generator

Separate 3V3 supply for Si5351 core, should reduce PLL phase noise.

Separate 3V3 supply for Si5351 outputs.

Supply which creates virtual ground node "GNDS" and powers CML buffer output stage.

Supply which powers the CML buffer core.

+10V supply for BF998 buffer

-5V supply for BF998 buffer and opamps.