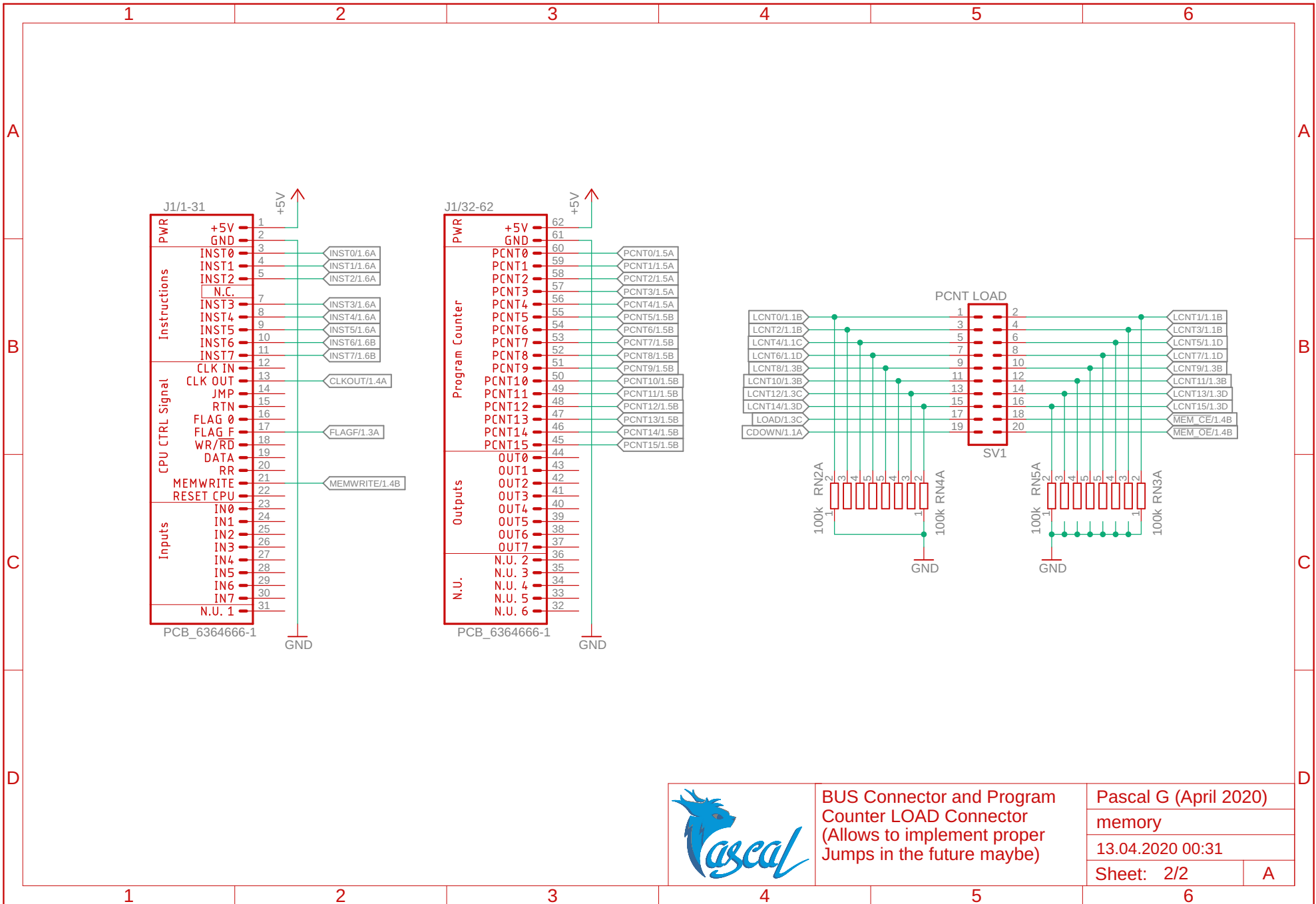


Memory Board with 64k x 8Bit Jumper J1 allows to experiment with interlaced memory, turning it into a 64k x 16Bit system (12Bit IO Address range)

Pascal G (April 2020)  
memory  
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BUS Connector and Program Counter LOAD Connector (Allows to implement proper Jumps in the future maybe)

Pascal G (April 2020)

memory

13.04.2020 00:31

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