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%-----
% QUIZ_COMPLEMENT-1
% VER 1.0
%
% QUIZ 2'COMPLEMENT FOR PERSEUS-7 (CPU:R6502) AND PARALLEL INTERFACE
%
% ENTERING A BINARY NUMBER AT THE PARALLEL INTERFACE SWITCHES, ITS TWO'S COMPLEMENT IS
% DISPLAYED ON THE LEDS
%
% HAND ASSEMBLED
%
% MITSURU YAMADA 07/FEB/2020
%
%-----
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%
%-----
%
% SYMBOL          DATA
% PORT           $4000          PARALLEL INTERFACE ADDRESS (R/W)
%-----
%
% ADDRESS(HEX) DATA(HEX)
% RESET VECTOR
% .ORIGIN $3FC
%           3FC  00 02
%-----
% QUIZ 2'COMPLEMENT
% .ORIGIN $200
%
% COMP_2      CLC          200  18          CLEAR CARRY FLAG
%             LDA PORT    201  AD 00 40      INPUT BINARY NUMBER
%             EOR #$FF    204  49 FF          BIT INVERSION
%             ADC #$01    206  69 01          +1
%             STA PORT    208  8D 00 40      OUTPUT ANSWER
%             CLC          20B  18
%             BCC COMP_2  20C  90 F2          REPEAT BACK TO THE BEGINNING
%
%-----
% END OF PROGRAM

```