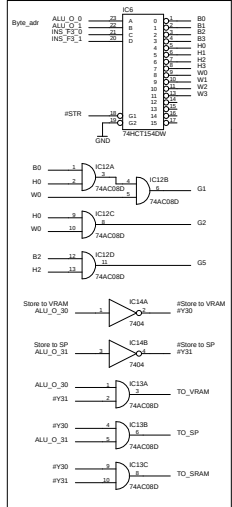
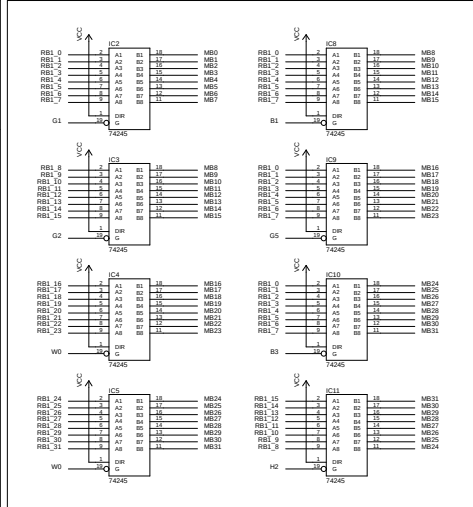


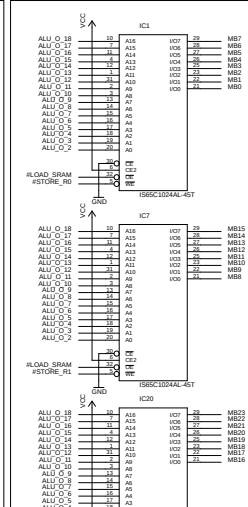
Function Decoder



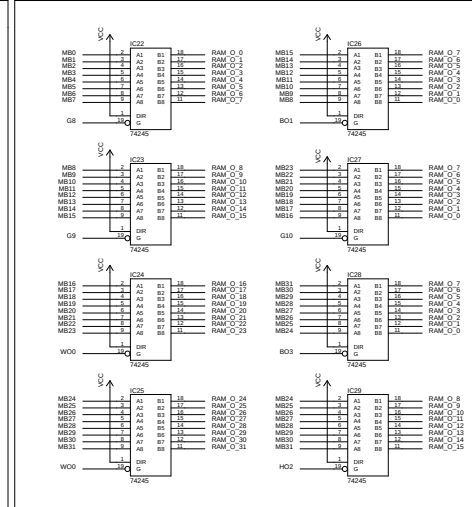
Byte Steering logic - input



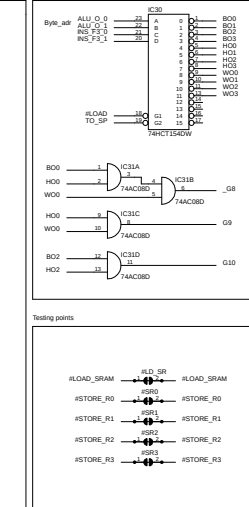
SRAM



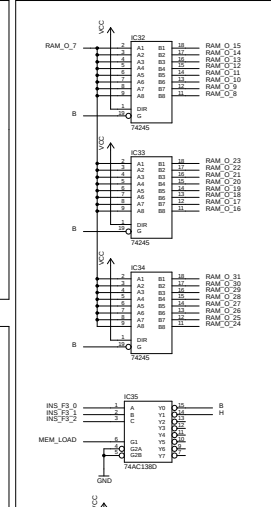
Byte Steering logic - output



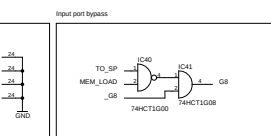
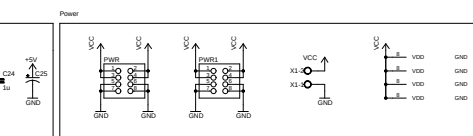
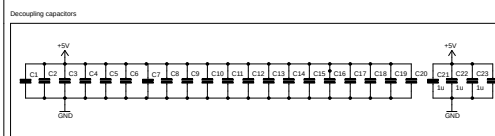
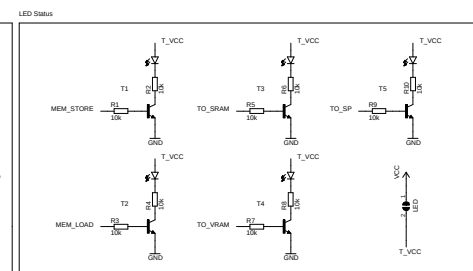
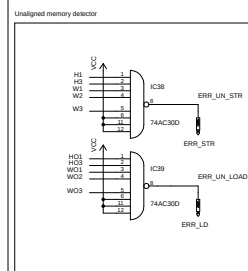
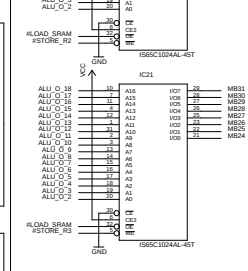
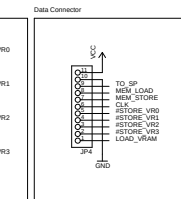
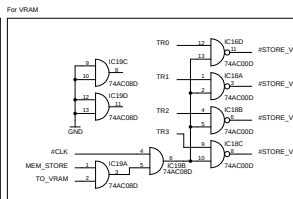
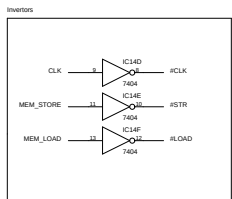
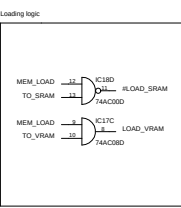
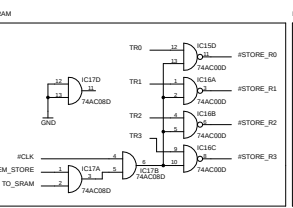
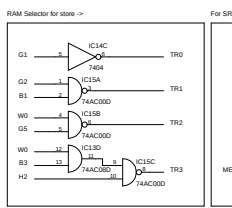
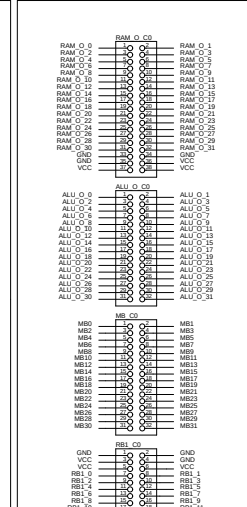
Function Decoder



Logic for signed operation



Connectors



RAM  
28/10/2020