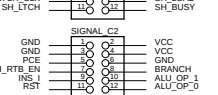
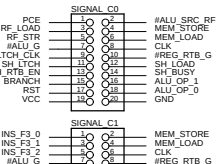
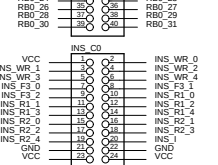
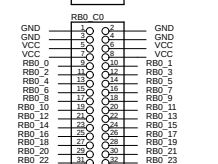
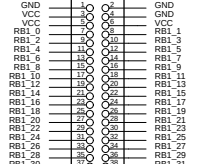
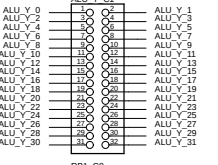
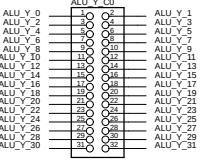
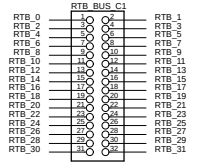
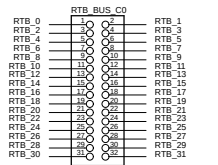
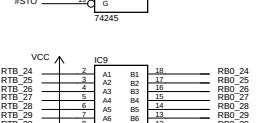
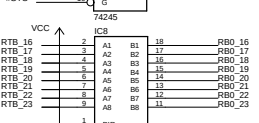
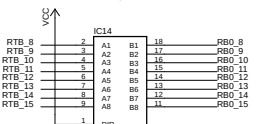
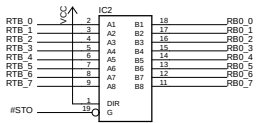


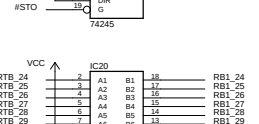
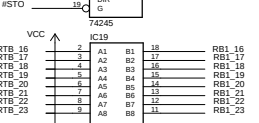
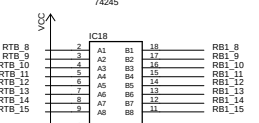
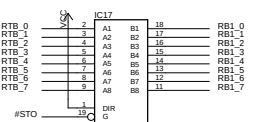
Connectors



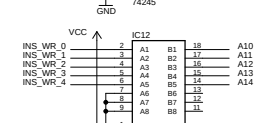
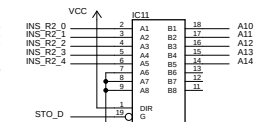
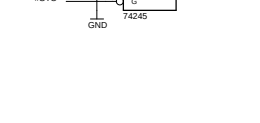
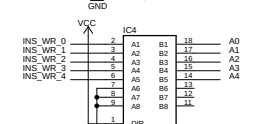
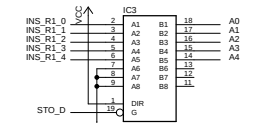
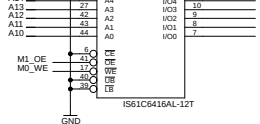
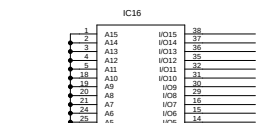
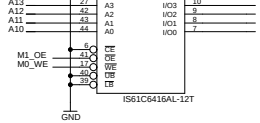
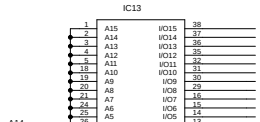
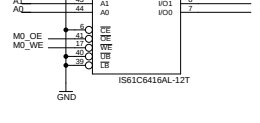
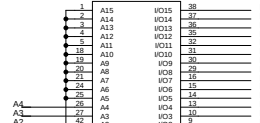
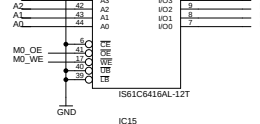
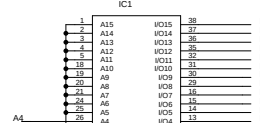
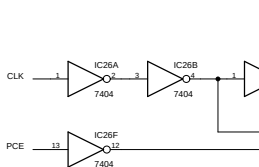
REGISTER BANK 0



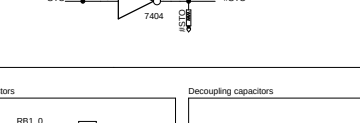
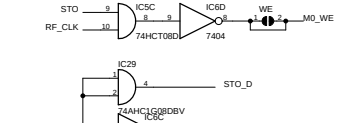
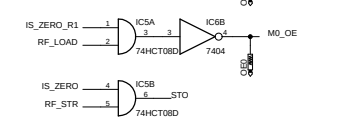
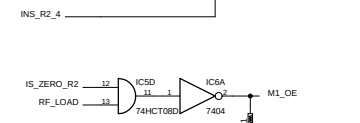
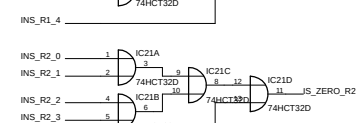
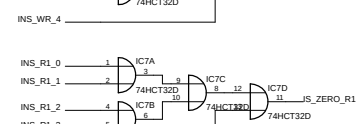
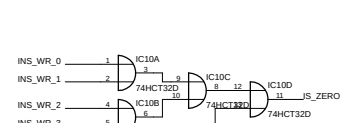
REGISTER BANK 1



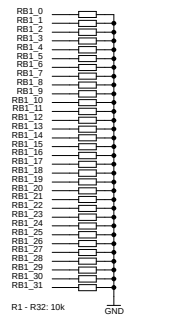
Rising-edge detector



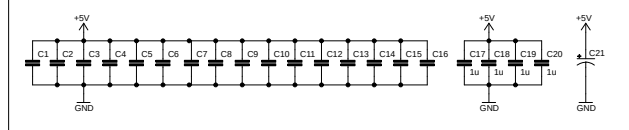
Additional logic



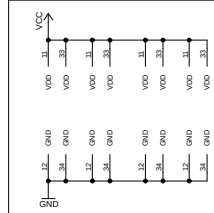
Pull-down resistors



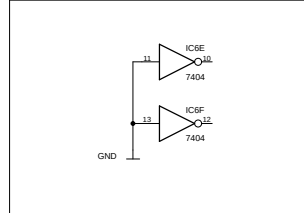
Decoupling capacitors



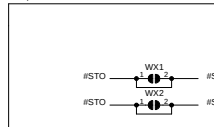
Power



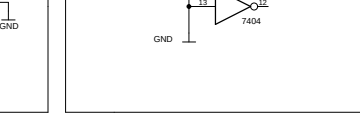
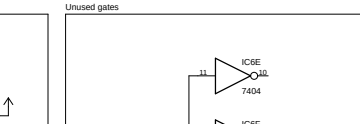
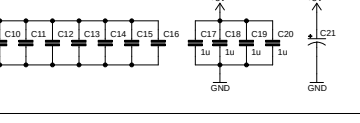
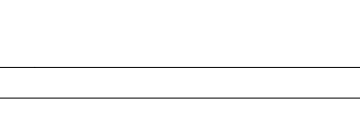
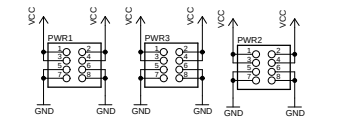
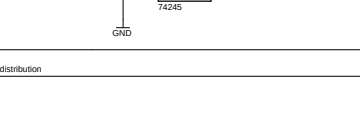
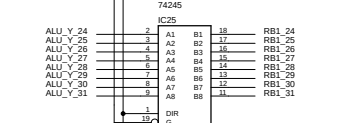
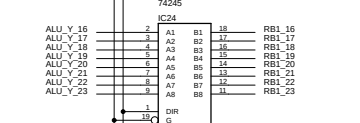
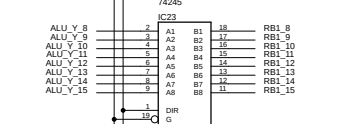
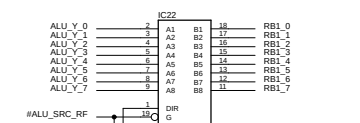
Unused gates



Test pins



ALU_MUX (SEL. 1)



REGISTER FILE 28/10/2020