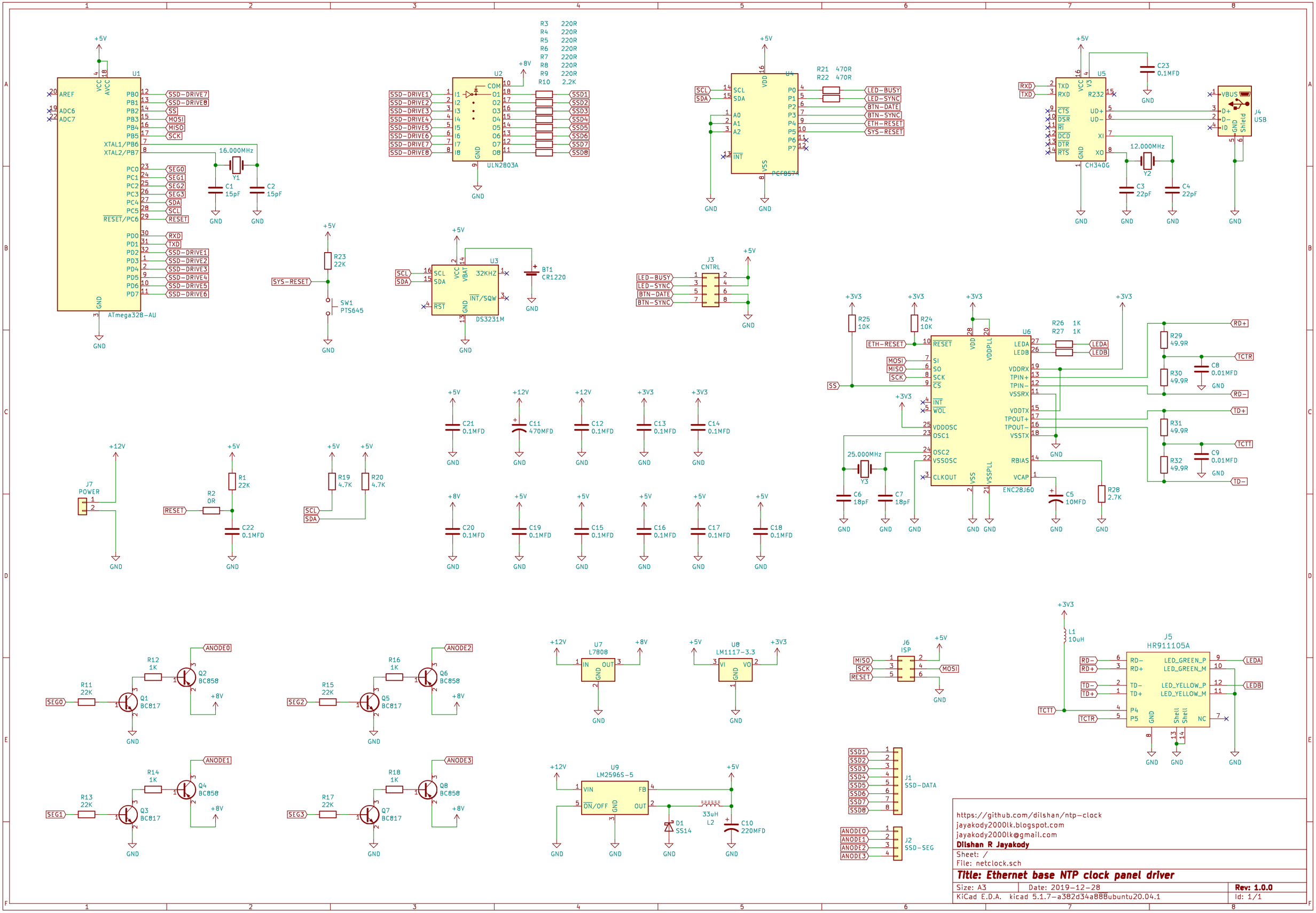
U1 ATmega328-AU
U2 ULN2803A
U3 DS3231M
U4 PCF8574
U5 CH340G
U6 ENC28J60
U7 L7808
U8 LM1117-3.3
U9 LM2596S-5
J1 SSD-DATA
J2 SSD-SEG
J3 CNTRL
J4 USB
J5 HR911105A
J6 ISP
J7 POWER
Y1 16.000MHz
Y2 12.000MHz
Y3 25.000MHz
BT1 CR1220
SW1 PTS645
D1 SS14
L1 10uH
L2 33uH
C10 220MFD
R10 2.2K
R21 470R
R22 470R
R26 1K
R27 1K
https://github.com/dilshan/ntp-clock
jayakody2000lk.blogspot.com
jayakody2000lk@gmail.com
Dilshan R Jayakody
Sheet: /
File: netclock.sch
Title: Ethernet base NTP clock panel driver
Size: A3
Date: 2019-12-28
Rev: 1.0.0
KiCad E.D.A. kicad 5.1.7-a382d34a88ubuntu20.04.1
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