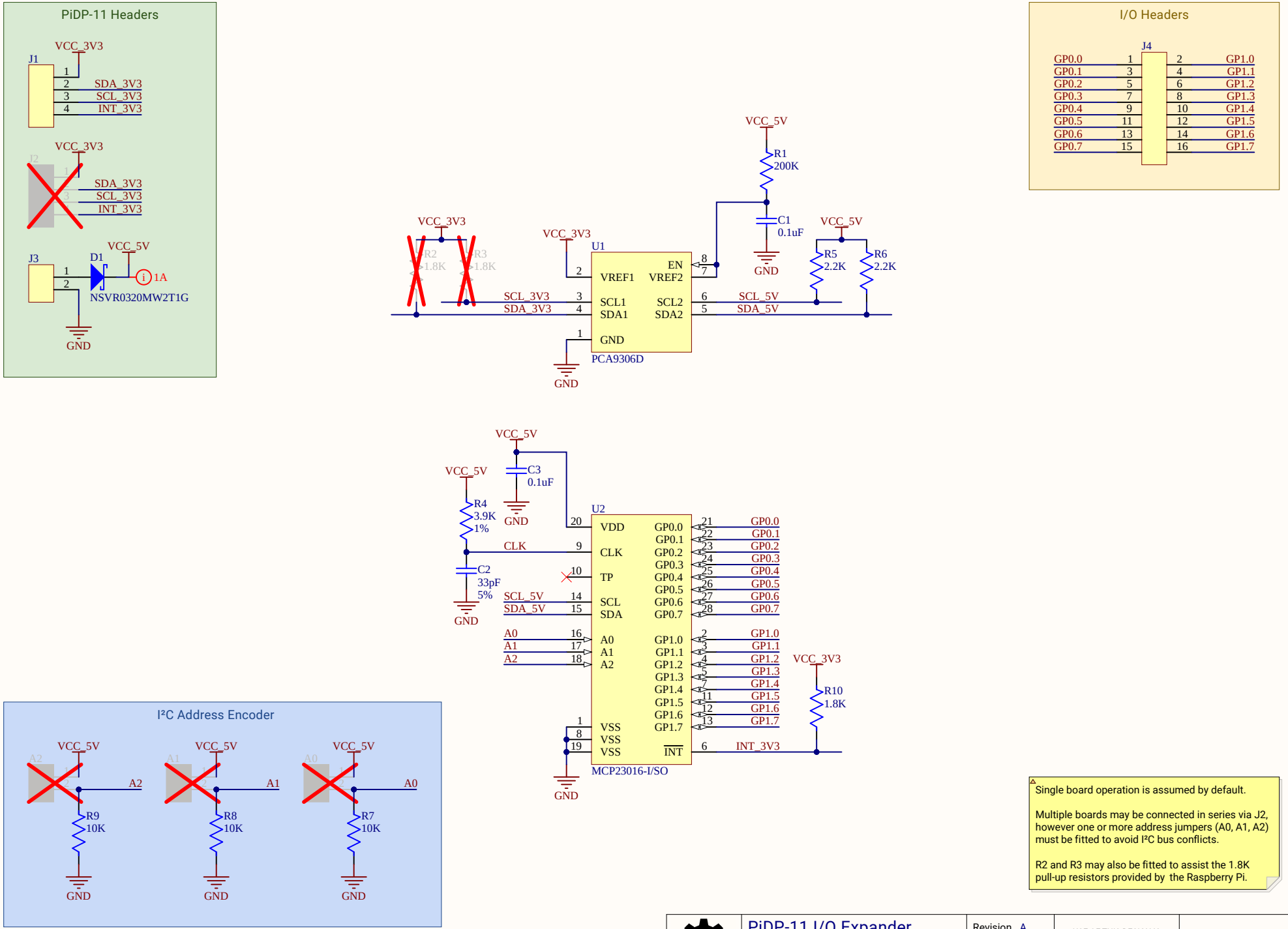

TABLE 1-4: DEVICE ADDRESS						
0	1	0	0	A2	A1	A0

Single board operation is assumed by default.

Multiple boards may be connected in series via J2, however one or more address jumpers (A0, A1, A2) must be fitted to avoid I²C bus conflicts.

R2 and R3 may also be fitted to assist the 1.8K pull-up resistors provided by the Raspberry Pi.

open source hardware

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Top Sheet	Sheet 1 of 1	
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File: PiDP11IOExpander.SchDoc		

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GitHub

https://github.com/sstallion/PCB-PiDP11IOExpander