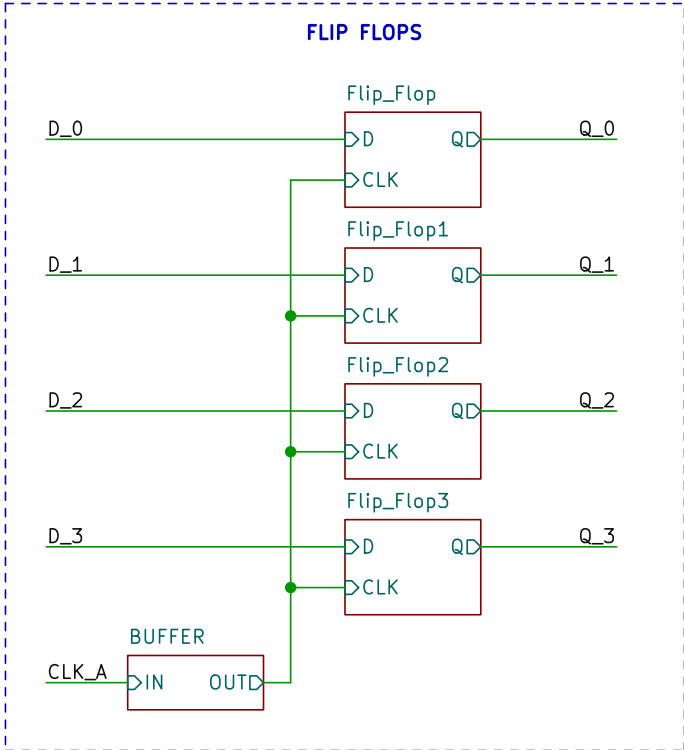
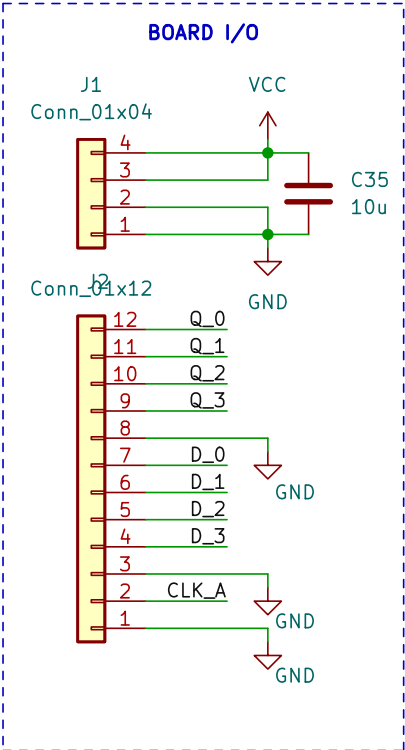


# 4BIT REGISTER



jessefarrell92@gmail.com

Sheet: /  
File: 4Bit\_Register.kicad\_sch

**Title: 4Bit Register**

Size: A5      Date: 2022-05-21

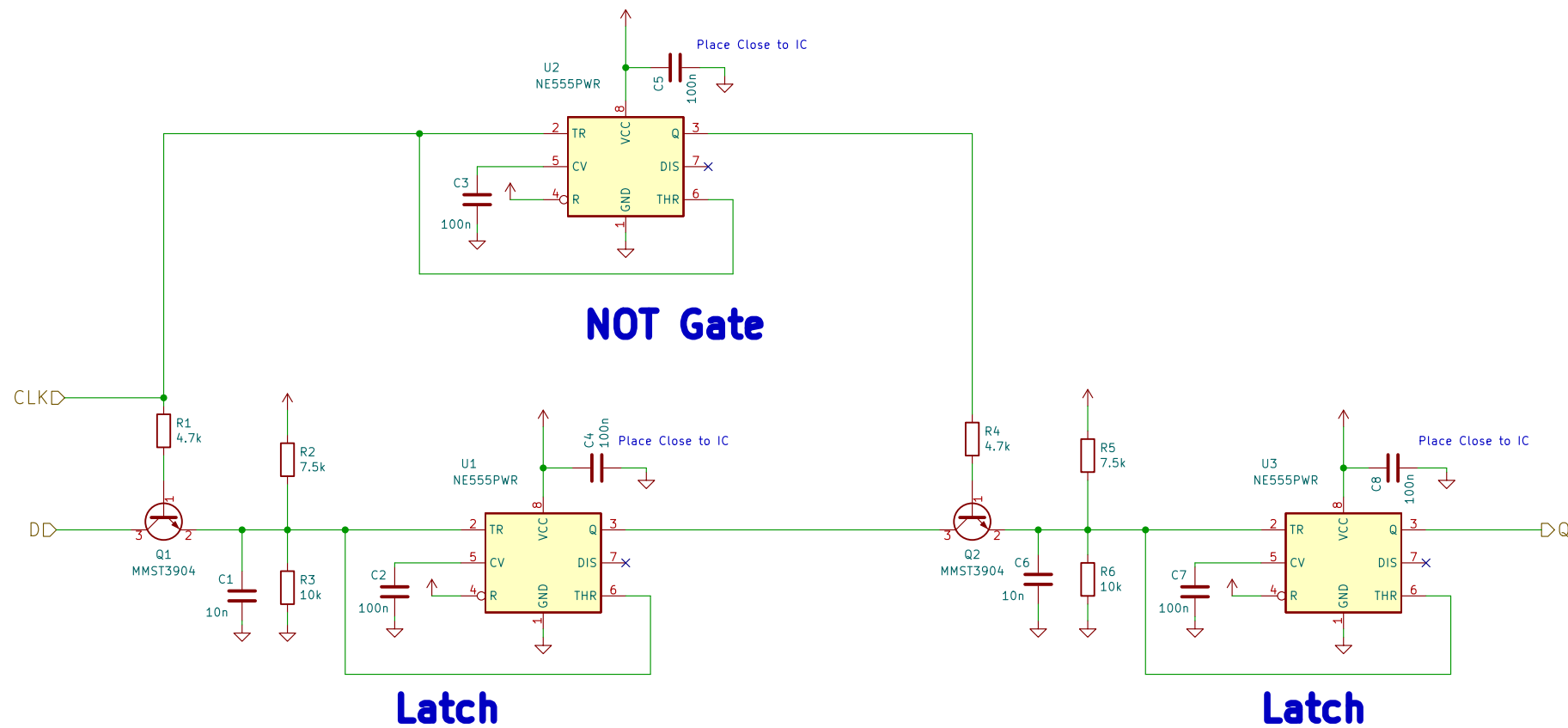
**Rev: 1**

KiCad E.D.A. kicad (6.0.5)

Id: 1/6

Note that this flip flop will trigger on the falling edge of CLK. Also the input D is flipped twice, hence output Q is D

# 555 FLIP FLOP



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Sheet: /Flip\_Flop/  
File: block\_555flipflop.kicad\_sch

**Title: 555 FLIP FLOP**

Size: A4 Date: 2022-05-21

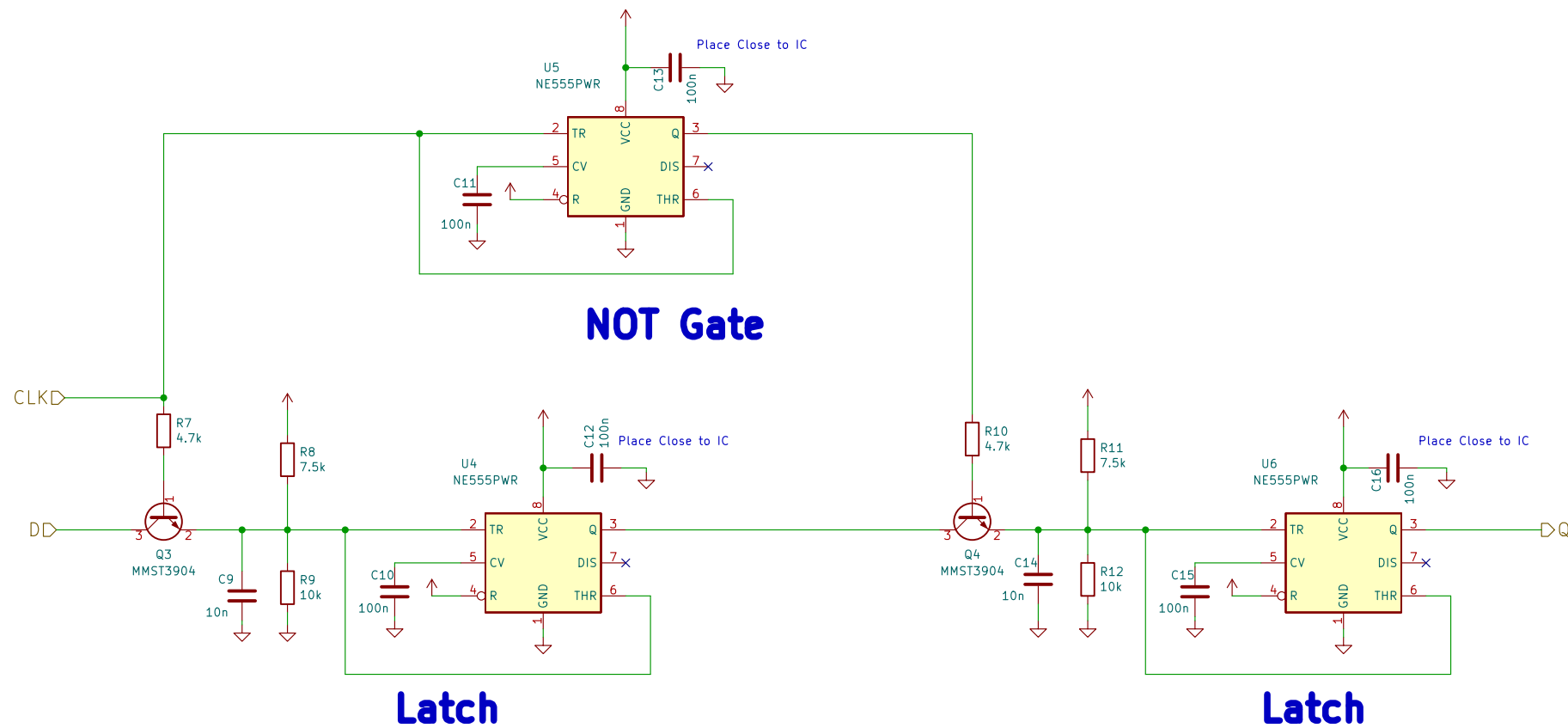
KiCad E.D.A. kicad (6.0.5)

Rev: 1

Id: 2/6

Note that this flip flop will trigger on the falling edge of CLK. Also the input D is flipped twice, hence output Q is D

# 555 FLIP FLOP



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Sheet: /Flip\_Flop1/  
File: block\_555flipflop.kicad\_sch

**Title: 555 FLIP FLOP**

Size: A4 Date: 2022-05-21

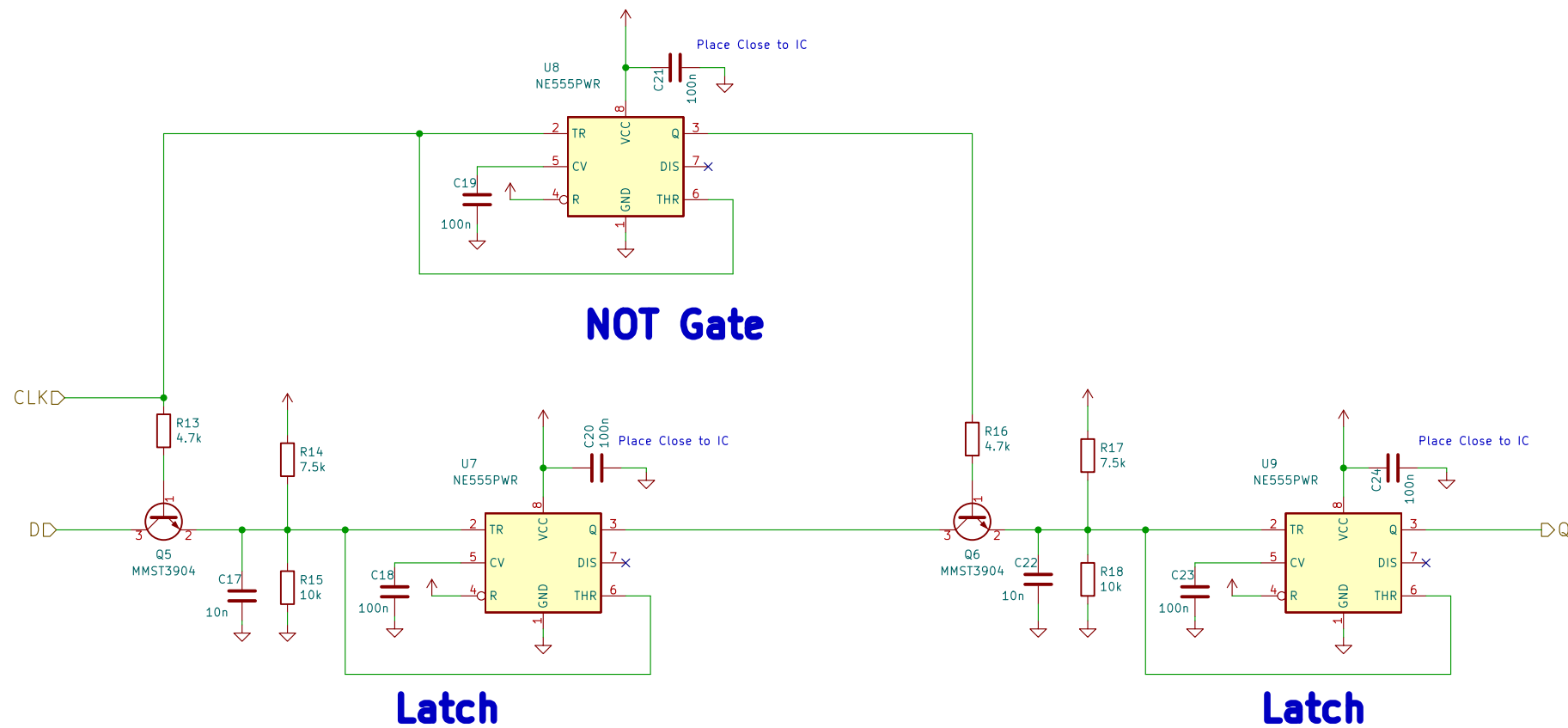
KiCad E.D.A. kicad (6.0.5)

Rev: 1

Id: 3/6

Note that this flip flop will trigger on the falling edge of CLK. Also the input D is flipped twice, hence output Q is D

# 555 FLIP FLOP



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Sheet: /Flip\_Flop2/  
File: block\_555flipflop.kicad\_sch

**Title: 555 FLIP FLOP**

Size: A4 Date: 2022-05-21

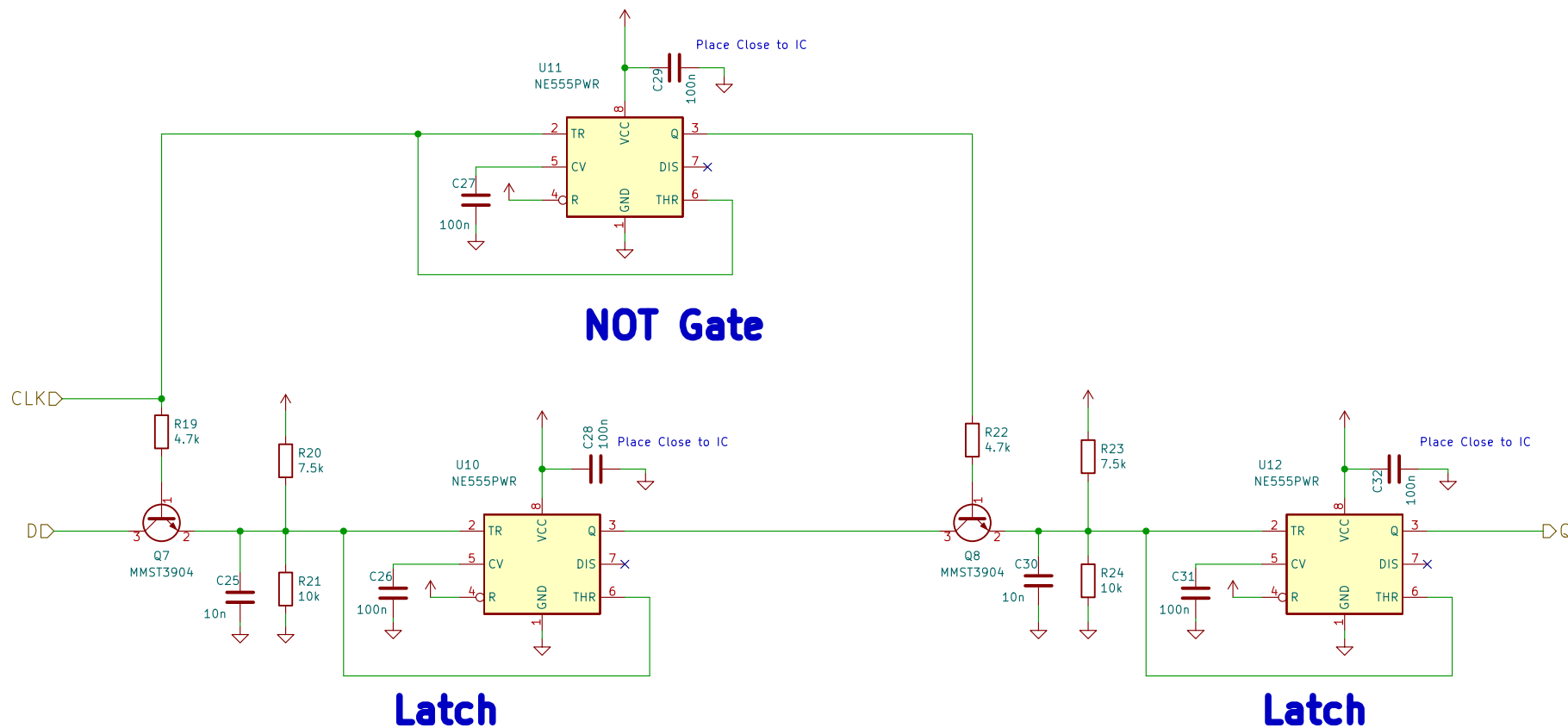
KiCad E.D.A. kicad (6.0.5)

Rev: 1

Id: 4/6

Note that this flip flop will trigger on the falling edge of CLK. Also the input D is flipped twice, hence output Q is D

# 555 FLIP FLOP



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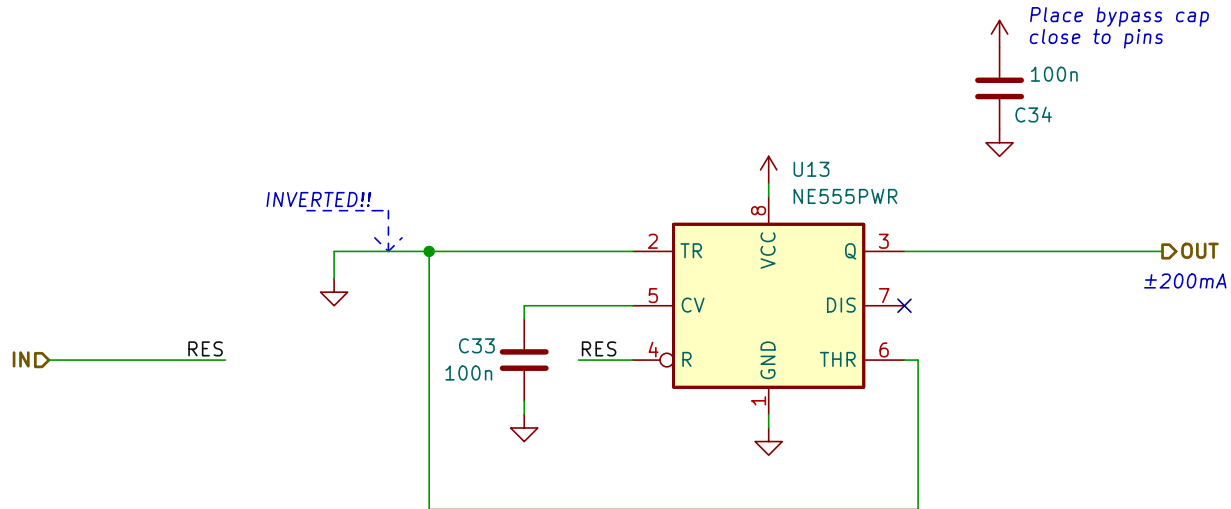
Sheet: /Flip\_Flop3/  
File: block\_555flipflop.kicad\_sch

**Title: 555 FLIP FLOP**

Size: A4 | Date: 2022-05-21  
KiCad E.D.A. kicad (6.0.5)

Rev: 1  
Id: 5/6

# 555 Buffer



Basic Buffer, note there is no hysteresis on reset... threshold is about 0.6V

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Sheet: /BUFFER/  
File: block\_555Buffer.kicad\_sch

**Title: 555 BUFFER**

Size: A5 Date: 2022-05-08

KiCad E.D.A. kicad (6.0.5)

Rev: 0

Id: 6/6