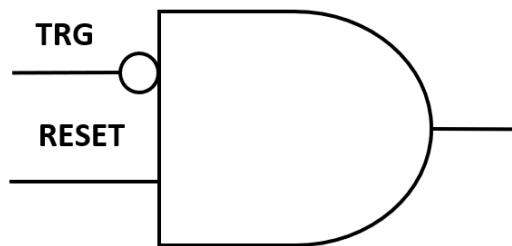
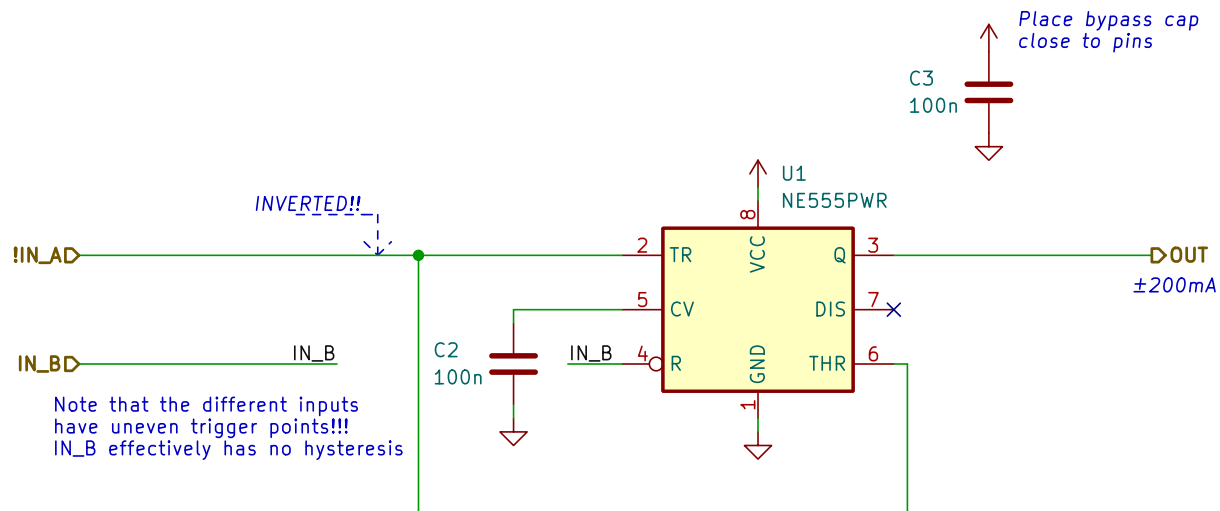


Rev: 1
Id: 1/35

555 AND Gate (inv. input)



jessefarrell92@gmail.com
AND gate based on a single 555, note inverting input

Sheet: /555_AND/
File: block_555AND.kicad_sch

Title: Single 555 AND Gate

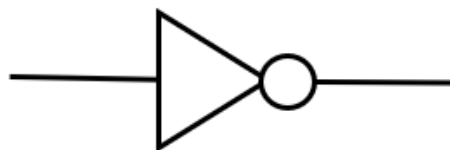
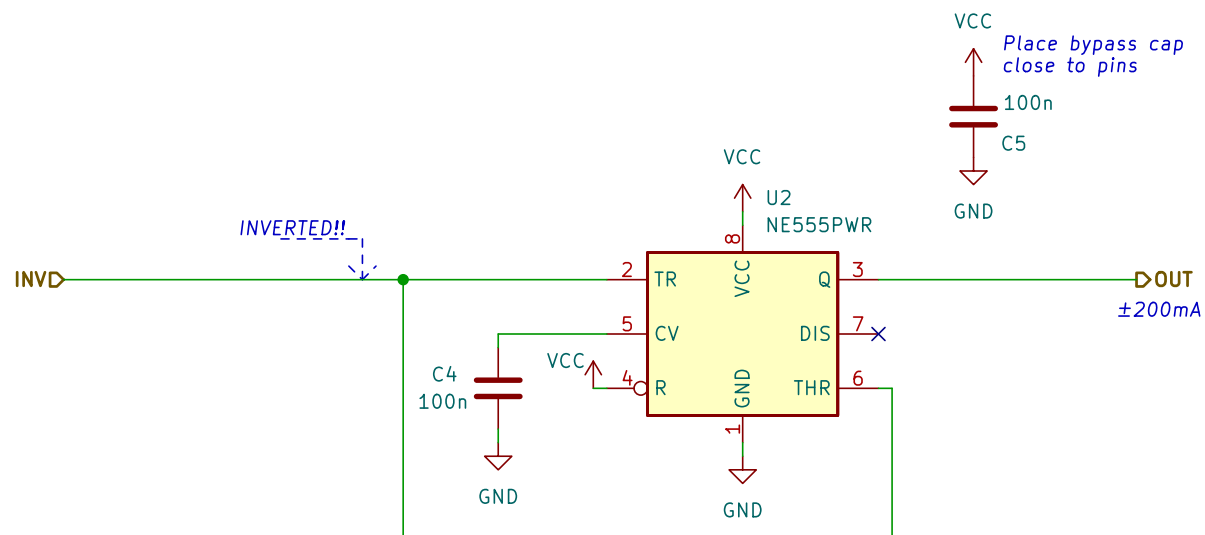
Size: A5 Date: 2022-05-04

KiCad E.D.A. kicad (6.0.5)

Rev: 0

Id: 2/35

555 NOT Gate



jessefarrell92@gmail.com
Single 555 not gate

Sheet: /NOT/
File: block_555inverter.kicad_sch

Title: 555 NOT Gate

Size: A5

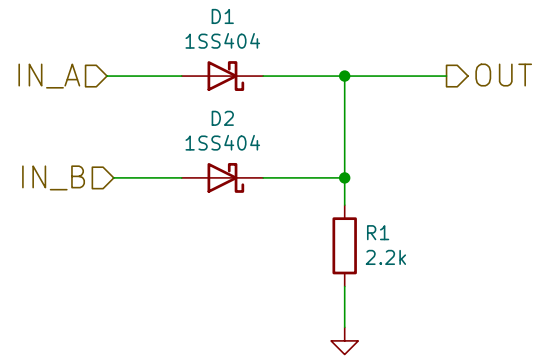
Date: 2022-05-04

Rev: 0

KiCad E.D.A. kicad (6.0.5)

Id: 3/35

Diode OR Gate



jessefarrell92@gmail.com

Sheet: /OR/
File: block_2inOR.kicad_sch

Title: Diode 2in OR Gate

Size: A5 Date: 2022-05-05

KiCad E.D.A. kicad (6.0.5)

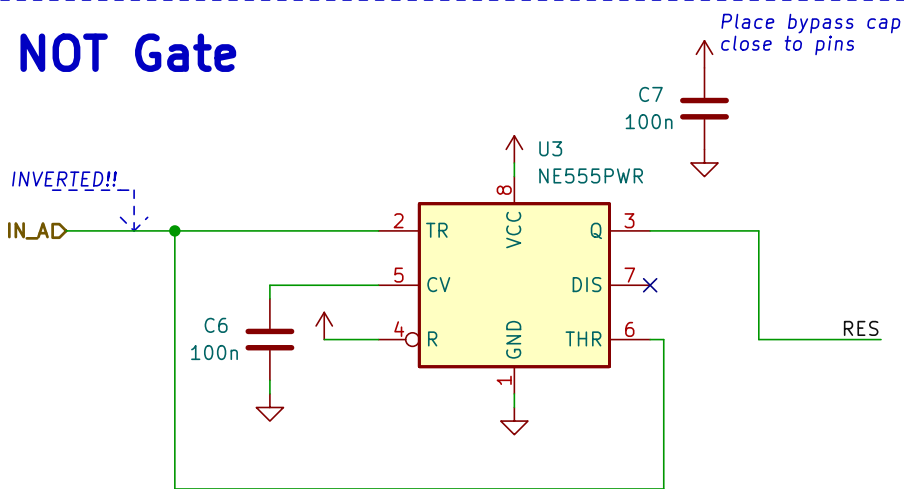
Rev: 0

Id: 4/35

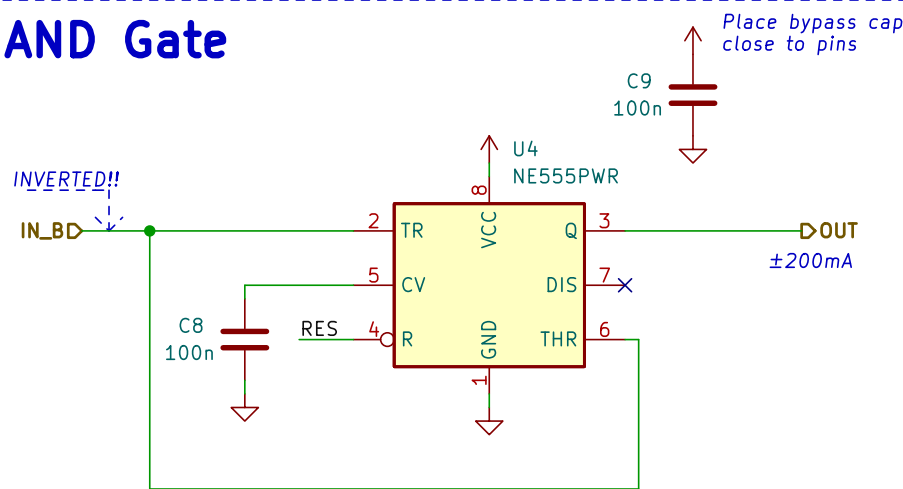
Very simple or gate
do not cascade these

555 NOR Gate

NOT Gate

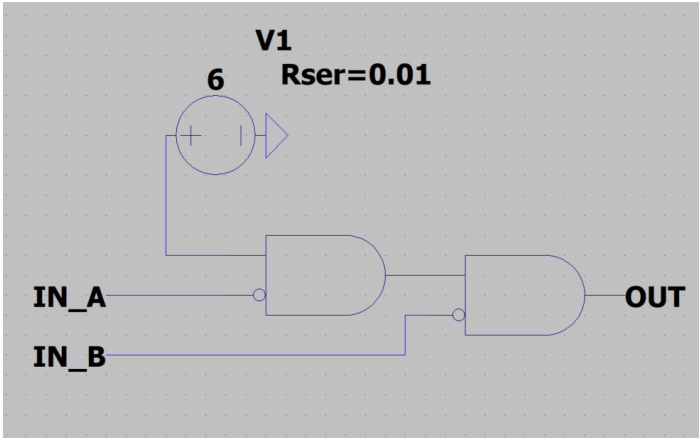


AND Gate



Truth Table

0	0	->	1
1	0	->	0
0	1	->	0
1	1	->	0



jessefarrell92@gmail.com

Sheet: /555_NOR/
File: block_555nor.kicad_sch

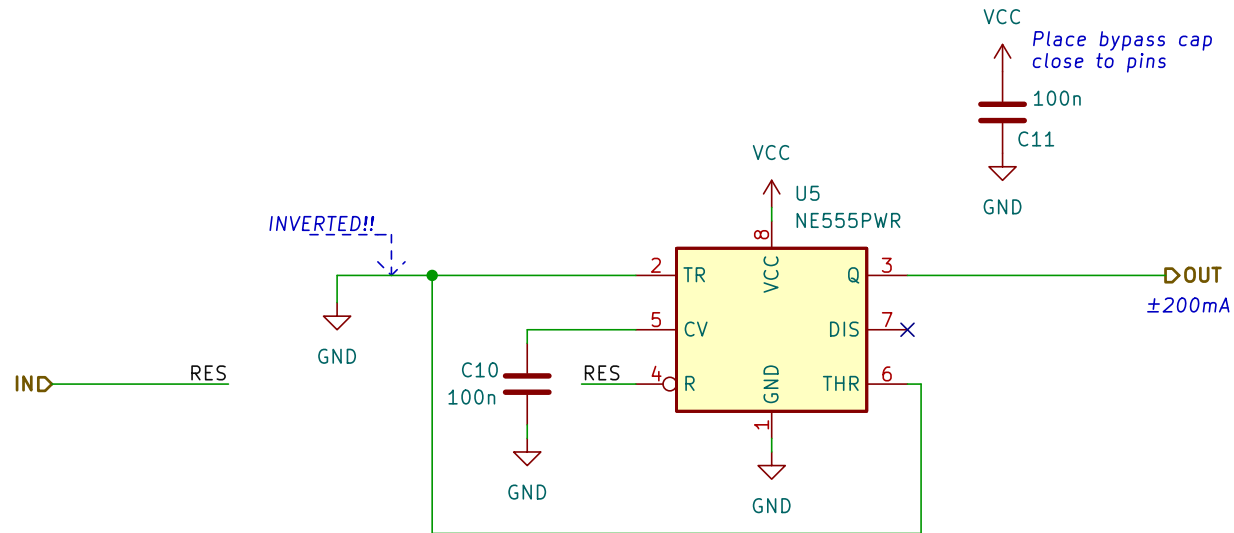
Title: 555 NOR Gate

Size: A5
KiCad E.D.A. kicad (6.0.5)

Date: 2022-05-05

Rev: o
Id: 5/35

555 Buffer



Basic Buffer, note there
is no hysteresis on reset...
threshold is about 0.6V

jessefarrell92@gmail.com

Sheet: /BUFFER/
File: block_555Buffer.kicad_sch

Title: 555 BUFFER

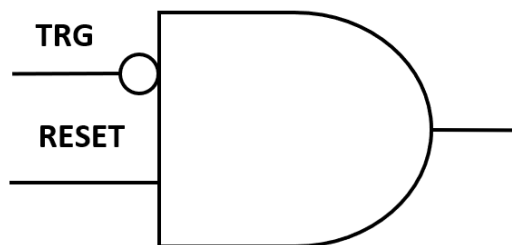
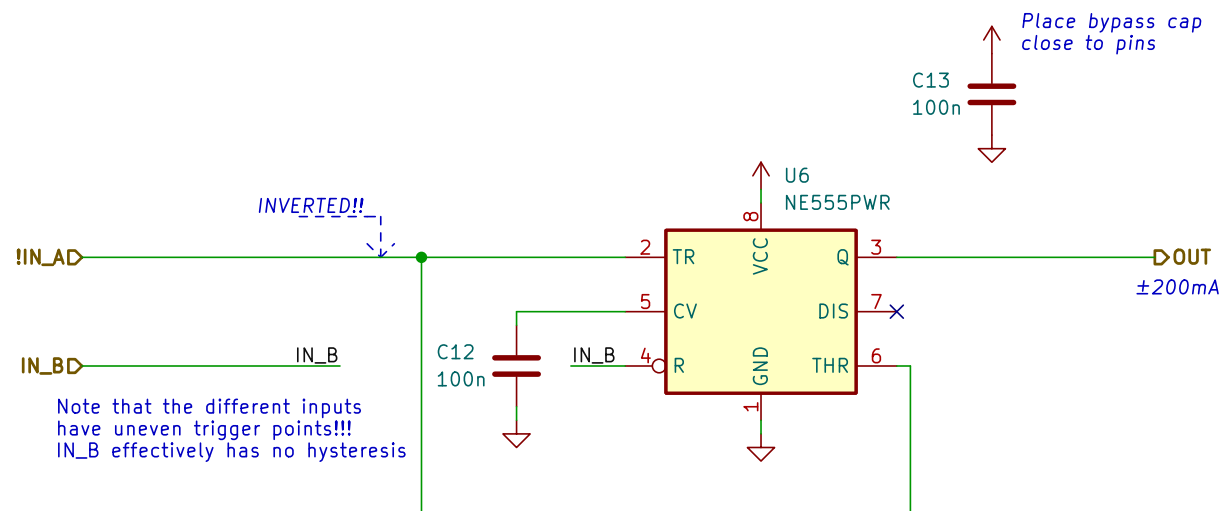
Size: A5 Date: 2022-05-08

Rev: 0

KiCad E.D.A. kicad (6.0.5)

Id: 6/35

555 AND Gate (inv. input)



jessefarrell92@gmail.com
AND gate based on a single 555, note inverting input

Sheet: /555_AND1/
File: block_555AND.kicad_sch

Title: Single 555 AND Gate

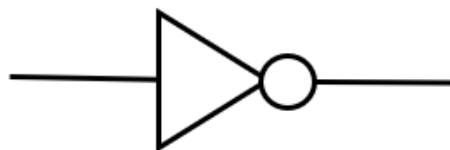
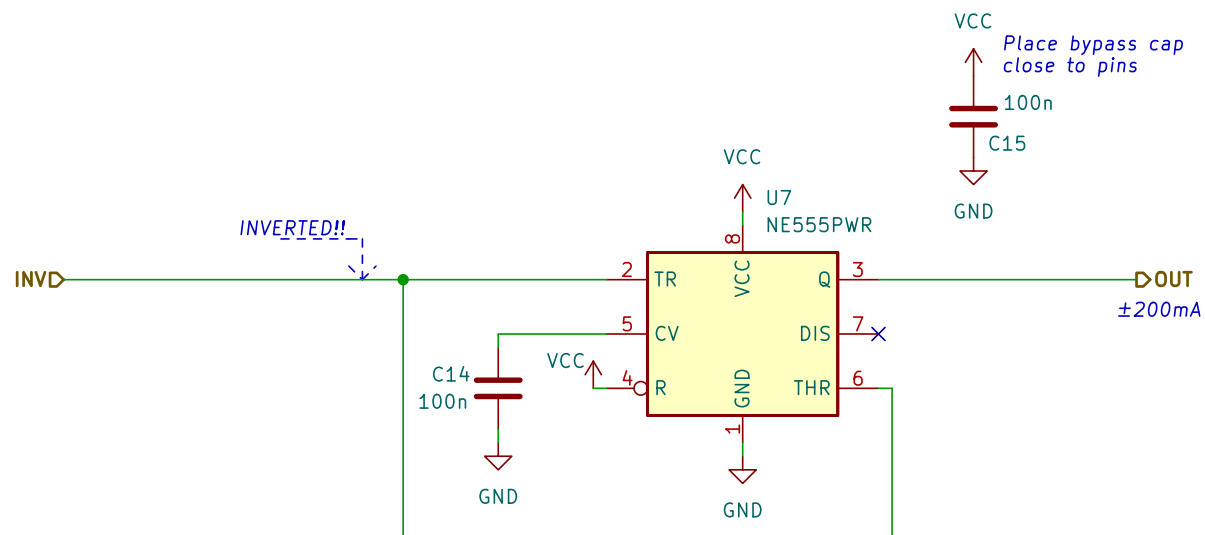
Size: A5 Date: 2022-05-04

KiCad E.D.A. kicad (6.0.5)

Rev: 0

Id: 7/35

555 NOT Gate



jessefarrell92@gmail.com
Single 555 not gate

Sheet: /NOT1/
File: block_555inverter.kicad_sch

Title: 555 NOT Gate

Size: A5

Date: 2022-05-04

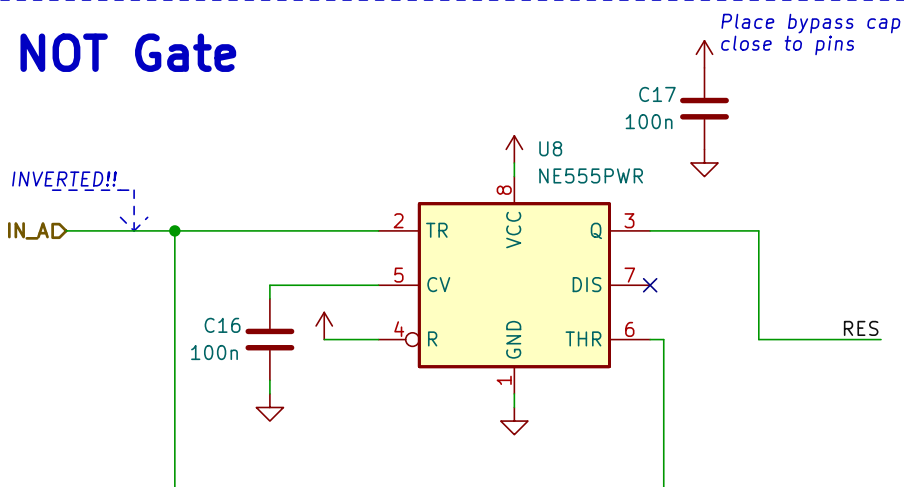
Rev: 0

KiCad E.D.A. kicad (6.0.5)

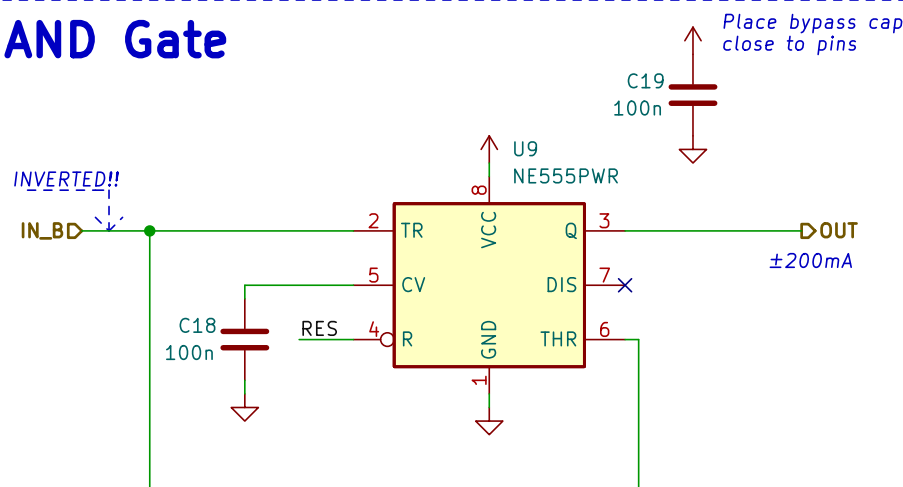
Id: 8/35

555 NOR Gate

NOT Gate

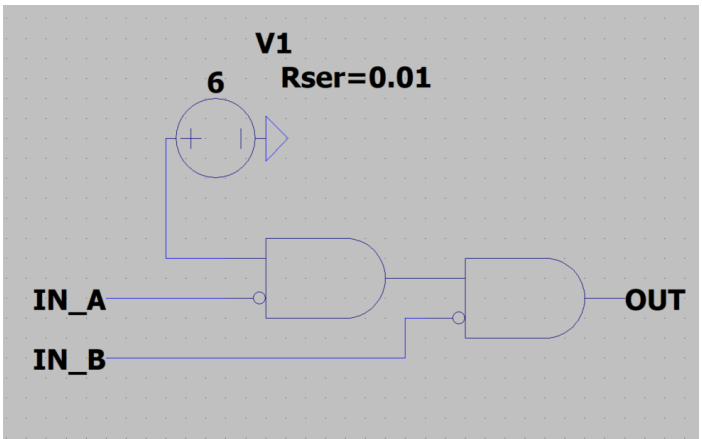


AND Gate



Truth Table

0	0	->	1
1	0	->	0
0	1	->	0
1	1	->	0



jessefarrell92@gmail.com

Sheet: /555_NOR1/
File: block_555nor.kicad_sch

Title: 555 NOR Gate

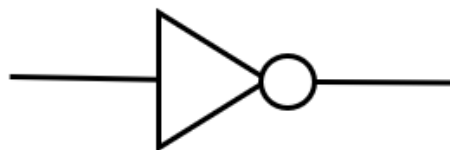
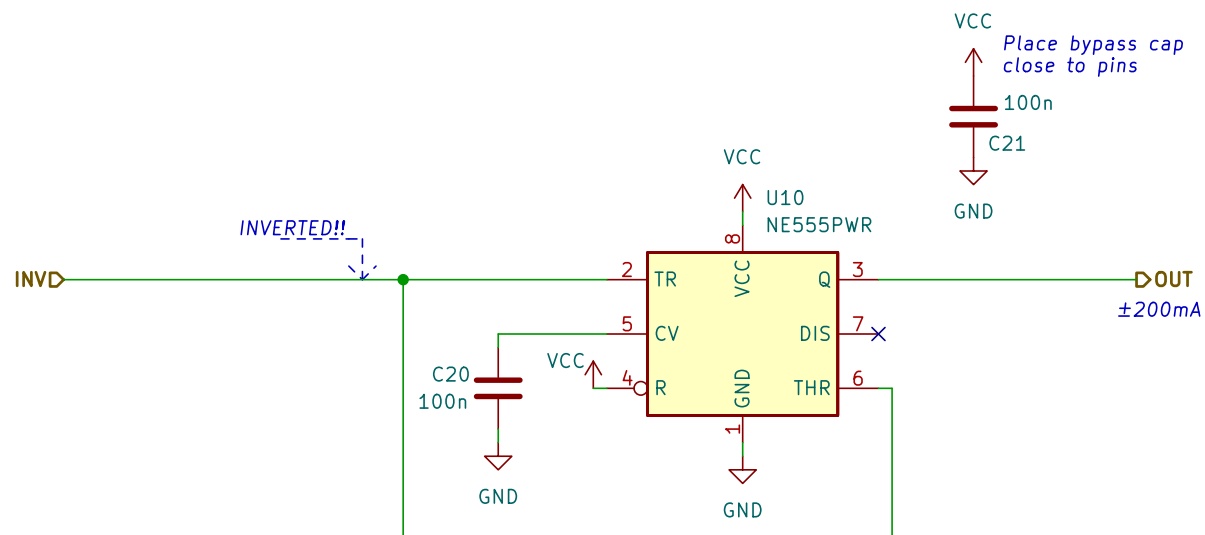
Size: A5 Date: 2022-05-05

KiCad E.D.A. kicad (6.0.5)

Rev: o

Id: 9/35

555 NOT Gate



jessefarrell92@gmail.com
Single 555 not gate

Sheet: /NOT2/
File: block_555inverter.kicad_sch

Title: 555 NOT Gate

Size: A5 Date: 2022-05-04

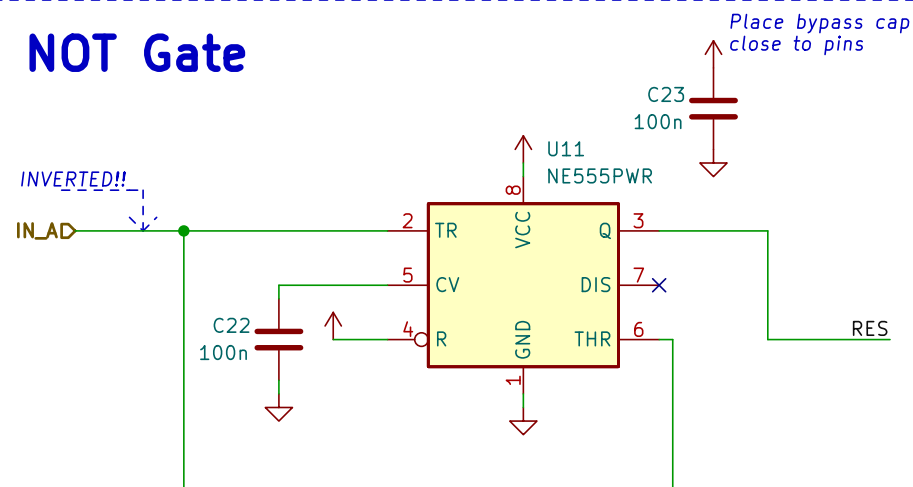
KiCad E.D.A. kicad (6.0.5)

Rev: 0

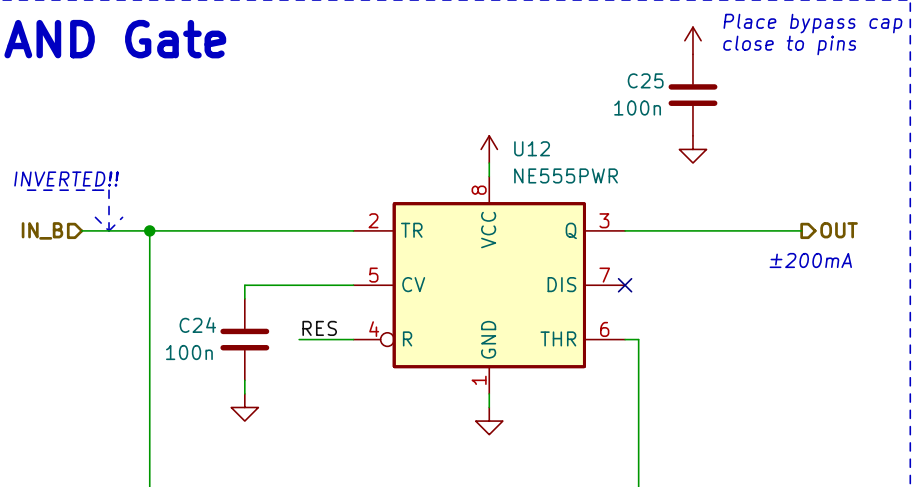
Id: 10/35

555 NOR Gate

NOT Gate

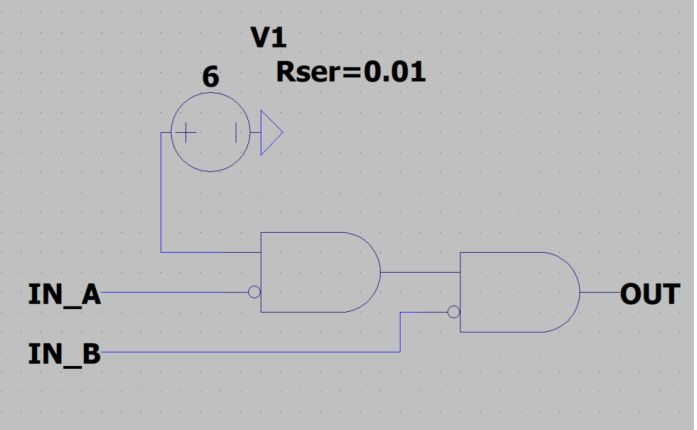


AND Gate



Truth Table

0	0	->	1
1	0	->	0
0	1	->	0
1	1	->	0



jessefarrell92@gmail.com

Sheet: /555_NOR2/
File: block_555nor.kicad_sch

Title: 555 NOR Gate

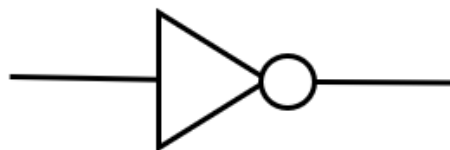
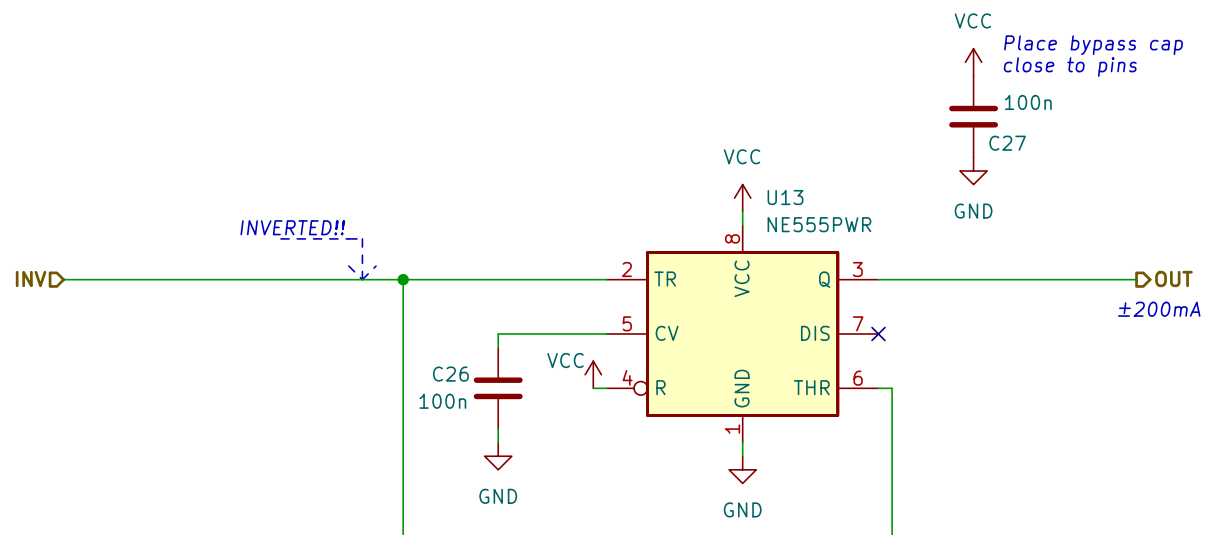
Size: A5 Date: 2022-05-05

Rev: o

KiCad E.D.A. kicad (6.0.5)

Id: 11/35

555 NOT Gate



jessefarrell92@gmail.com
Single 555 not gate

Sheet: /NOT3/
File: block_555inverter.kicad_sch

Title: 555 NOT Gate

Size: A5

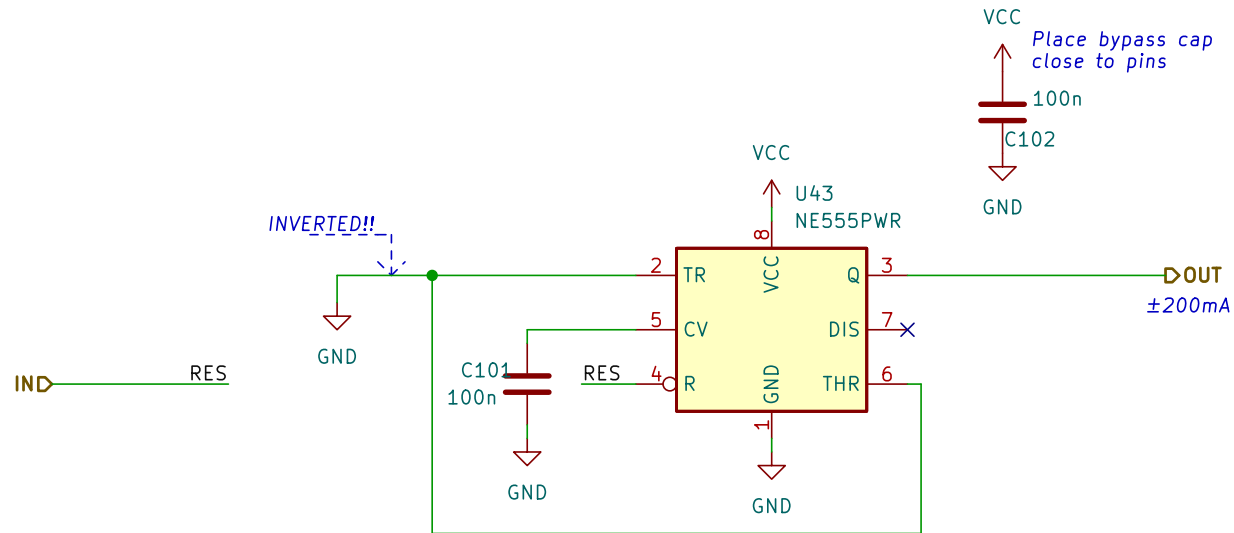
Date: 2022-05-04

Rev: 0

KiCad E.D.A. kicad (6.0.5)

Id: 12/35

555 Buffer



Basic Buffer, note there
is no hysteresis on reset...
threshold is about 0.6V

jessefarrell92@gmail.com

Sheet: /BUFFER3/

File: block_555Buffer.kicad_sch

Title: 555 BUFFER

Size: A5

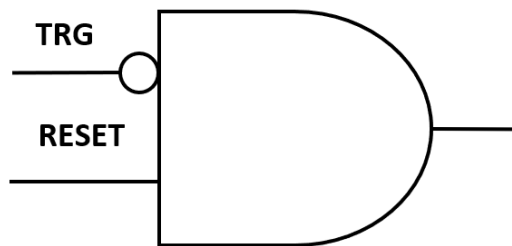
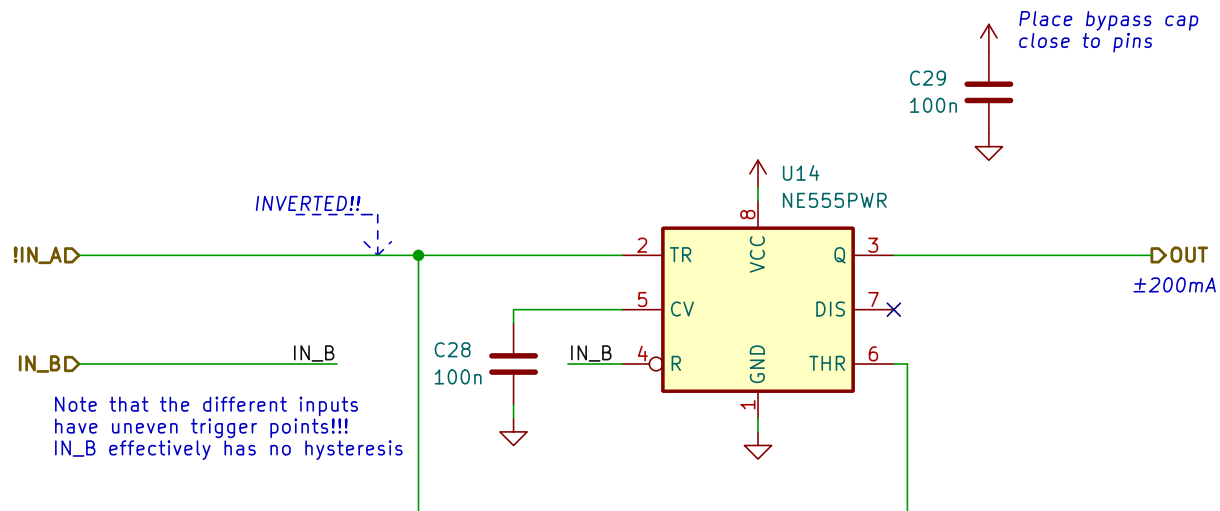
Date: 2022-05-08

Rev: 0

KiCad E.D.A. kicad (6.0.5)

Id: 13/35

555 AND Gate (inv. input)



jessefarrell92@gmail.com
AND gate based on a single 555, note inverting input

Sheet: /555_AND2/
File: block_555AND.kicad_sch

Title: Single 555 AND Gate

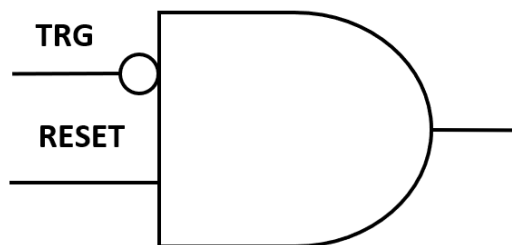
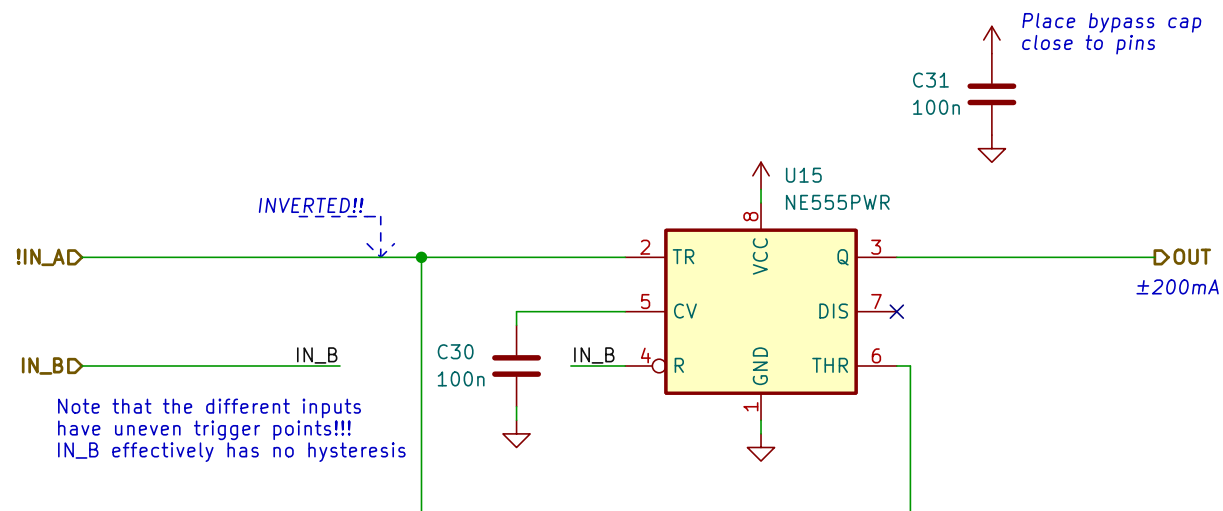
Size: A5 Date: 2022-05-04

KiCad E.D.A. kicad (6.0.5)

Rev: 0

Id: 14/35

555 AND Gate (inv. input)



jessefarrell92@gmail.com
AND gate based on a single 555, note inverting input

Sheet: /555_AND3/
File: block_555AND.kicad_sch

Title: Single 555 AND Gate

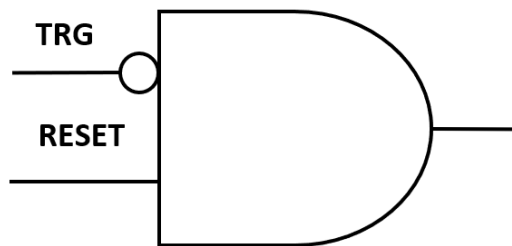
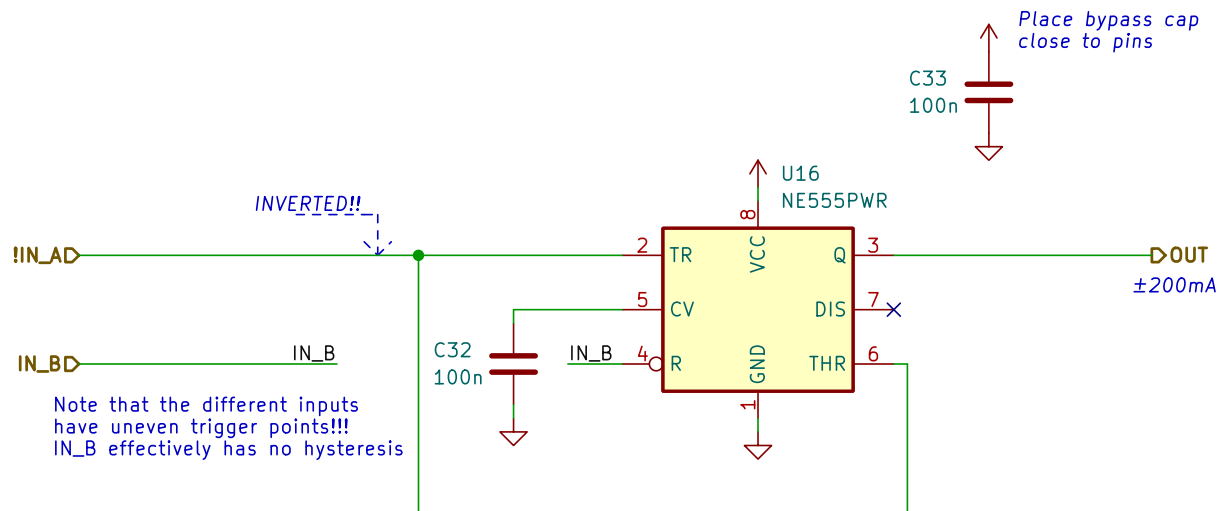
Size: A5 Date: 2022-05-04

KiCad E.D.A. kicad (6.0.5)

Rev: 0

Id: 15/35

555 AND Gate (inv. input)



jessefarrell92@gmail.com
AND gate based on a single 555, note inverting input

Sheet: /555_AND4/
File: block_555AND.kicad_sch

Title: Single 555 AND Gate

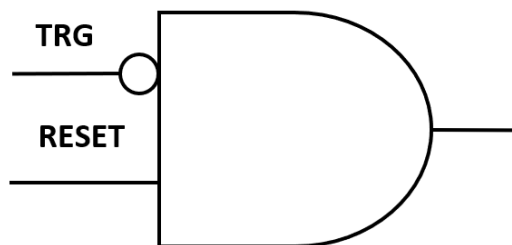
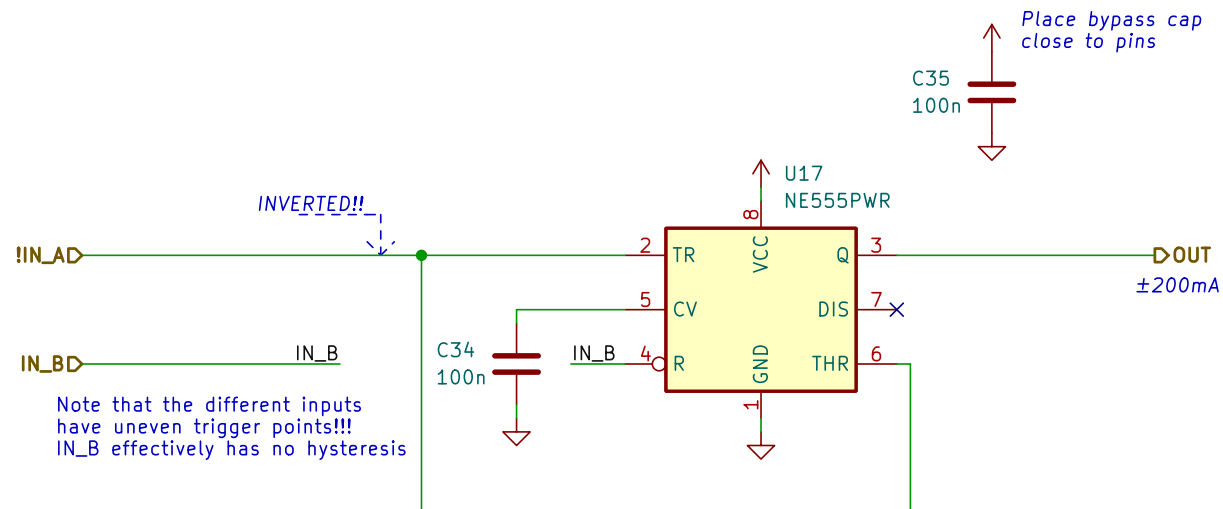
Size: A5 Date: 2022-05-04

Rev: 0

KiCad E.D.A. kicad (6.0.5)

Id: 16/35

555 AND Gate (inv. input)



jessefarrell92@gmail.com
AND gate based on a single 555, note inverting input

Sheet: /555_AND5/
File: block_555AND.kicad_sch

Title: Single 555 AND Gate

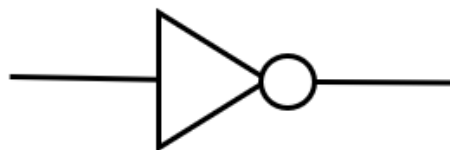
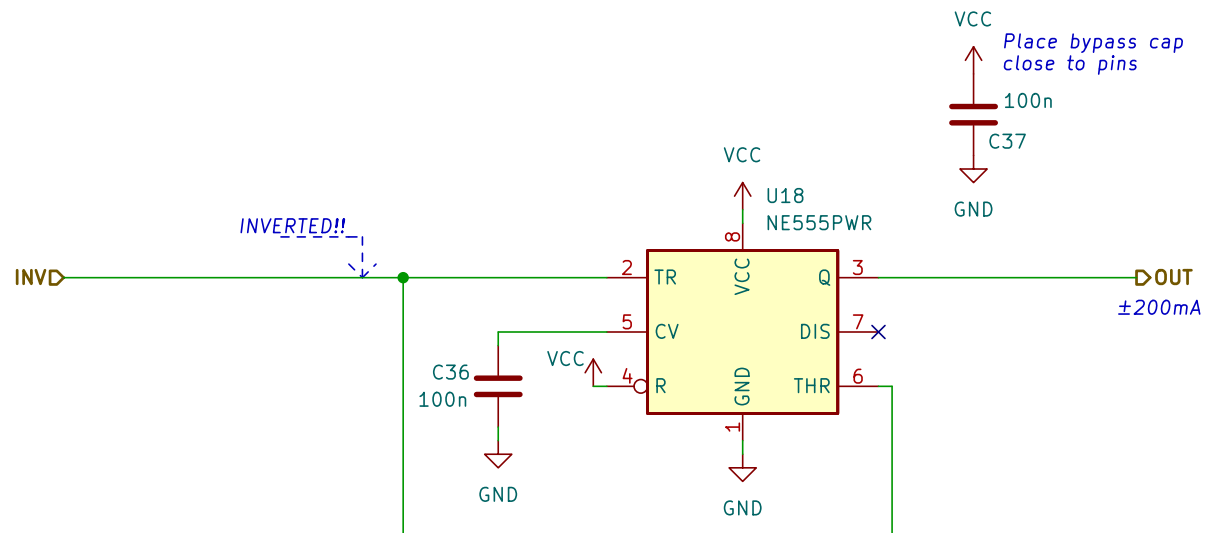
Size: A5 Date: 2022-05-04

Rev: 0

KiCad E.D.A. kicad (6.0.5)

Id: 17/35

555 NOT Gate



jessefarrell92@gmail.com
Single 555 not gate

Sheet: /NOT4/
File: block_555inverter.kicad_sch

Title: 555 NOT Gate

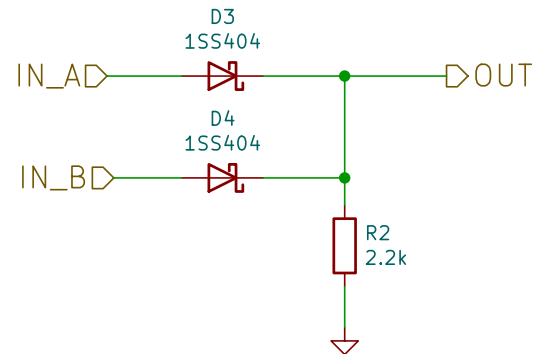
Size: A5 Date: 2022-05-04

KiCad E.D.A. kicad (6.0.5)

Rev: 0

Id: 18/35

Diode OR Gate



jessefarrell92@gmail.com

Sheet: /OR1/
File: block_2inOR.kicad_sch

Title: Diode 2in OR Gate

Size: A5 Date: 2022-05-05

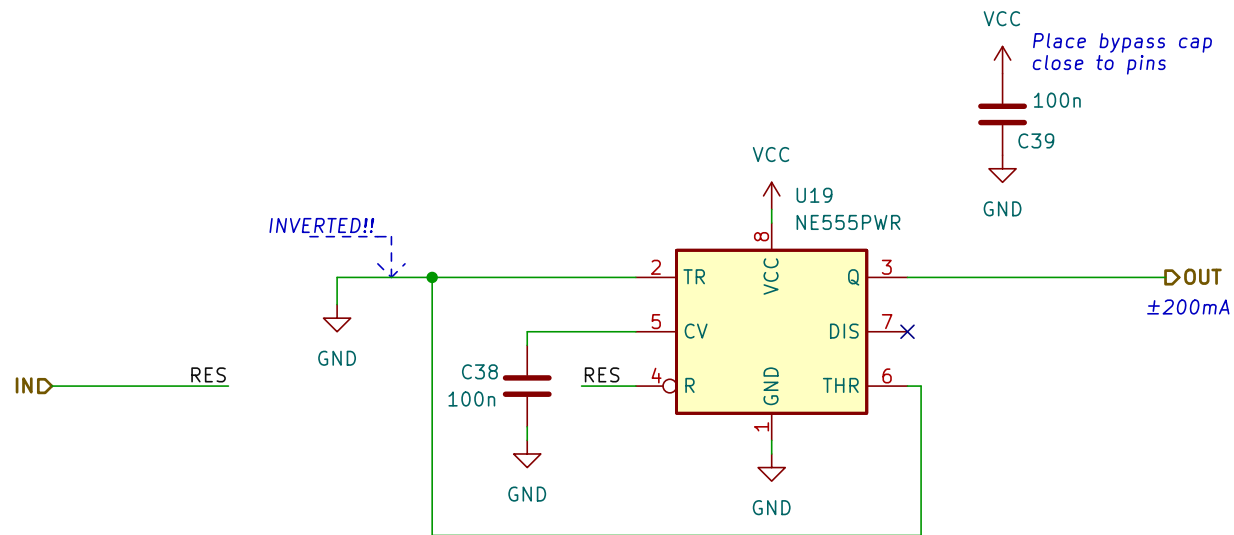
Rev: 0

KiCad E.D.A. kicad (6.0.5)

Id: 19/35

Very simple or gate
do not cascade these

555 Buffer



Basic Buffer, note there is no hysteresis on reset... threshold is about 0.6V

jessefarrell92@gmail.com

Sheet: /BUFFER1/

File: block_555Buffer.kicad_sch

Title: 555 BUFFER

Size: A5

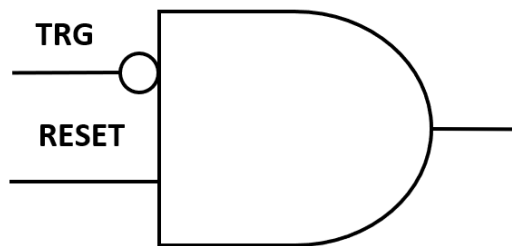
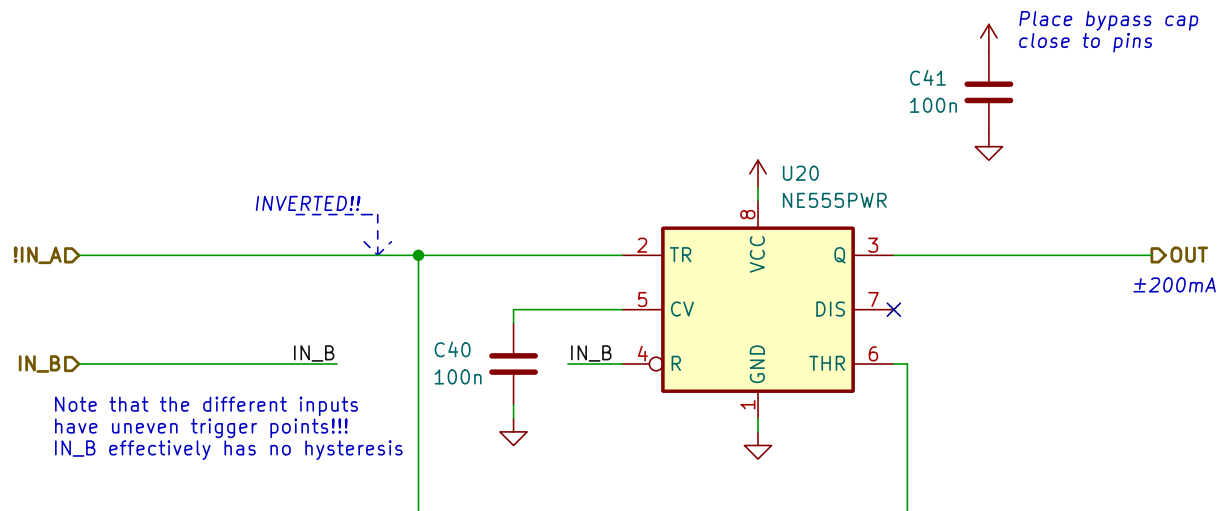
Date: 2022-05-08

Rev: 0

KiCad E.D.A. kicad (6.0.5)

Id: 20/35

555 AND Gate (inv. input)



jessefarrell92@gmail.com
AND gate based on a single 555, note inverting input

Sheet: /555_AND7/
File: block_555AND.kicad_sch

Title: Single 555 AND Gate

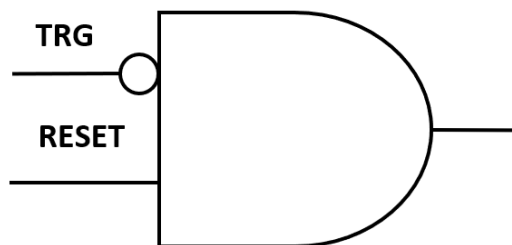
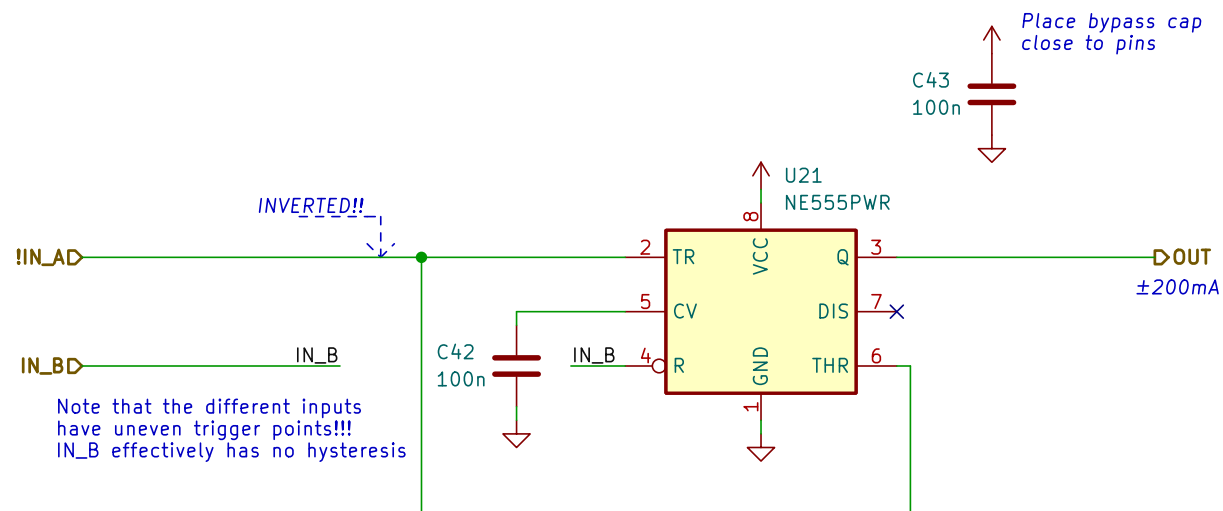
Size: A5 Date: 2022-05-04

Rev: 0

KiCad E.D.A. kicad (6.0.5)

Id: 21/35

555 AND Gate (inv. input)



jessefarrell92@gmail.com
AND gate based on a single 555, note inverting input

Sheet: /555_AND6/
File: block_555AND.kicad_sch

Title: Single 555 AND Gate

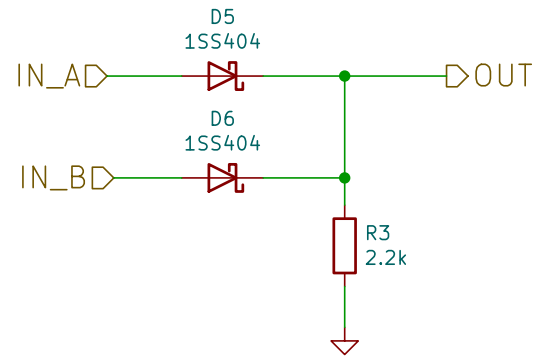
Size: A5 Date: 2022-05-04

Rev: 0

KiCad E.D.A. kicad (6.0.5)

Id: 22/35

Diode OR Gate



jessefarrell92@gmail.com

Sheet: /OR2/
File: block_2inOR.kicad_sch

Title: Diode 2in OR Gate

Size: A5 Date: 2022-05-05

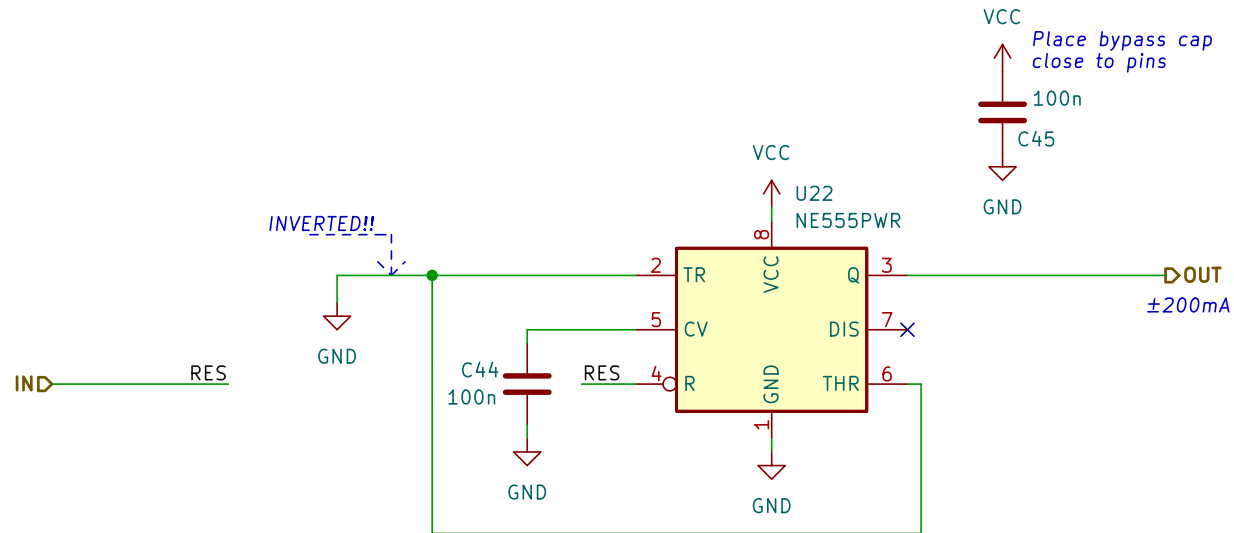
Rev: 0

KiCad E.D.A. kicad (6.0.5)

Id: 24/35

Very simple or gate
do not cascade these

555 Buffer



Basic Buffer, note there
is no hysteresis on reset...
threshold is about 0.6V

jessefarrell92@gmail.com

Sheet: /BUFFER2/

File: block_555Buffer.kicad_sch

Title: 555 BUFFER

Size: A5

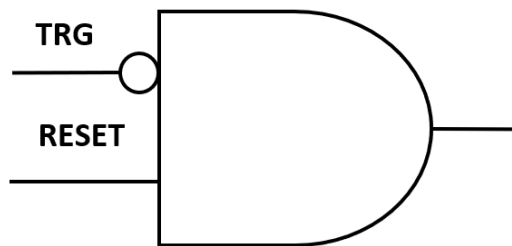
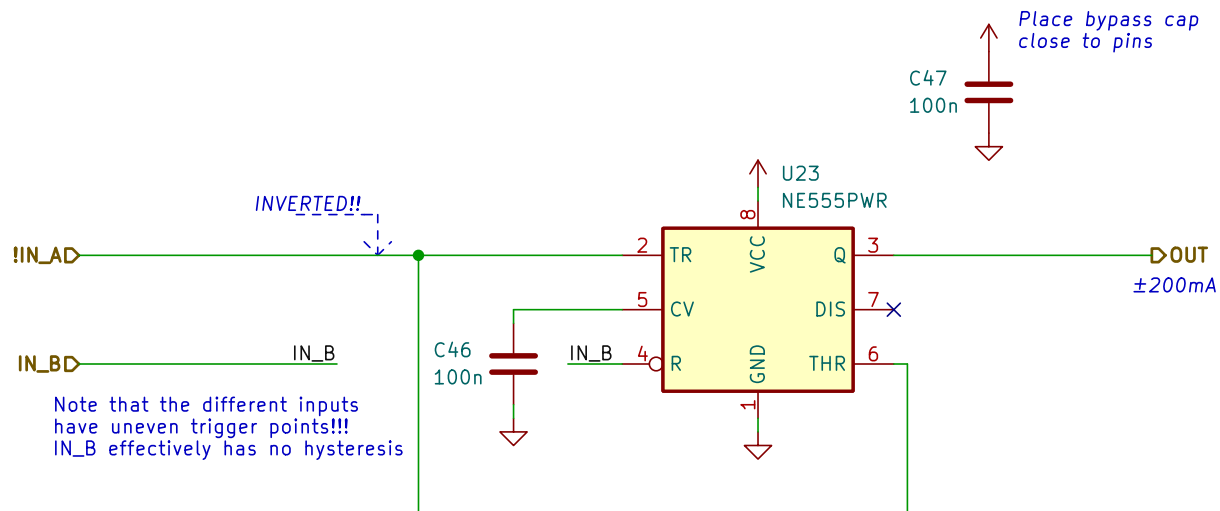
Date: 2022-05-08

Rev: 0

KiCad E.D.A. kicad (6.0.5)

Id: 25/35

555 AND Gate (inv. input)



jessefarrell92@gmail.com
AND gate based on a single 555, note inverting input

Sheet: /555_AND8/
File: block_555AND.kicad_sch

Title: Single 555 AND Gate

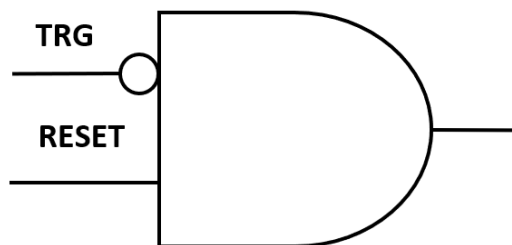
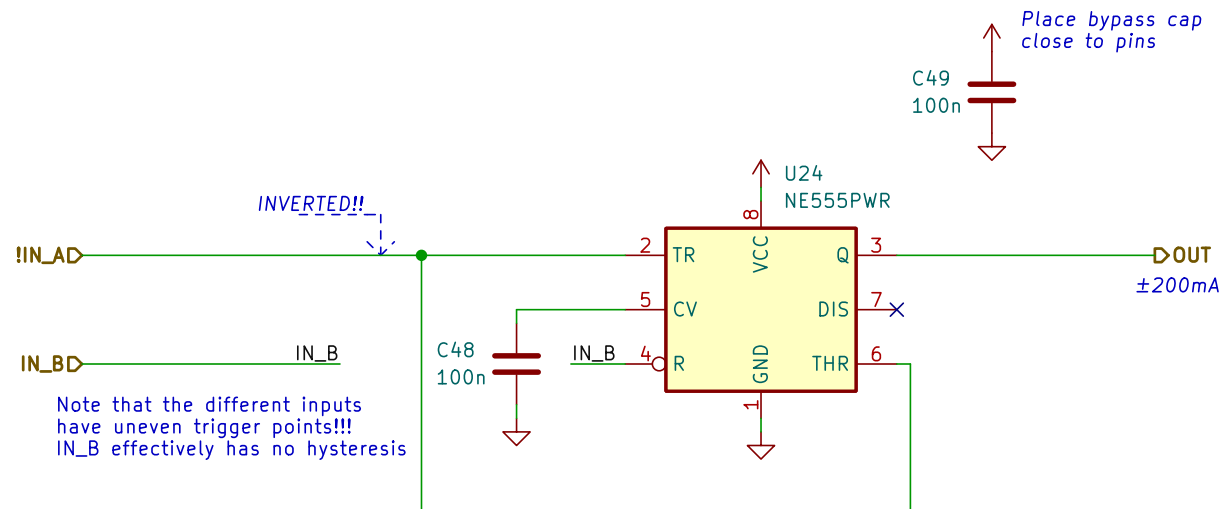
Size: A5 Date: 2022-05-04

KiCad E.D.A. kicad (6.0.5)

Rev: 0

Id: 26/35

555 AND Gate (inv. input)



jessefarrell92@gmail.com
AND gate based on a single 555, note inverting input

Sheet: /555_AND9/
File: block_555AND.kicad_sch

Title: Single 555 AND Gate

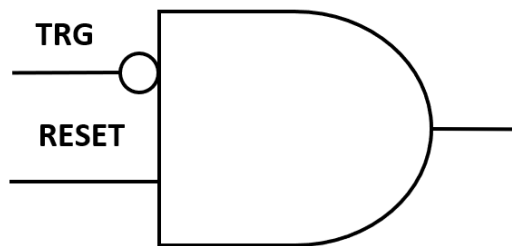
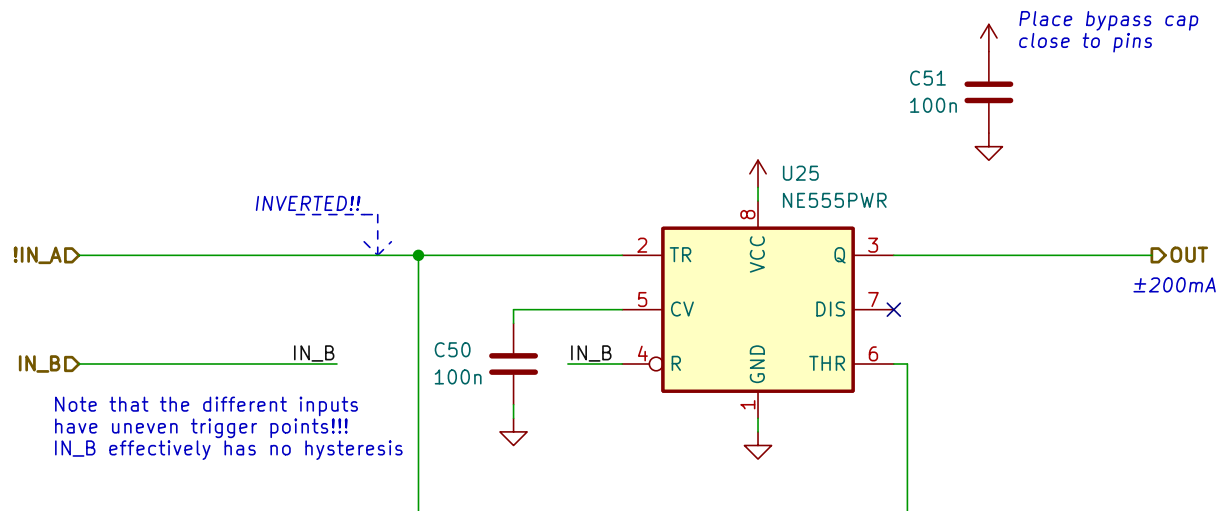
Size: A5 Date: 2022-05-04

KiCad E.D.A. kicad (6.0.5)

Rev: 0

Id: 27/35

555 AND Gate (inv. input)



jessefarrell92@gmail.com
AND gate based on a single 555, note inverting input

Sheet: /555_AND10/
File: block_555AND.kicad_sch

Title: Single 555 AND Gate

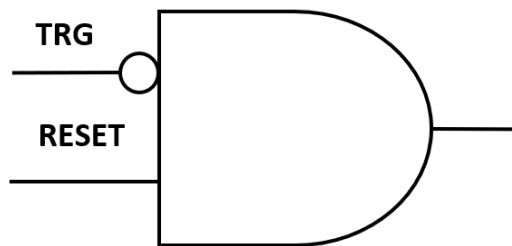
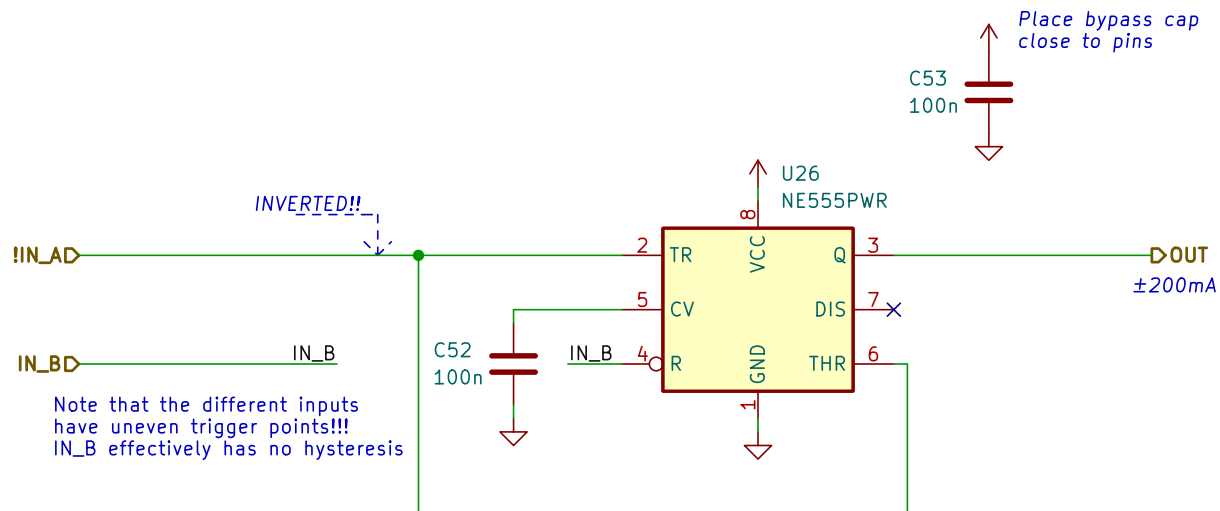
Size: A5 Date: 2022-05-04

KiCad E.D.A. kicad (6.0.5)

Rev: 0

Id: 29/35

555 AND Gate (inv. input)



jessefarrell92@gmail.com
AND gate based on a single 555, note inverting input

Sheet: /555_AND11/
File: block_555AND.kicad_sch

Title: Single 555 AND Gate

Size: A5 Date: 2022-05-04

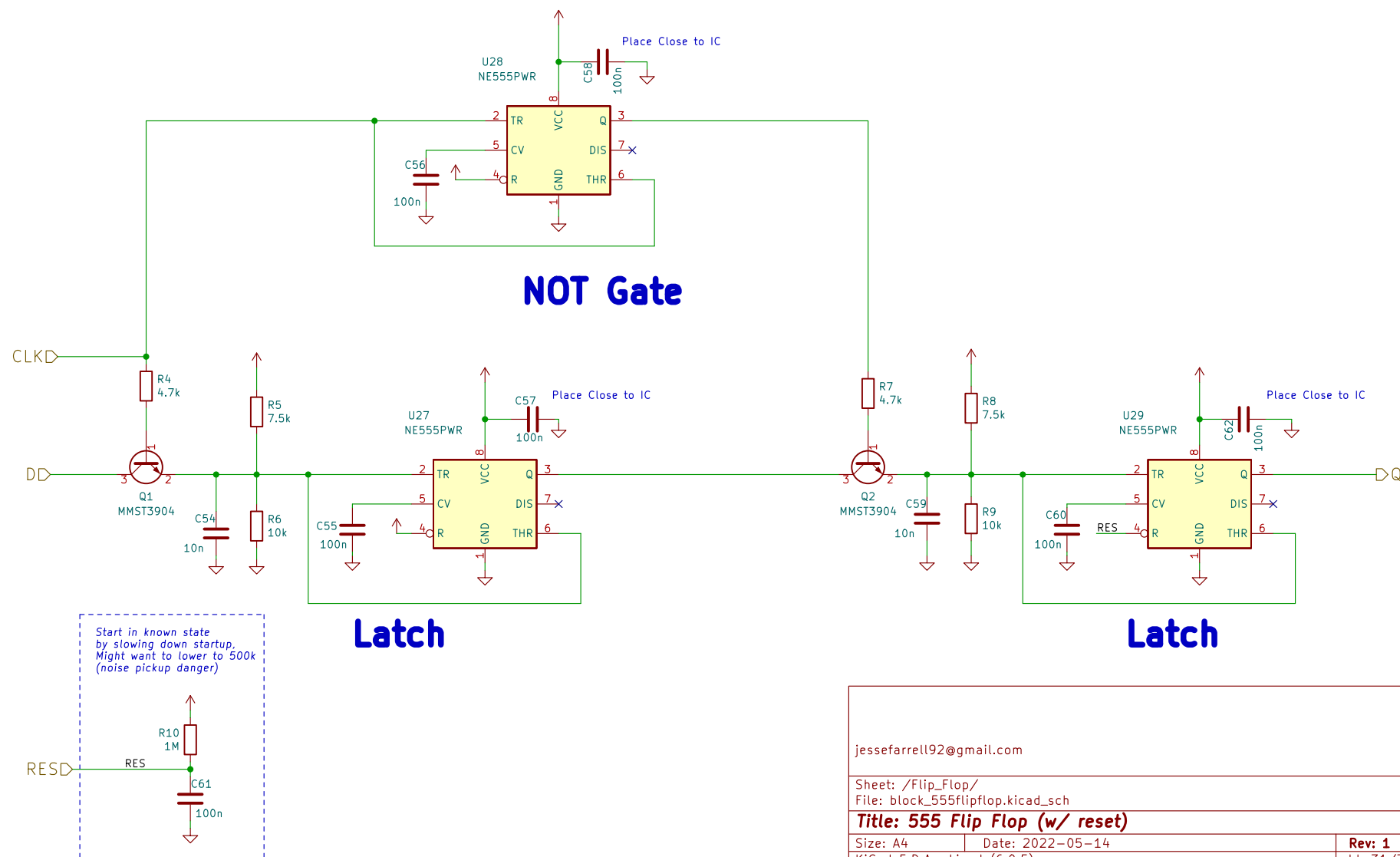
Rev: 0

KiCad E.D.A. kicad (6.0.5)

Id: 30/35

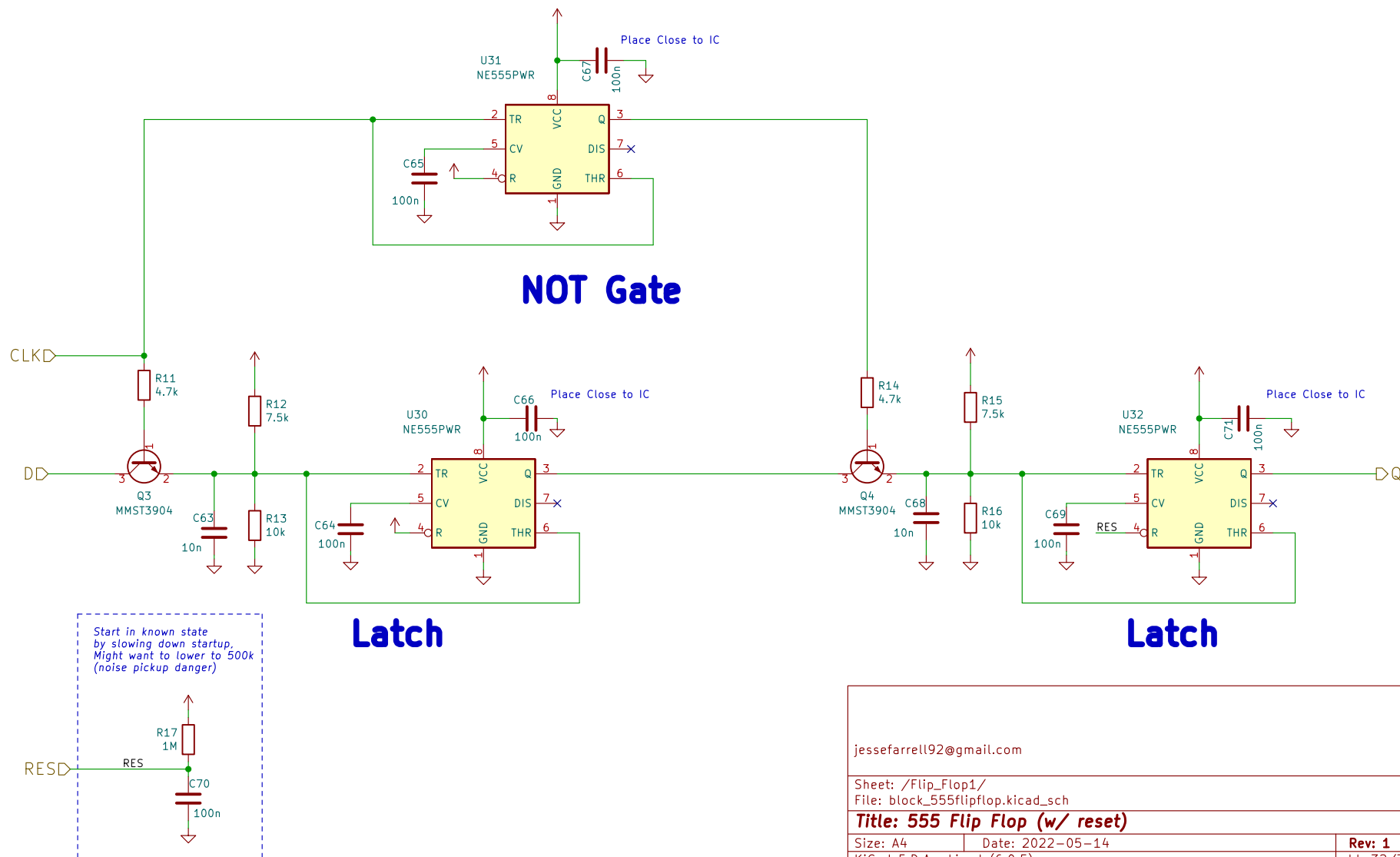
Note that this flip flop will trigger on the falling edge of CLK. Also the input D is flipped twice, hence output Q is D

555 FLIP FLOP



Note that this flip flop will trigger on the falling edge of CLK. Also the input D is flipped twice, hence output Q is D

555 FLIP FLOP



jessefarrell92@gmail.com

Sheet: /Flip_Flop1/
File: block_555flipflop.kicad_sch

Title: 555 Flip Flop (w/ reset)

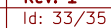
Size: A4 Date: 2022-05-14

KiCad E.D.A. kicad (6.0.5)

Rev: 1

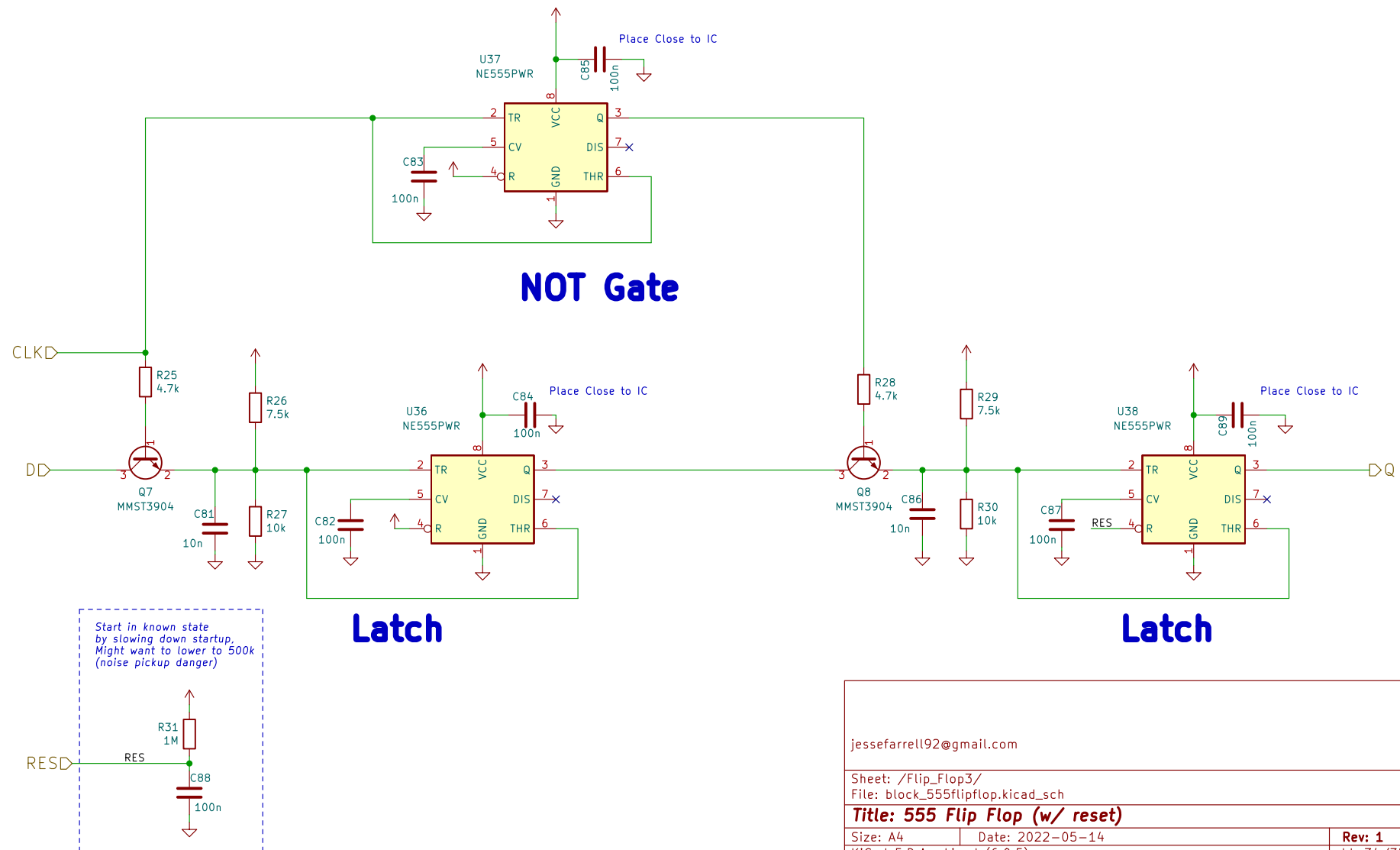
Id: 32/35

555 FLIP FLOP



Note that this flip flop will trigger on the falling edge of CLK. Also the input D is flipped twice, hence output Q is D

555 FLIP FLOP



jessefarrell92@gmail.com

Sheet: /Flip_Flop3/
File: block_555flipflop.kicad_sch

Title: 555 Flip Flop (w/ reset)

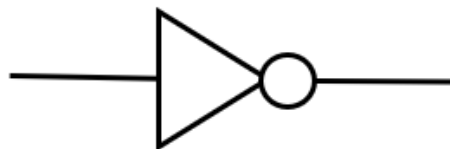
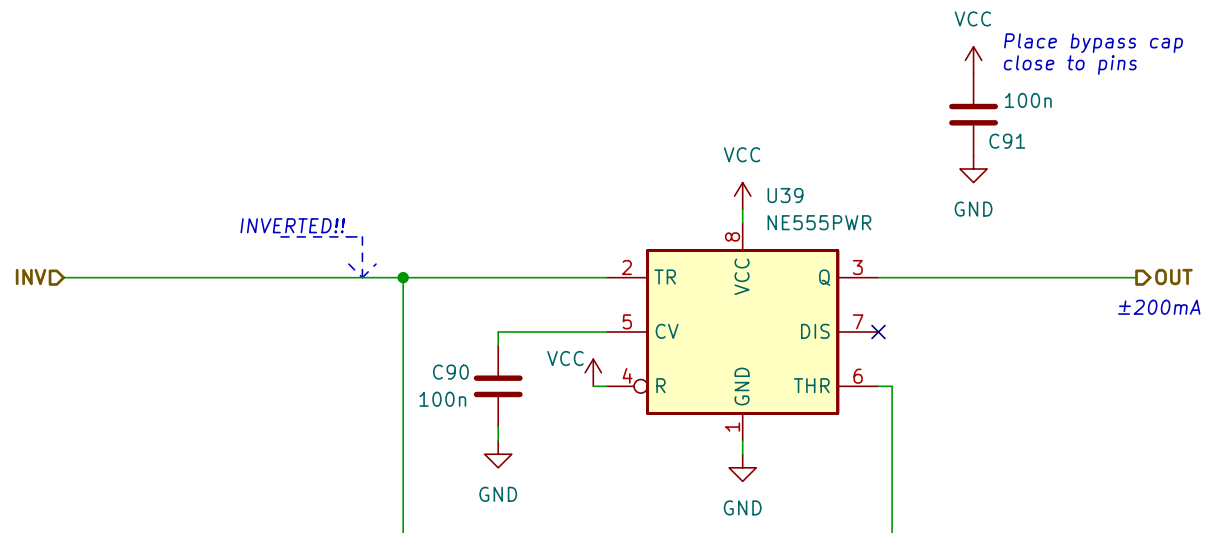
Size: A4 Date: 2022-05-14

KiCad E.D.A. kicad (6.0.5)

Rev: 1

Id: 34/35

555 NOT Gate



jessefarrell92@gmail.com
Single 555 not gate

Sheet: /NOT7/
File: block_555inverter.kicad_sch

Title: 555 NOT Gate

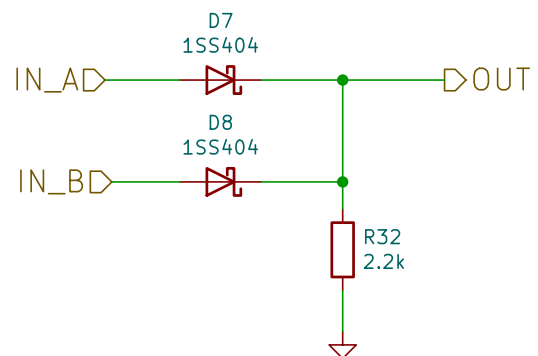
Size: A5 Date: 2022-05-04

KiCad E.D.A. kicad (6.0.5)

Rev: 0

Id: 35/35

Diode OR Gate



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Sheet: /Diode_OR/
File: block_2inOR.kicad_sch

Title: Diode 2in OR Gate

Size: A5 Date: 2022-05-05

Rev: 0

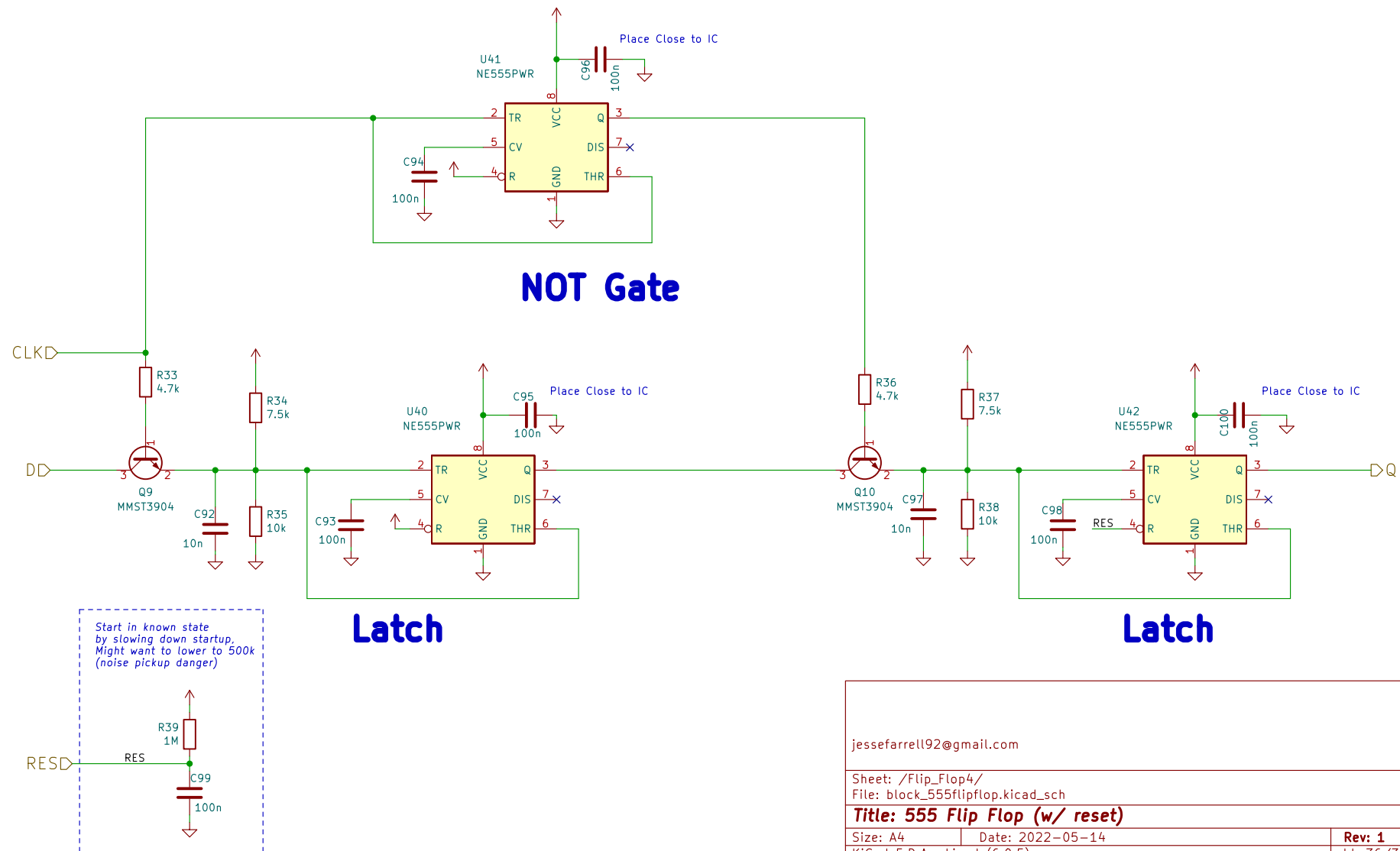
KiCad E.D.A. kicad (6.0.5)

Id: 36/35

Very simple or gate
do not cascade these

Note that this flip flop will trigger on the falling edge of CLK. Also the input D is flipped twice, hence output Q is D

555 FLIP FLOP



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Sheet: /Flip_Flop4/
File: block_555flipflop.kicad_sch

Title: 555 Flip Flop (w/ reset)

Size: A4 Date: 2022-05-14

KiCad E.D.A. kicad (6.0.5)

Rev: 1

Id: 36/35