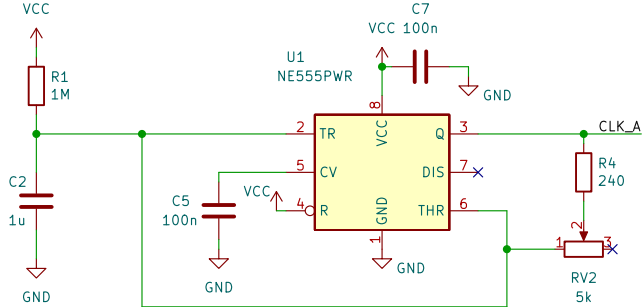
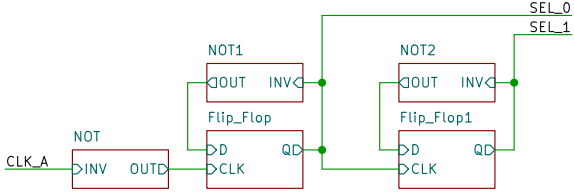


555 Clock Sources

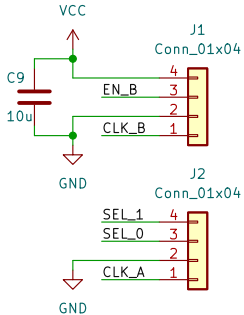
CLOCK A – 7Segment Refresh



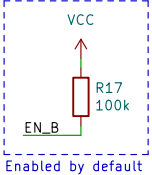
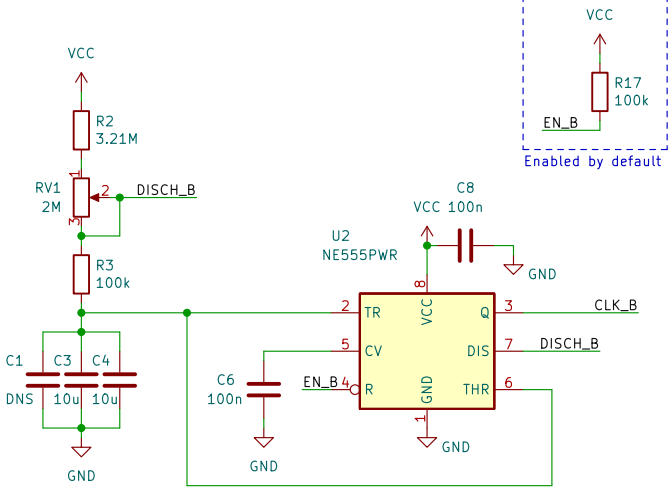
2Bit Counter / Selector



Connector



CLOCK B – Main Timer Clock (1sec ticks)



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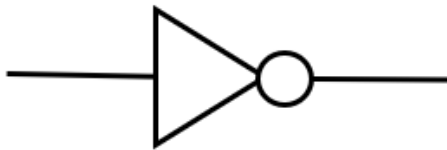
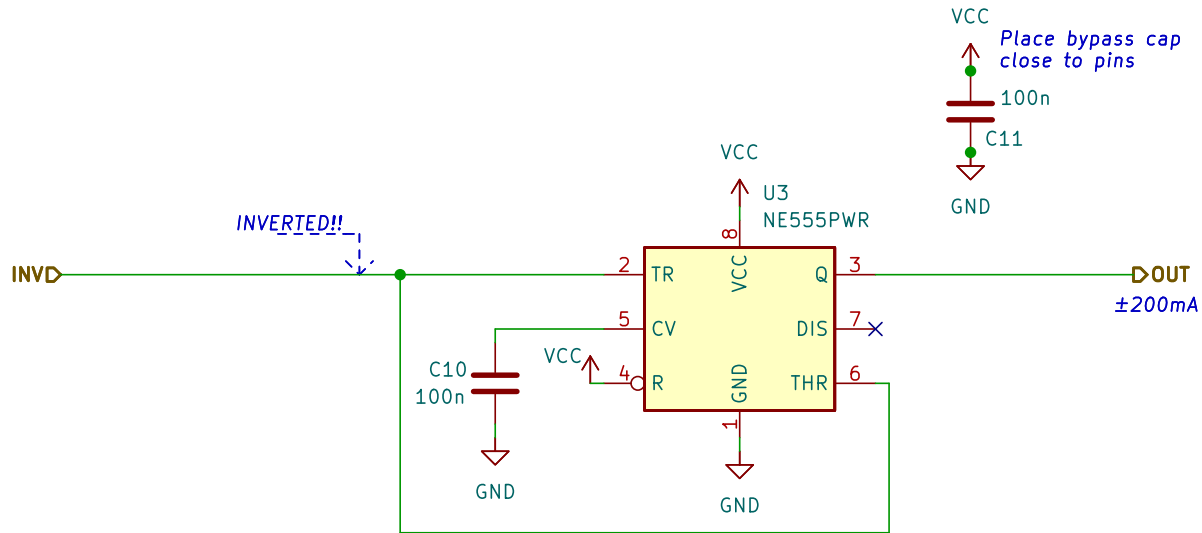
Sheet: /
File: clocks&select.kicad_sch

Title: 555 Clocks

Size: A4 Date: 2022-05-08
KiCad E.D.A. kicad (6.0.5)

Rev: 1
Id: 1/6

555 NOT Gate



jessefarrell92@gmail.com
Single 555 not gate

Sheet: /NOT/
File: block_555inverter.kicad_sch

Title: 555 NOT Gate

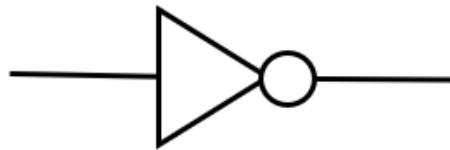
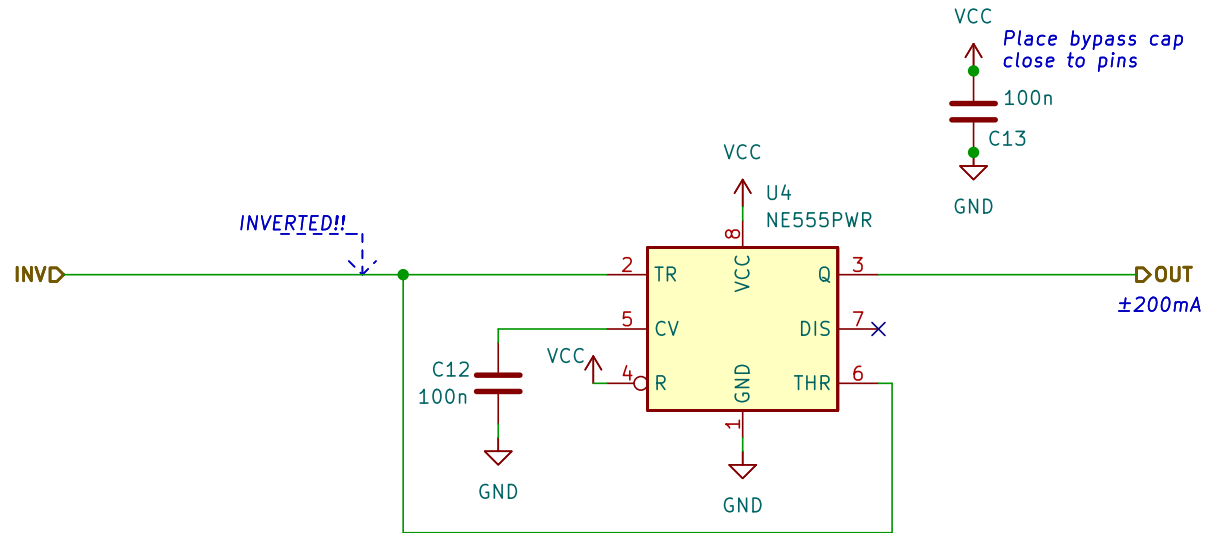
Size: A5 Date: 2022-05-04

Rev: 0

KiCad E.D.A. kicad (6.0.5)

Id: 2/6

555 NOT Gate



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Single 555 not gate

Sheet: /NOT1/
File: block_555inverter.kicad_sch

Title: 555 NOT Gate

Size: A5 Date: 2022-05-04

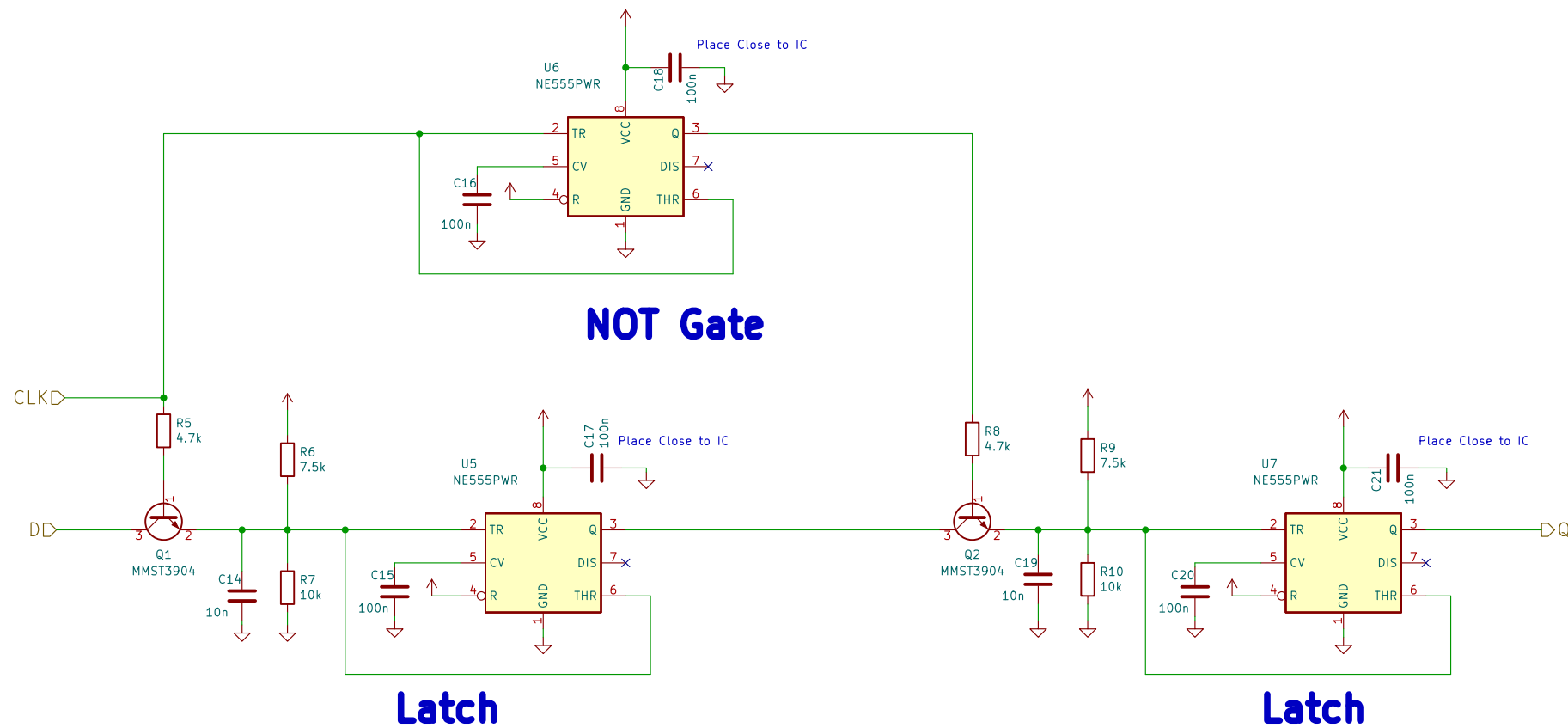
Rev: 0

KiCad E.D.A. kicad (6.0.5)

Id: 3/6

Note that this flip flop will trigger on the falling edge of CLK. Also the input D is flipped twice, hence output Q is D

555 FLIP FLOP



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Sheet: /Flip_Flop/
File: block_555flipflop.kicad_sch

Title: 555 Flip Flop

Size: A4 Date: 2022-05-22

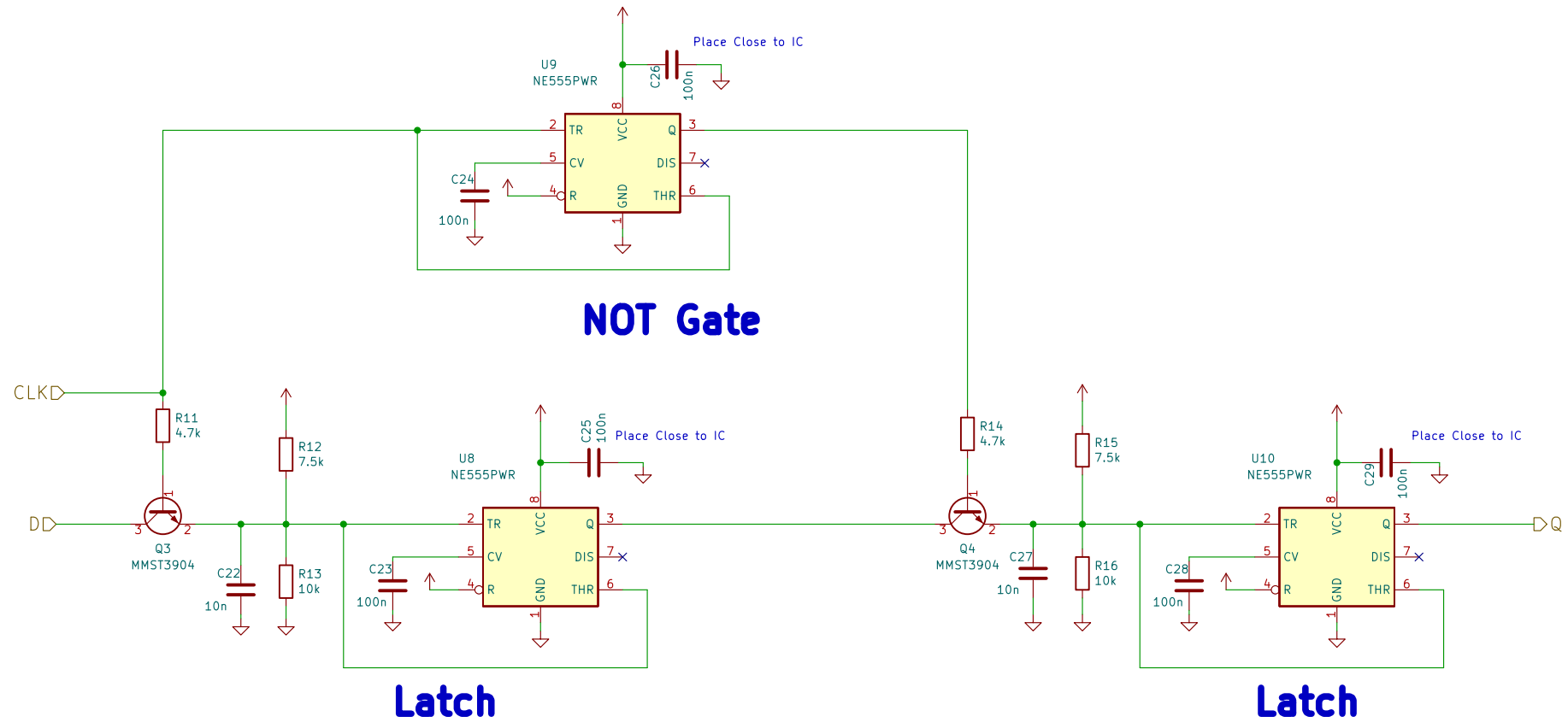
KiCad E.D.A. kicad (6.0.5)

Rev: 1

Id: 4/6

Note that this flip flop will trigger on the falling edge of CLK. Also the input D is flipped twice, hence output Q is D

555 FLIP FLOP



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Sheet: /Flip_Flop1/
File: block_555flipflop.kicad_sch

Title: 555 Flip Flop

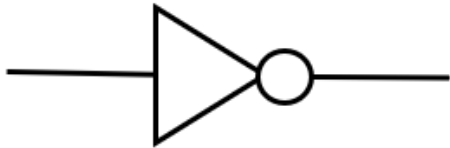
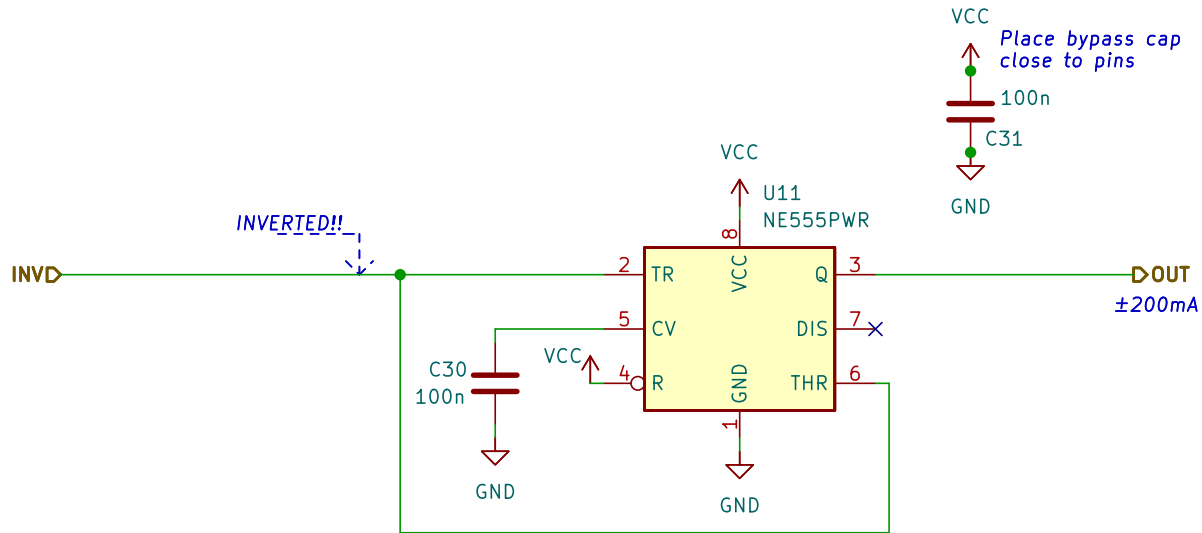
Size: A4 Date: 2022-05-22

KiCad E.D.A. kicad (6.0.5)

Rev: 1

Id: 5/6

555 NOT Gate



jessefarrell92@gmail.com
Single 555 not gate

Sheet: /NOT2/
File: block_555inverter.kicad_sch

Title: 555 NOT Gate

Size: A5 Date: 2022-05-04

Rev: 0

KiCad E.D.A. kicad (6.0.5)

Id: 6/6