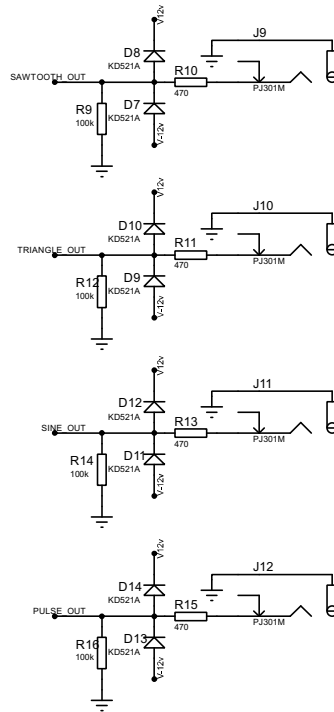
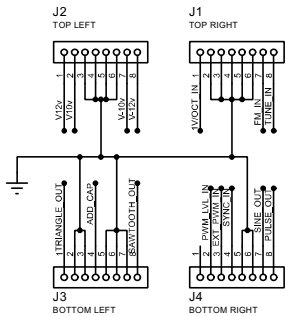
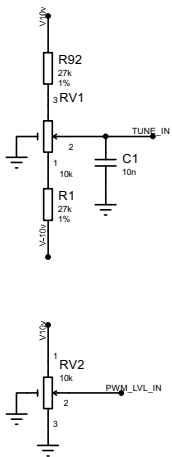
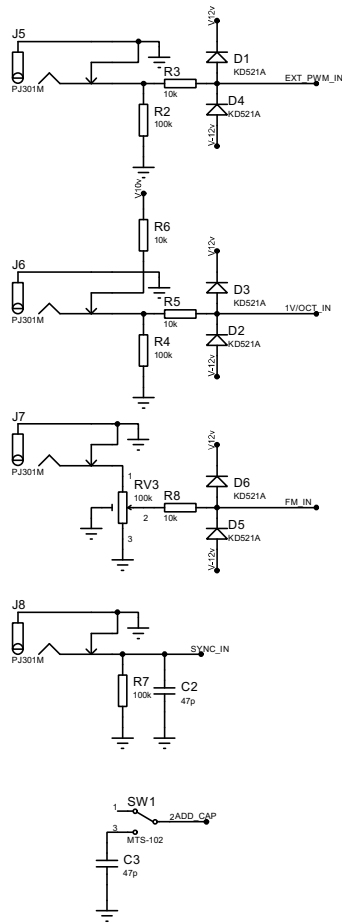




Change log:	v1.0	v1.1
RV1	incorrect pin 1 and 3 assignment	pins 1 and 3 swapped

PROJECT: MSynth-Pollvoiks-VCO	DATE: 13-Jan-23
BOARD: Control	TIME: 14:43:55
BY: jvdem	REV: 1.1