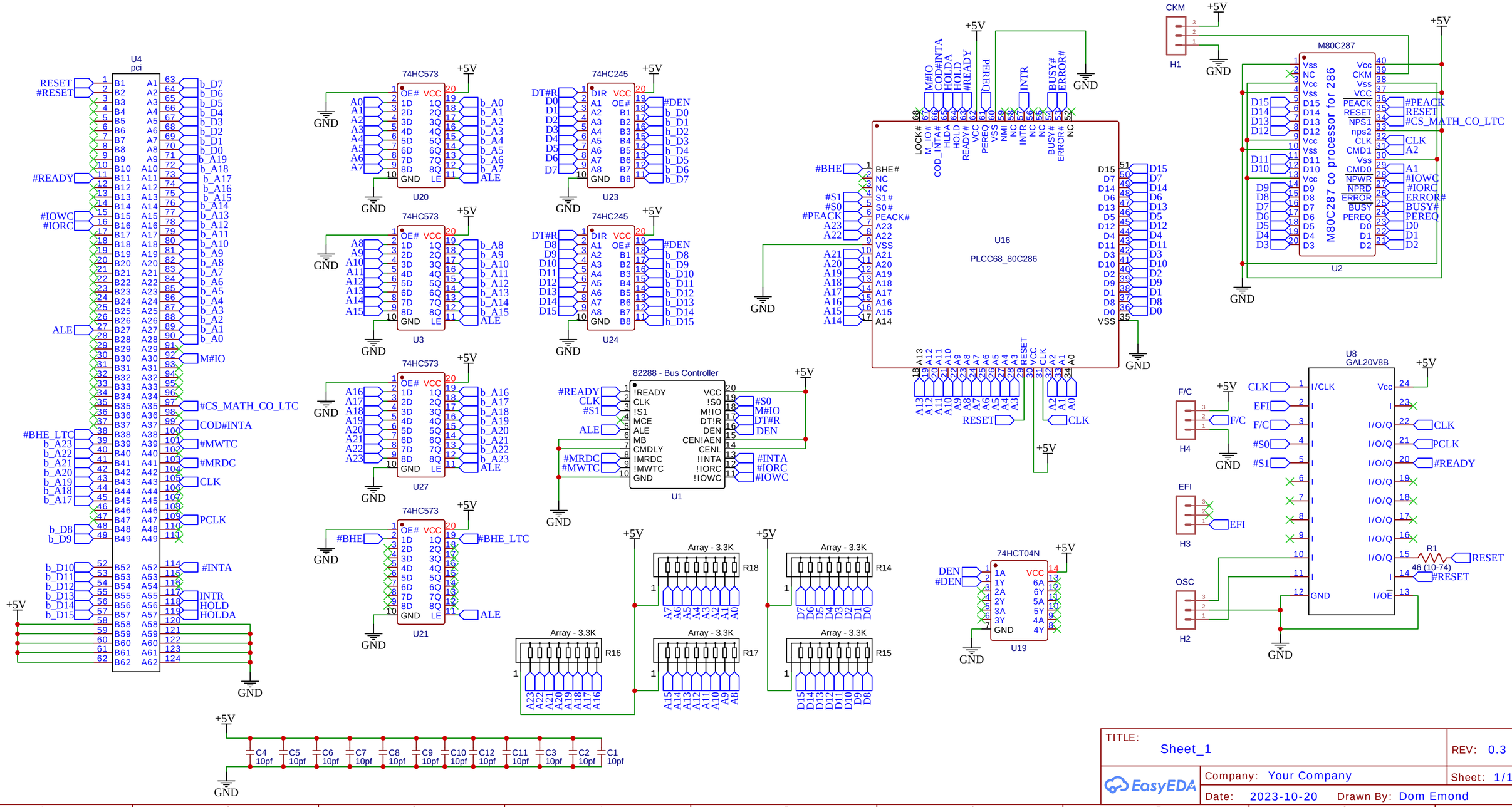
U4
pci
RESET
#RESET
#READY
#IOWC
#IORC
ALE
M#IO
#CS_MATH_CO_LTC
COD#INTA
#MWTC
#MRDC
CLK
PCLK
#INTA
INTR
HOLD
HOLDA
#BHE_LTC
b_D8
b_D9
+5V
GND
74HC573
U20
U3
U27
U21
74HC245
U23
U24
#DEN
#BHE_LTC
82288 - Bus Controller
U1
#S0
#S1
#MRDC
#MWTC
#INTA
#IORC
#IOWC
DEN
U16
PLCC68_80C286
M80C287
U2
M80C287 co processor for 286
#PEACK
#CS_MATH_CO_LTC
#IOWC
#IORC
ERROR#
BUSY#
PEREQ
CKM
H1
F/C
H4
EFI
H3
OSC
H2
U8
GAL20V8B
PCLK
#READY
R1
46 (10-74)
74HCT04N
U19
Array - 3.3K
R18
R14
R16
R17
R15
C4 10pf
C5 10pf
C6 10pf
C7 10pf
C8 10pf
C9 10pf
C10 10pf
C12 10pf
C11 10pf
C3 10pf
C2 10pf
C1 10pf
TITLE:
Sheet_1
REV: 0.3
EasyEDA
Company: Your Company
Sheet: 1/1
Date: 2023-10-20
Drawn By: Dom Emond