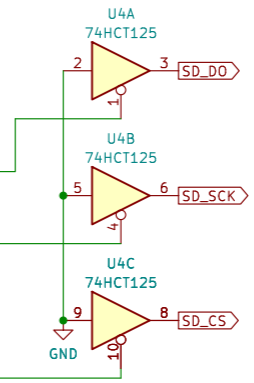
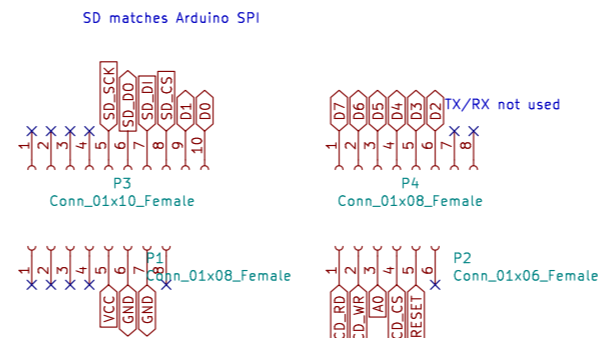
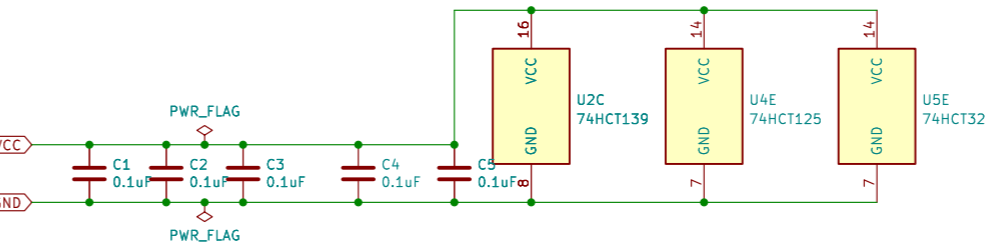
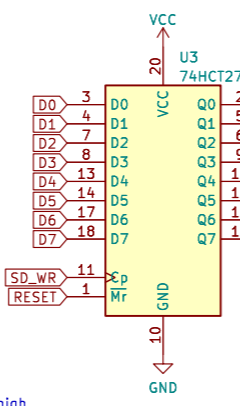
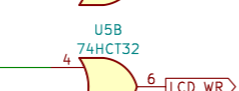
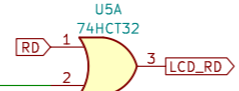
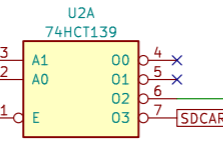
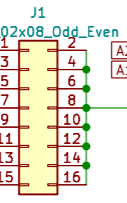
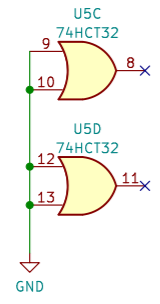
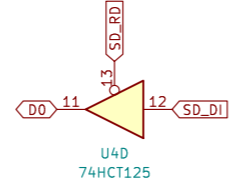
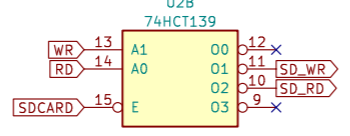


01XXX1AN  
 A 0 LCD  
 A 1 KBD  
 B - reg select on LCD, unused on latches



For interrupt cycles M1 and IORQ are low but RD/WR are high so we don't need to consider M1 as well



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