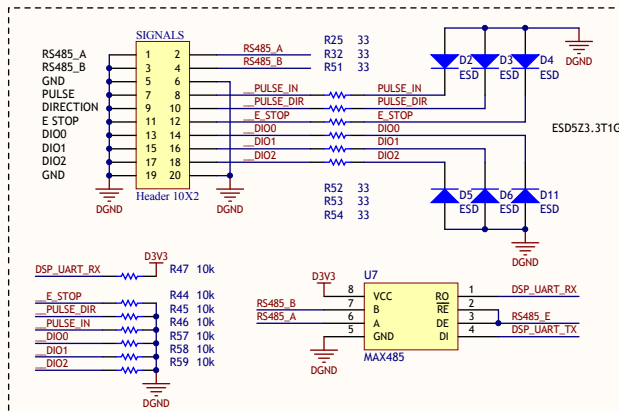
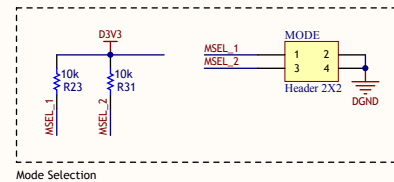


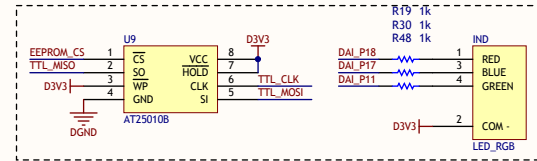
Sync In & Out



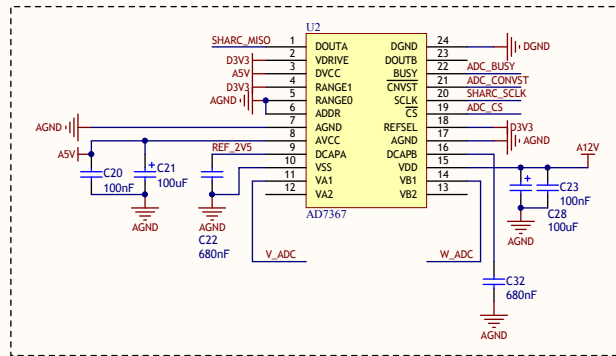
Interface & transceiver



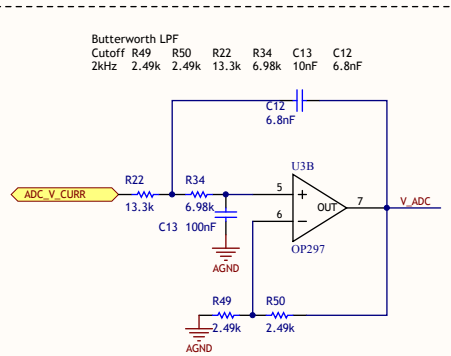
Mode Selection



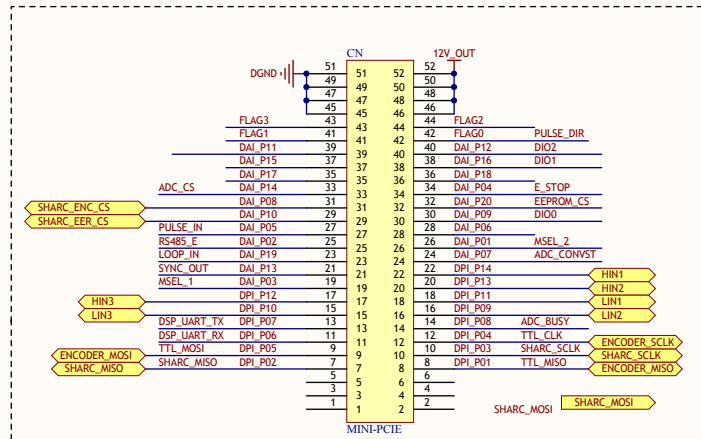
EEPROM & LED



Current Sample ADC

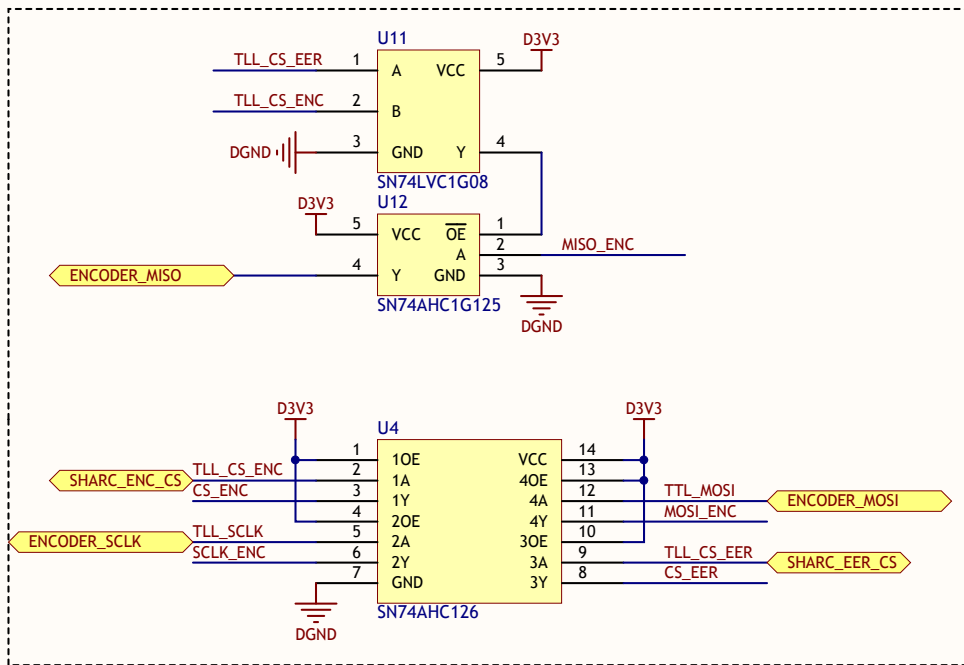


Current Sample LPF

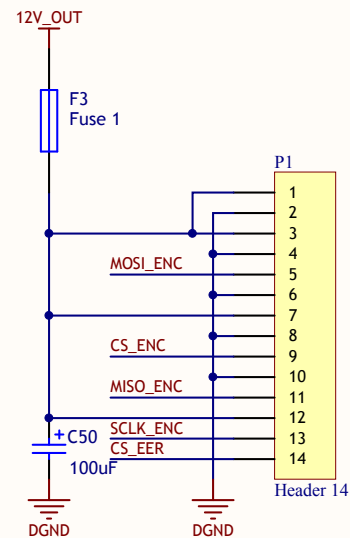
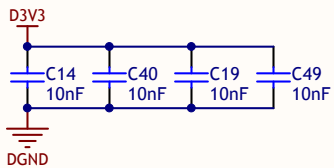


SHARC Core

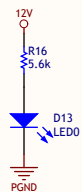
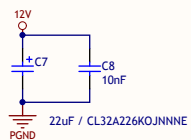
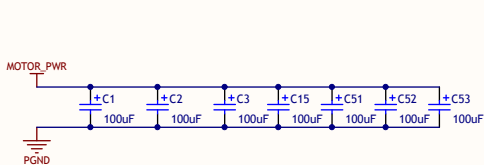
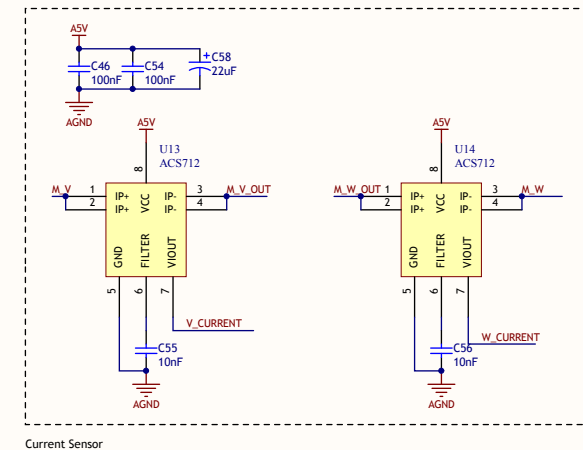
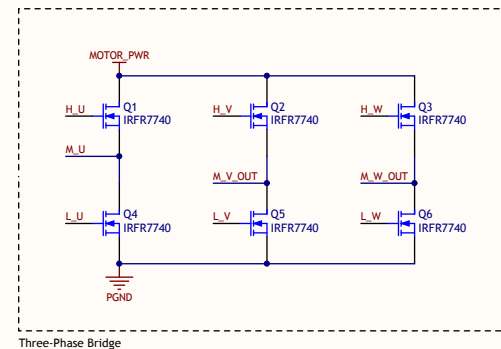
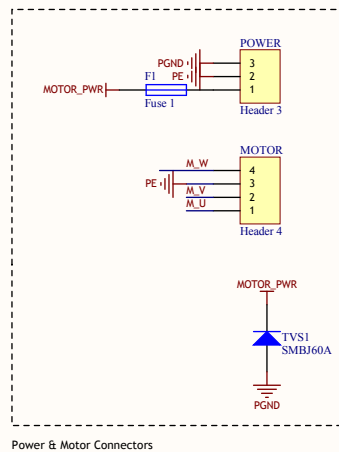
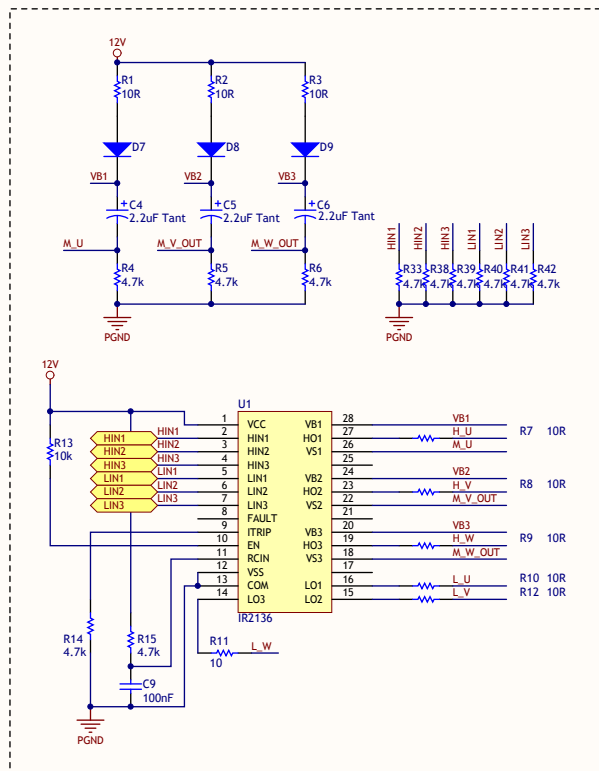
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File:	C:\Users\...M-DSP_SchDoc	Drawn By:



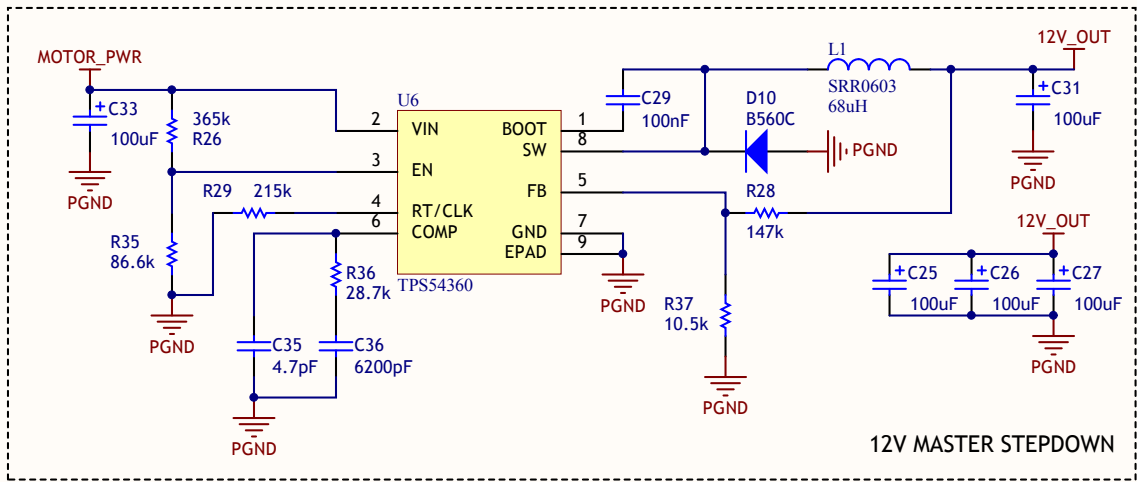
Encoder Buffers



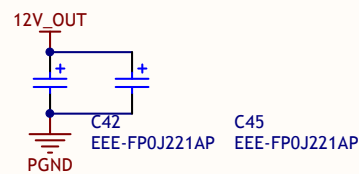
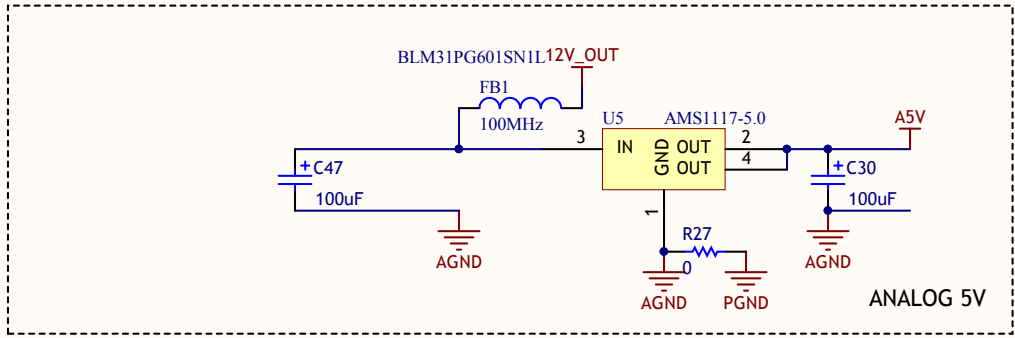
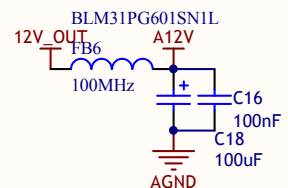
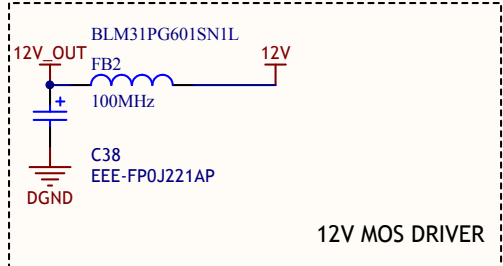
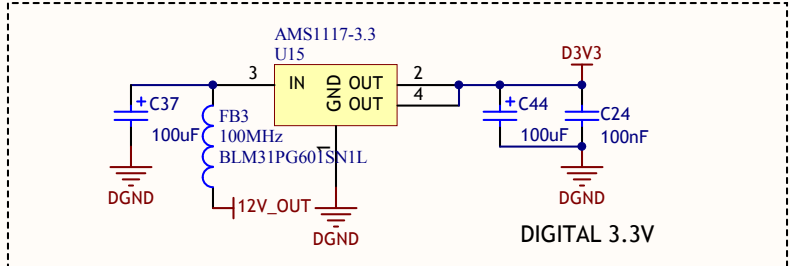
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Size B	Number	Revision
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DESCRIPTION	NET NAME
MASTER POWER: 12V/4A	12V_OUT
MOS DRIVER :12V/1A	12V
DSP POWER: 12V/1A	12V
ANALOG POWER: 5V/1A	A5V
DIGIAL POWER: 3.3V/1A	D3V3



Title		
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Date:	2017/1/19	Sheet of
File:	C:\Users\...M-Power.SchDoc	Drawn By:

