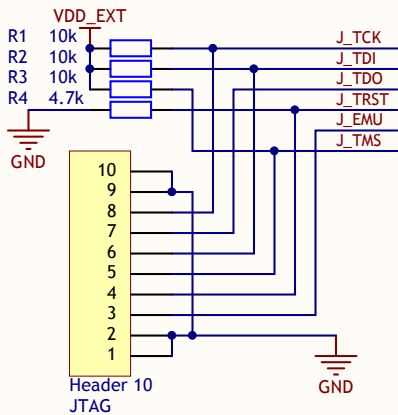
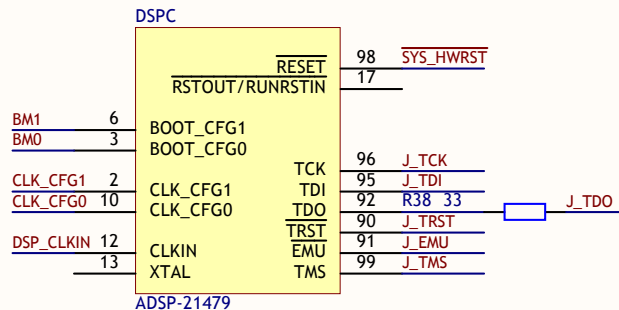
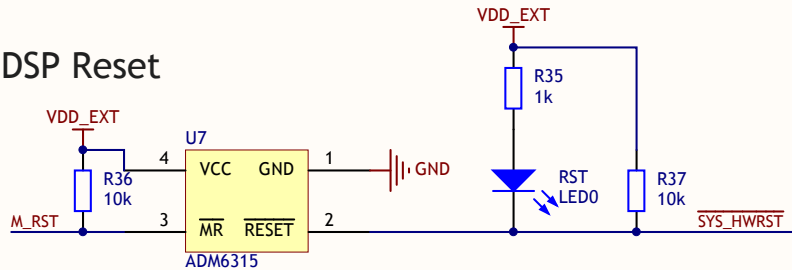


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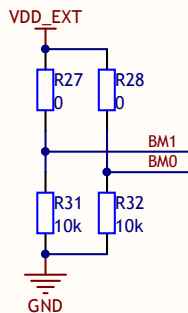
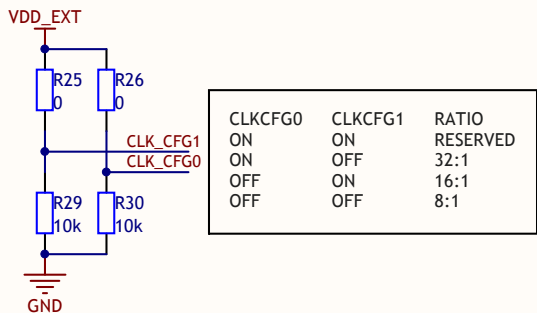
JTAG Interface



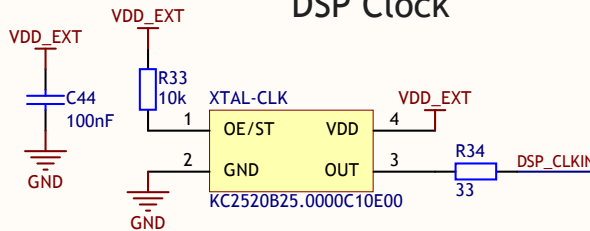
DSP Reset



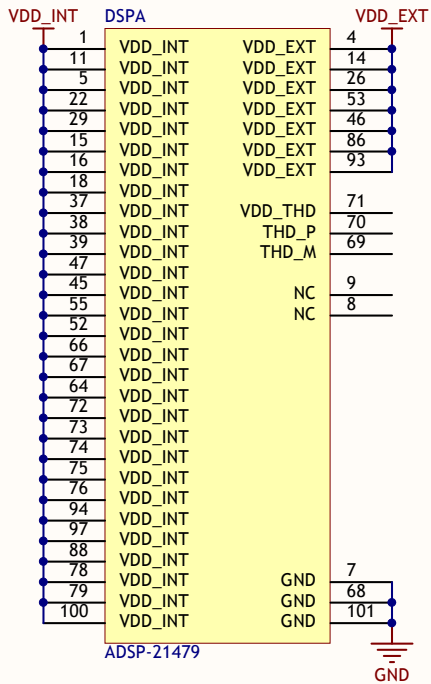
Boot mode config & clock mode config



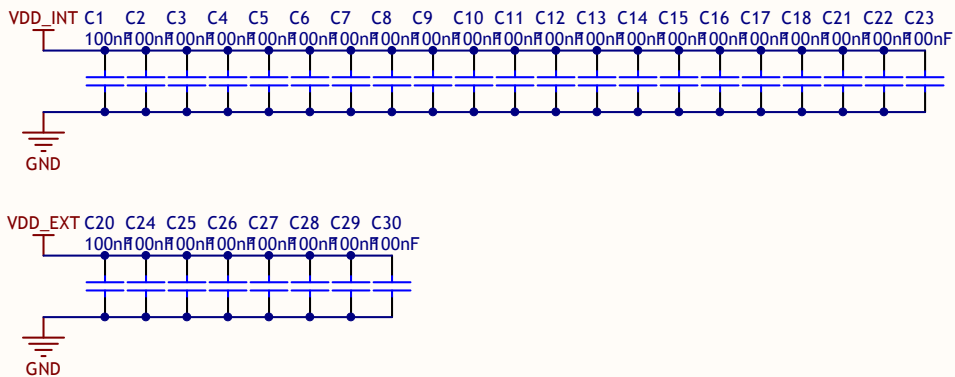
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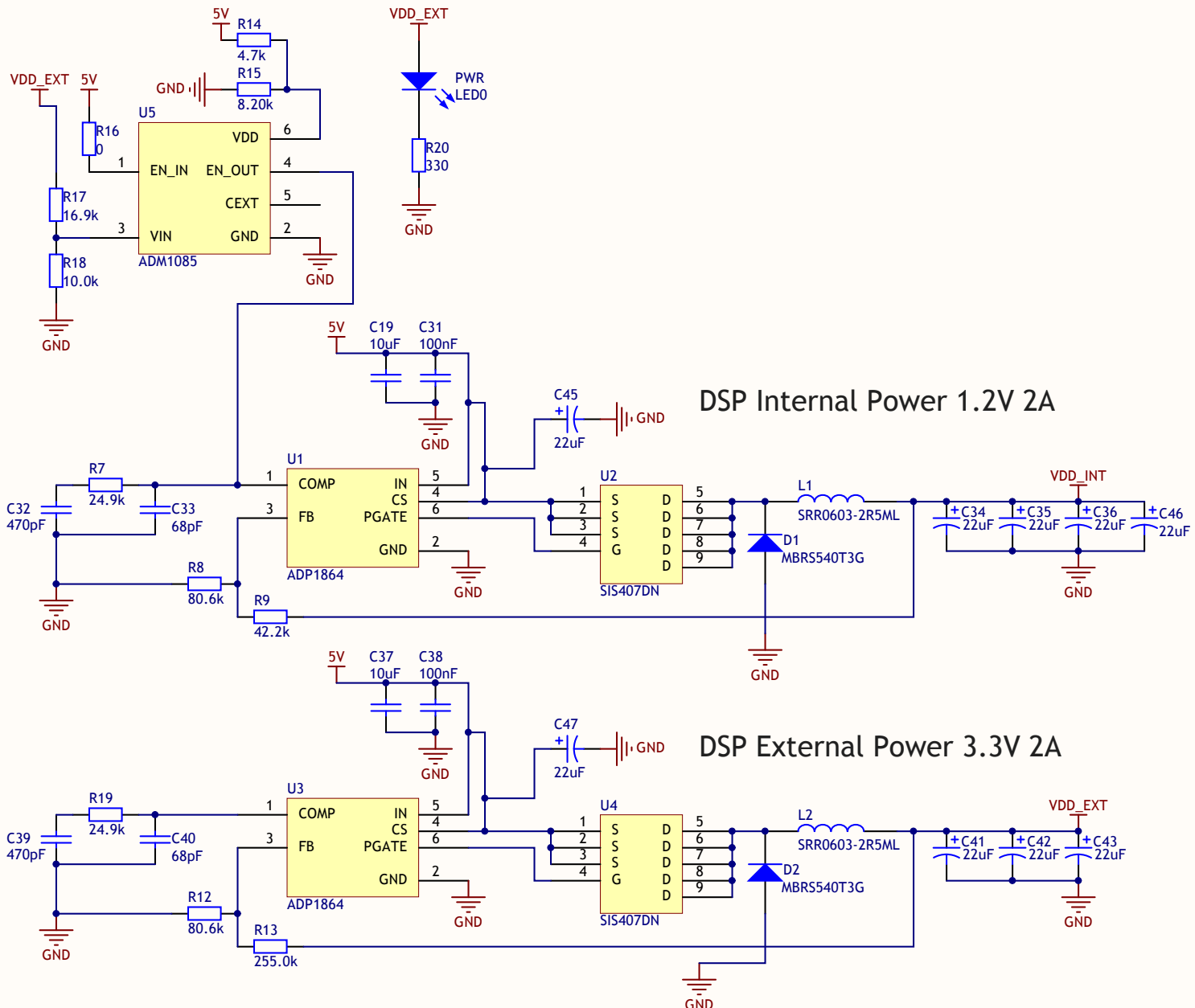
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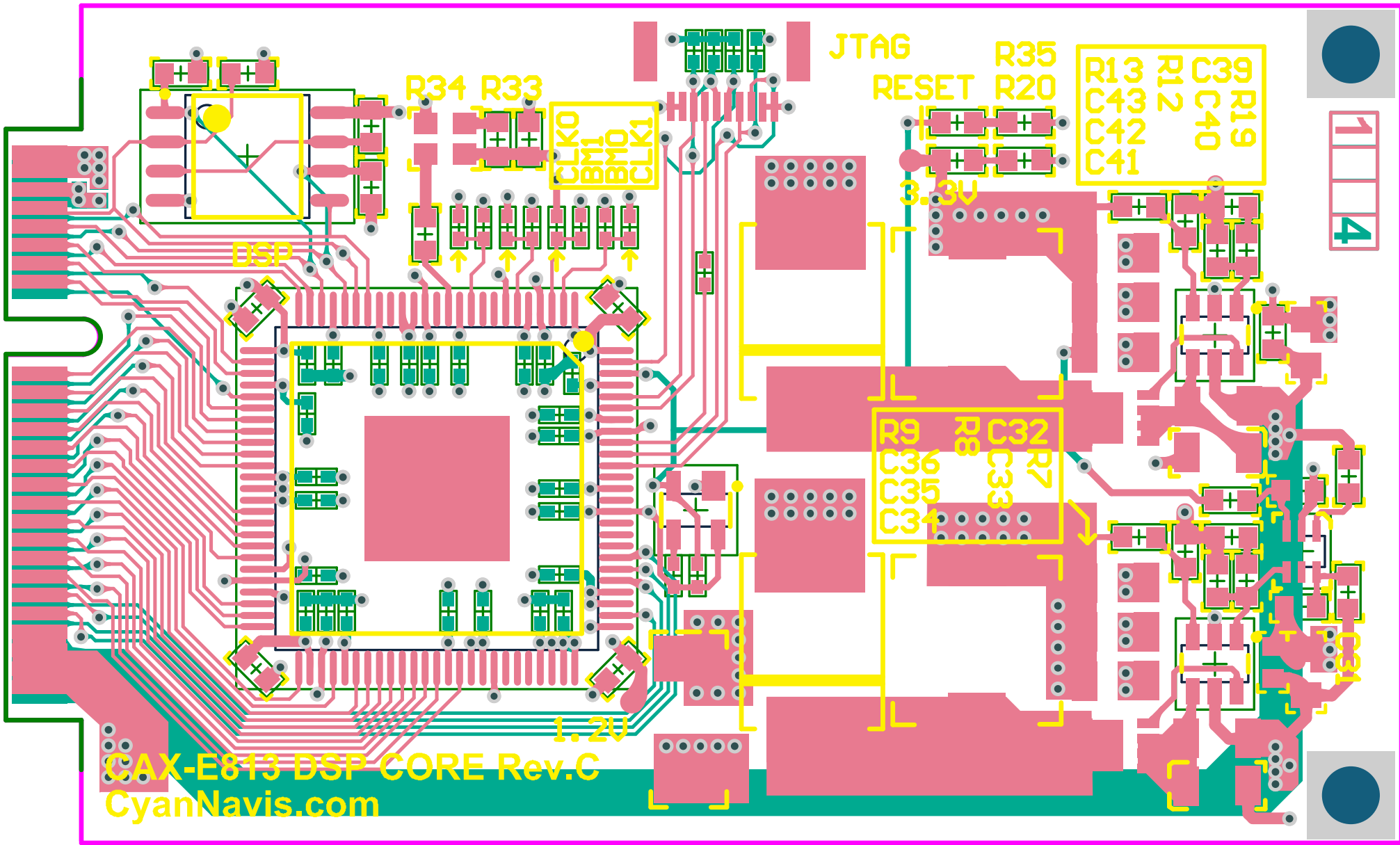
DSP Power Decoupling



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