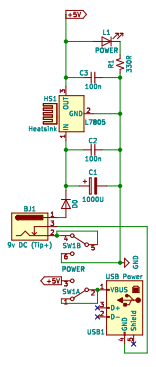
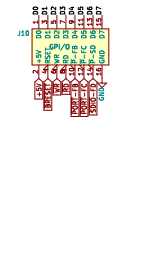


TEC-1G

Power Delivery

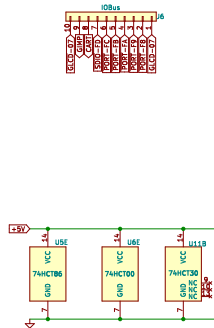


General Input/Output

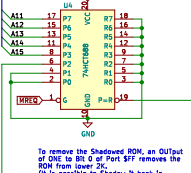


The TEC Deck

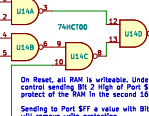
The new way to expand your TEC-1G with appropriate snap-on headers, expansion boards can be slotted on top of each other, just like the original TEC-1. But now you have access to ALL the 286 pins as well as port and memory select lines. Memory expansion is EZ with ease. Input/output options for every



Memory Management Unit



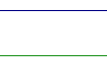
Memory Protection



System Latch

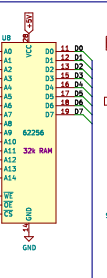


Status Lights



64k Memory

The lower 32K of all RAM is a single chip. The upper 16K of the memory map is reserved for the system ROM, although it is made up of up to a 64K EPROM to allow selection of multiple members, using a pair of switches.



I/O Decoders

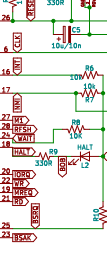


System Input

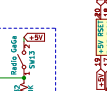


CPU & Clock

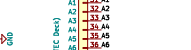
These three jumpers allow for 2MHz ROM/RAM to be used in the Expansion socket.



Expansion Connectors



MEMBus



Female Socket (for TEC Deck)



Z80bus Vertical



BSRD



BSAK



MUT



MHT



RFSH



INT



WR



MREQ

ORDR

M1

CLK

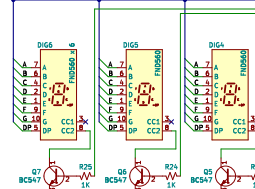
RST

PST

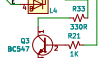
LCD 20 Characters x 4 Lines



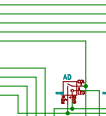
7 Segment Display Unit



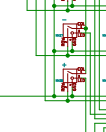
Disco LEDs



HexPad Encoder



Matrix Keyboard & Joystick



Speed Control



Modelled on the TEC-1 rev.D with DAT add-on
Originally designed by John Hardy, Ken Stone & Jim Robertson
published in Talking Electronics Magazine, 1983 - 1985
Thanks for assistance from: Craig Hart, Brian Chien, Ian McLean, James Elphick
© Mark Hele, 2025
Sheet / File: TEC-1G.kicad_sch
Title: TEC-1G (Board revision: Production v1.10)
Size: A2 | Date: 2023-10-31 | Rev: 1.13
Kicad E.D.A. kicad (6.0.10) | 16 / 171