M.2										
Key M	Key B	Key E	Key A	Key	Pin	Key	Key A	Key E	Key B	Key M
					1		GND	GND	CONFIG_3	GND
3.3V	3.3V	3.3V	3.3V		2					
					3		USB+	USB+	GND	GND
3.3V	3.3V	3.3V	3.3V		4					
					5		USB-	USB-	GND	PE3 R-
n.c.	nCARD_PWR_OFF	nLED1	nLED1		6					
					7		GND	GND	USB+	PE3 R+
n.c.	nW_DISABLE1	I2S SCK		A	8	A				
					9			SDIO CLK	USB-	GND
DAS/DSS/nLED1	GPIO_9/nLED1	I2S WS			10					
				A	11	A		SDIO CMD	GND	PE3 T-
3.3V		I2S SD IN		AB	12	AB				
					13			SDIO DATA0		PE3 T+
3.3V		I2S SD OUT			14					
				AB	15	AB		SDIO DATA1		GND
3.3V		nLED2	nLED2	B	16	B				
					17		n.c.	SDIO DATA2		PE2 R-
3.3V		GND	GND		18					
				B	19	B	n.c.	SDIO DATA3		PE2 R+
n.c.	GPIO_5	UART nWAKE	UART nWAKE		20					
					21		n.c.	SDIO nWAKE	CONFIG_0	GND
n.c.	GPIO_6	UART RXD	UART RXD		22					
					23		n.c.	SDIO nRST	GPIO_11	PE2T-
n.c.	GPIO_7			E	24	E				
					25				DPR	PE2 T+
n.c.	GPIO_10				26					
					27		GND		GND	GND
n.c.	GPIO_8				28					
					29				PE1/USB3/SSIC Rx-	PE1 R-
n.c.	UIM_RESET				30					
				E	31	E			PE1/USB3/SSIC Rx+	PE1 R+
n.c.	UIM_CLK	UART TXD	UART TXD		32					
					33		GND	GND	GND	GND
n.c.	UIM_DATA	UART CTS	UART CTS		34					
					35		PETx P0	PETx P0	PE1/USB3/SSIC Tx-	PE1 T-
n.c.	UIM_PWR	UART RTS	UART RTS		36					
					37		PETx N0	PETx N0	PE1/USB3/SSIC Tx+	PE1 T+
DEVSLP	DEVSLP	VEN DEF1	VEN DEF1		38					
					39		GND	GND	GND	GND
SMB_CLK	GPIO_0/SMB_CLK	VEN DEF2	VEN DEF2		40					
					41		PERx P0	PERx P0	PE0 R-/SATA-B+	PE0 R-/SATA-B+
SMB_DATA	GPIO_1/SMB_DATA	VEN DEF3	VEN DEF3		42					
					43		PERx N0	PERx N0	PE0 R+/SATA-B-	PE0 R+/SATA-B-
nALRET	GPIO_2/nALRET	COEX3	COEX3		44					
					45		GND	GND	GND	GND
n.c.	GPIO_3	COEX2	COEX2		46					
					47		REFCLK P0	REFCLK P0	PE0 Tx-/SATA-A-	PE0 Tx-/SATA-A-
n.c.	GPIO_4	COEX1	COEX1		48					
					49		REFCLK N0	REFCLK N0	PE0 Tx+/SATA-A+	PE0 Tx+/SATA-A+
nPERST	nPERST	SUSCLK	SUSCLK		50					
					51		GND	GND	GND	GND
nCLKREQ	nCLKREQ	nPERST0	nPERST0		52					
					53		nCLKREQ0	nCLKREQ0	REFCLK-	REFCLK-
nPEWAKE	nPEWAKE	nW DISABLE2	nW DISABLE2		54					
					55		nPEWAKE0	nPEWAKE0	REFCLK+	REFCLK+
n.c.	n.c.	nW DISABLE1	nW DISABLE1		56					
					57		GND	GND	GND	GND
n.c.	n.c.	I2C SDA	I2C SDA		58					
				M	59	M	PETx P1	PETx P1	ANTCTL0	
	COEX3	I2C SCL	I2C SCL		60					
					61		PETx N1	PETx N1	ANTCTL1	
	COEX_TXD	I2C nALERT	I2C nALERT		62					
					63		GND	GND	ANTCTL2	
	COEX_RXD	RESV	RESV		64					
					65		PERx P1	PERx P1	ANTCTL3	
	SIM_DETECT	nPERST1	nPERST1	M	66	M				
					67		PERx N1	PERx N1	nRESET	n.c.
SUSCLK	SUSCLK	nCLKREQ1	nCLKREQ1		68					
					69		GND	GND	CONFIG_1	PEDET
3.3V	3.3V	nPEWAKE1	nPEWAKE1		70					
					71		REFCLK P1	REFCLK P1	GND	GND
3.3V	3.3V	3.3V	3.3V		72					
					73		REFCLK N1	REFCLK N1	GND	GND
3.3V	3.3V	3.3V	3.3V		74					
					75		GND	GND	CONFIG_2	GND

PE	1.8V IO
CLK	
USB	3.3V IO
UART	
I2C	Open Drain
LED	
3.3V	
GND	
n.c.	