

A

B

C

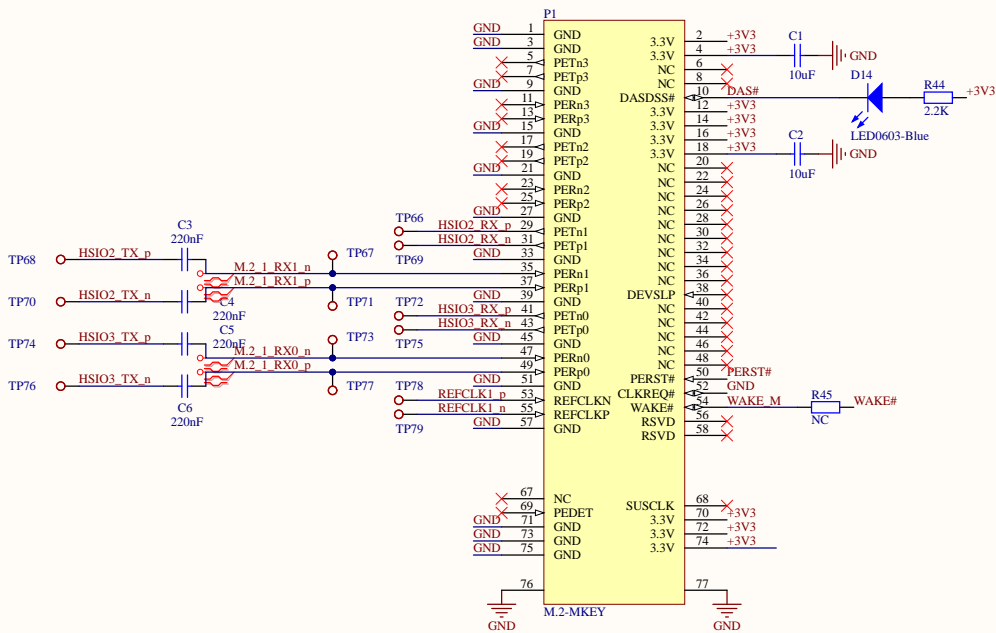
D

A

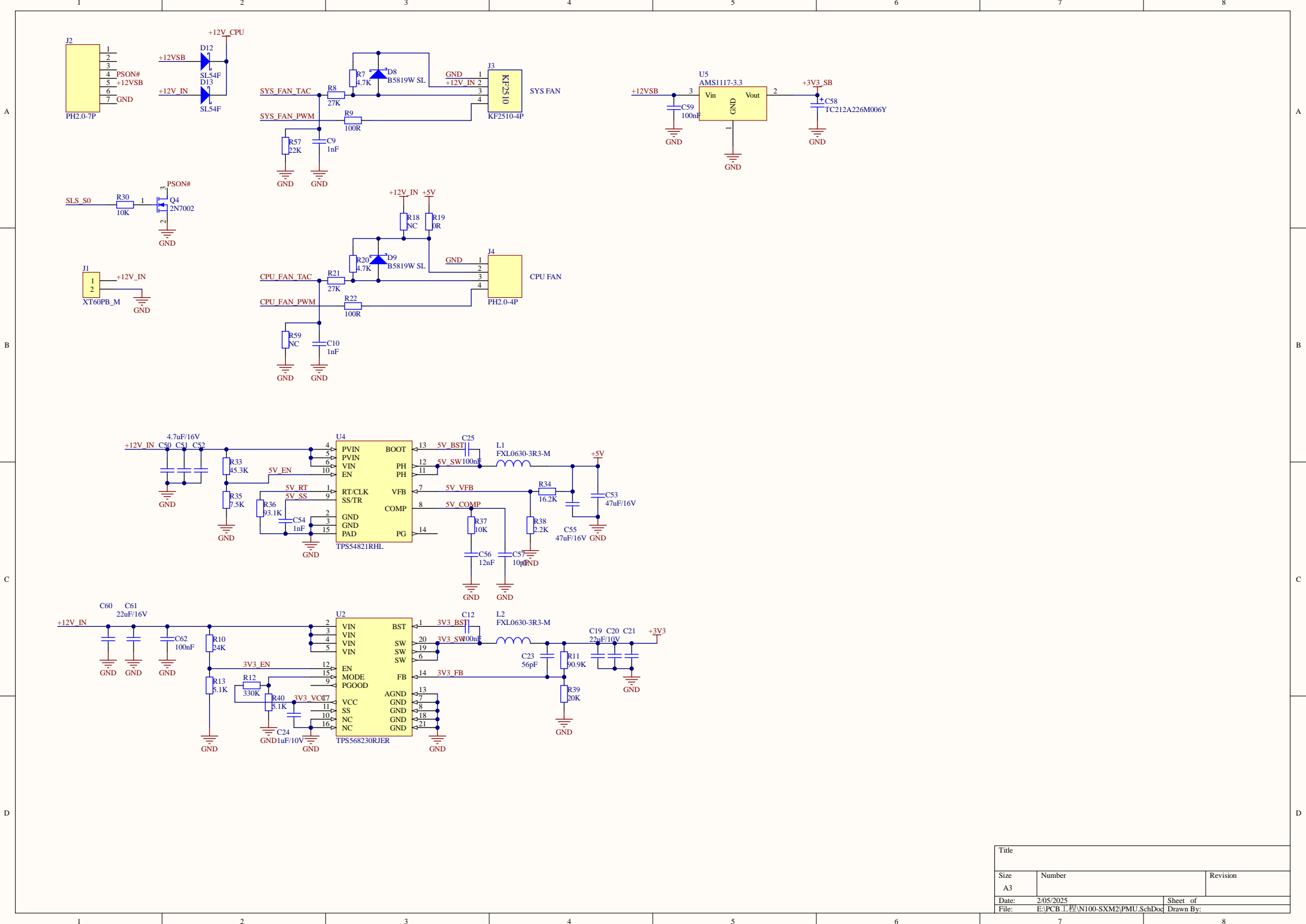
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C

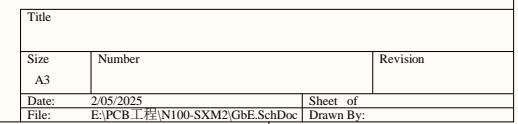
D



Title		
Size	Number	Revision
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Date:	2/05/2025	Sheet of
File:	E:\PCB工程\N100-SXM2\M.2-PCIE.Sch	Drawn By:



Title		
Size	Number	Revision
A3		
Date:	2/05/2025	Sheet of
File:	E:\PCB工程\N100-SXM2\PMU.SchDoc	Drawn By:



A

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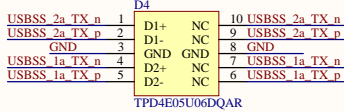
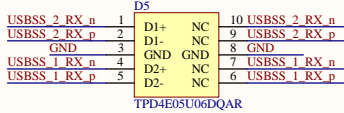
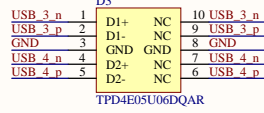
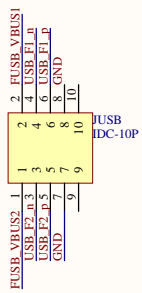
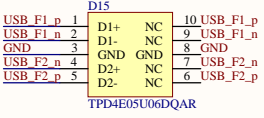
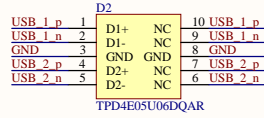
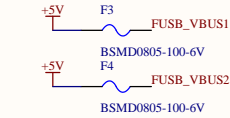
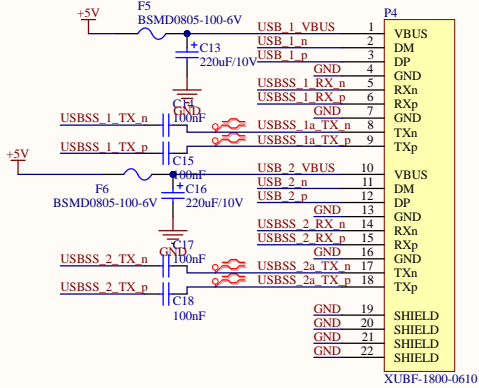
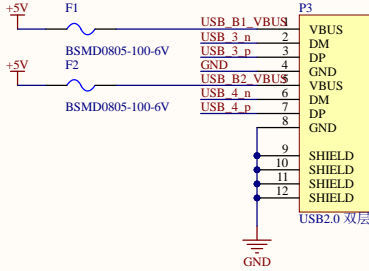
D

A

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Title		
Size	Number	Revision
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File:	E:\PCB工程\N100-SXM2\USB_SchDoc	Drawn By:

A

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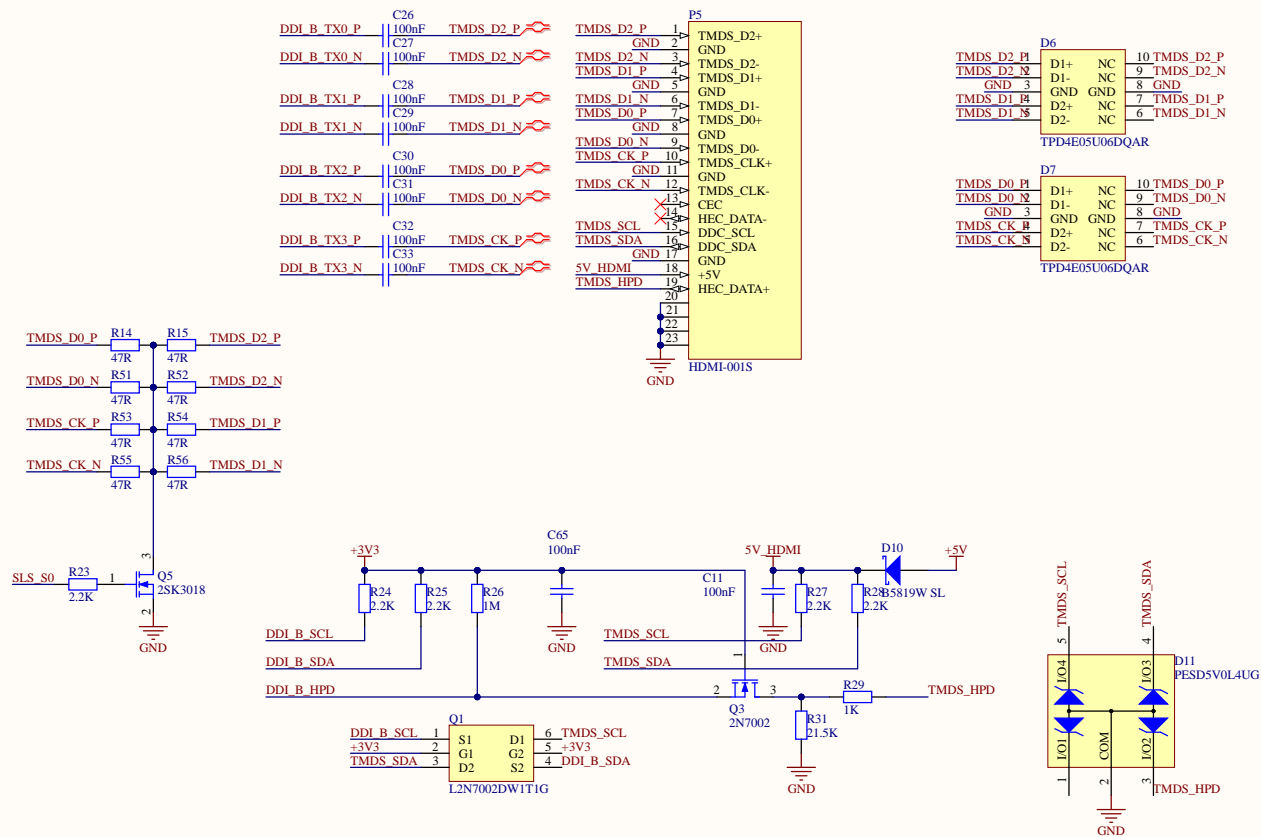
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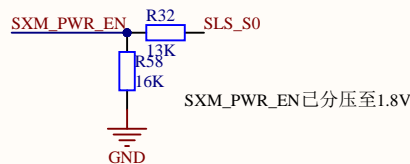
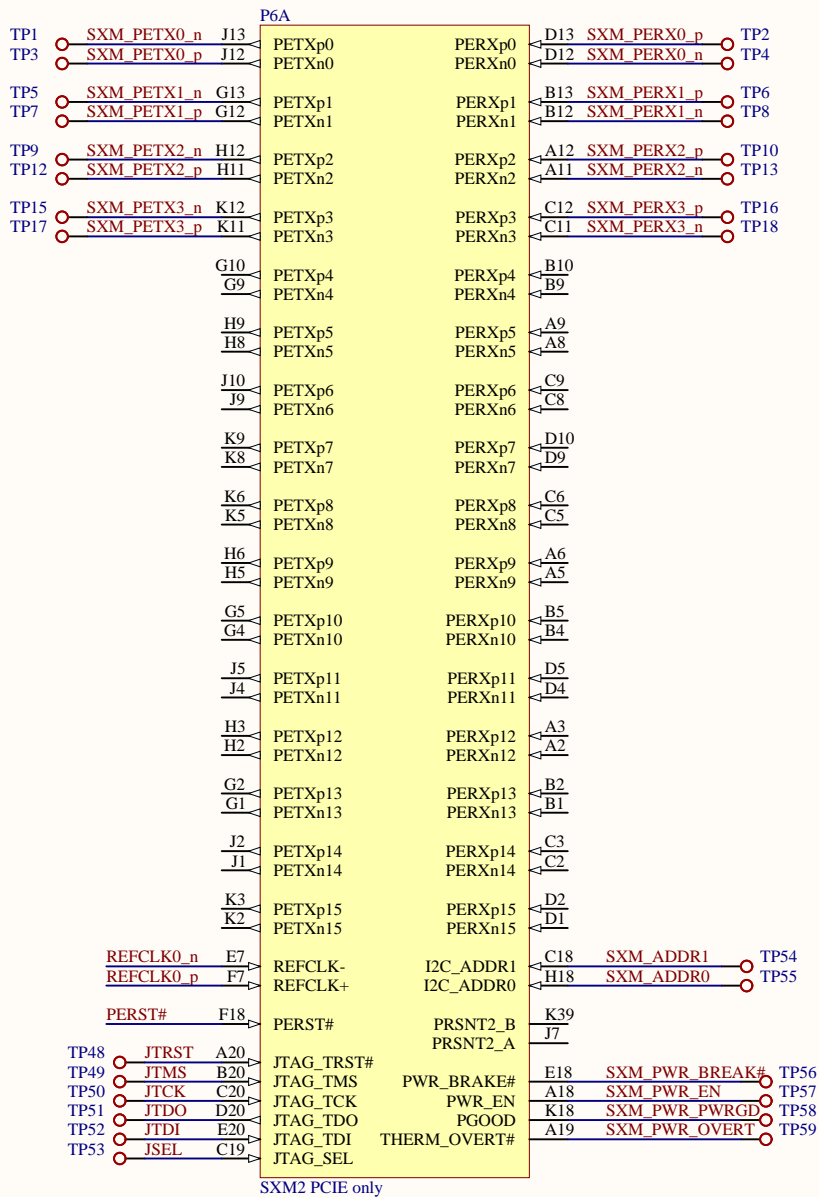
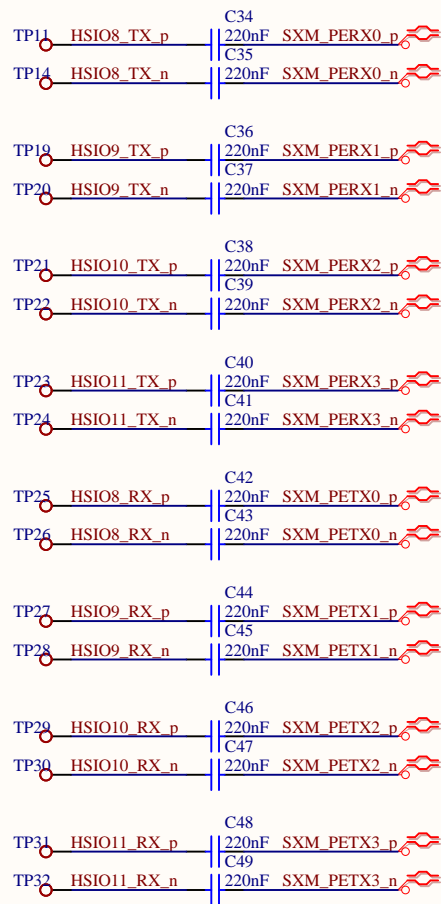
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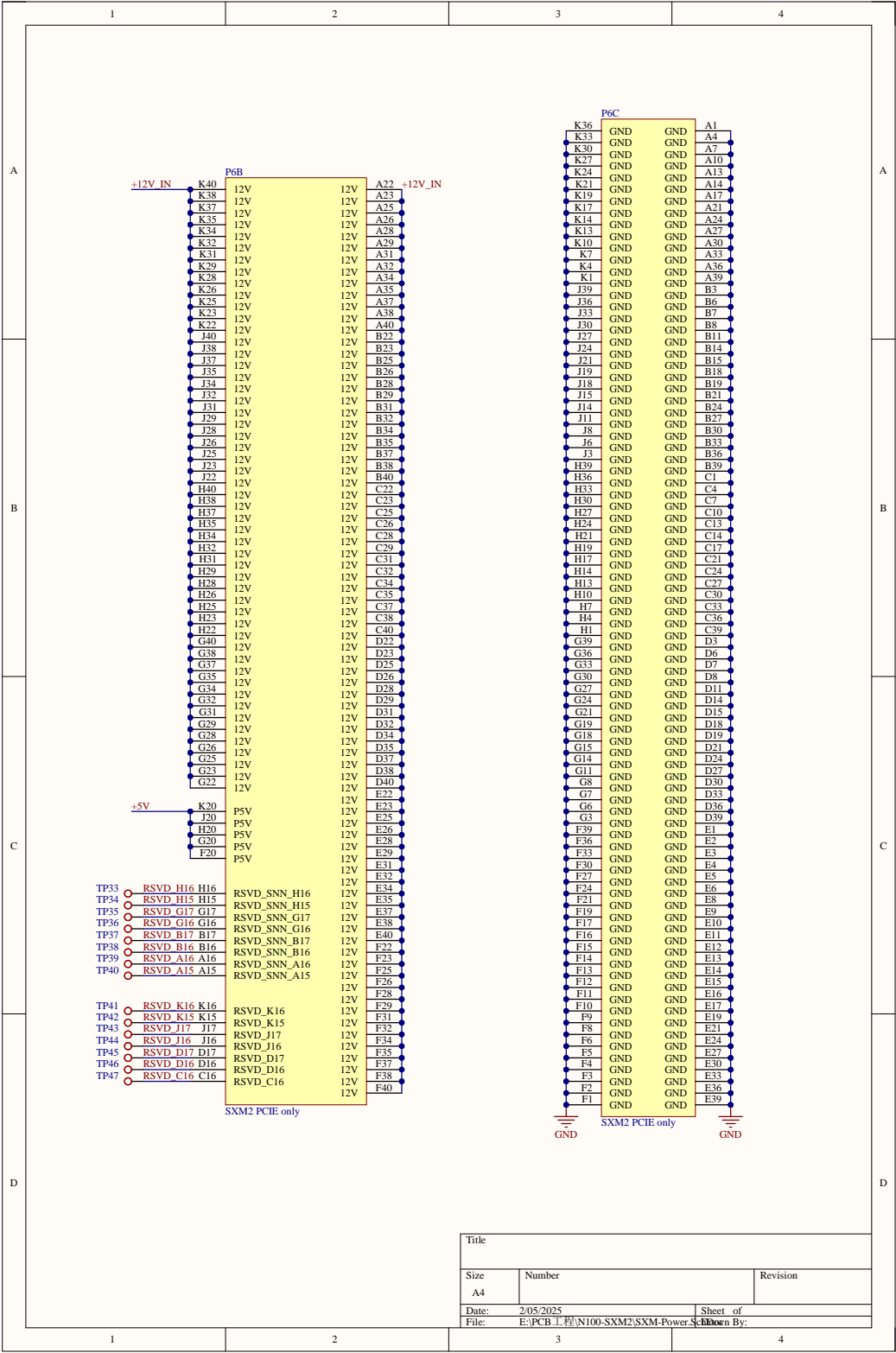


Title		
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A3		
Date:	2/05/2025	Sheet of
File:	E:\PCB工程\N100-SXM2\DP-HDMI Sch	Down By:

SXM2模块装配高度: 4.0mm (PCB-PCB)



Title		
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A4		
Date:	2/05/2025	Sheet of
File:	E:\PCB工程\N100-SXM2\SXM-PCIE.SchDrawn By:	



Title		
Size	Number	Revision
A4		
Date:	2/05/2025	Sheet of
File:	E:\PCB 1.程\N100-SXM2\SXM-Power.Sch	Drawn By: