As of 27 October 2021 development has halted for an indeterminate length of time.

Here's roughly where things stand:

- Software
  - Have a reasonably well-featured Boot Monitor, not all features implemented
  - Pending Tasks
    - Implement IM2-mode serial console I/O so can have reliable input when bogged in CPU-intensive tasks
    - Implement SD-card I/O support routines in BIOS wrapped by top-level Read Sector / Write Sector
    - Wrapper the ROM code with a jump table-style dispatch so as to make it more "bios like" and not just a collection of subroutines

- Hardware
  - Testing SpiffyDrive SD card prototype boad
    - Rev 3 board has a design flaw – should be 74HCT00 ff instead of 74HCT02 ff. Populated proto board under testing has a kludge socket adapter to correct and initial logic probe tests confirm change fixes this issue
    - R/W, Select, and Xfer Start all test to work as designed
    - Corrected Rev 4 board in KiCAD should be ready for OshPark (confirm)
  - KiCAD design in progress for LED Debug Display board
    - Unresolved design issue is re: blanking select/deselect

- Known Bugs/Issues
  - FTDi serial connector issue on Z80 board. Testing suggests TX from board into USB cable RX line is OK. Testing suggests TX from USB cable into board RX line is squelched by overzealous current-limiting resistor(s). Test a bypass
  - Not sure if RS-232 level conversion on one-off adapter board is working right but observe FTDi cable to work fine when connected there.