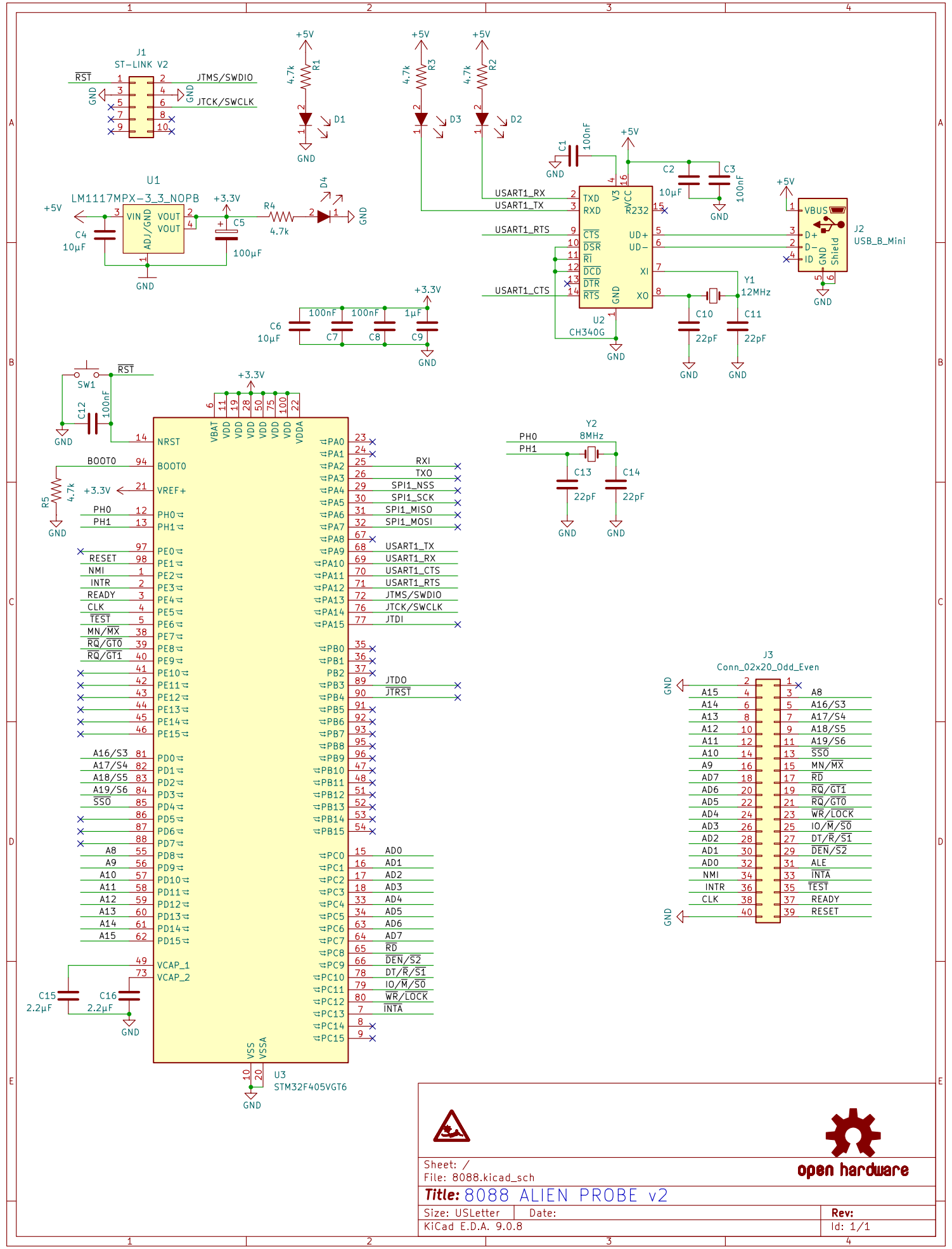
J1
ST-LINK V2
RST
JTMS/SWDIO
JTCK/SWCLK
GND
R1
4.7k
D1
R2
D2
R3
D3
+5V
U1
LM1117MPX-3_3_NOPB
VIN
VOUT
ADJ/GND
+3.3V
C4
10µF
C5
100µF
R4
4.7k
D4
C1
100nF
C2
10µF
C3
100nF
U2
CH340G
TXD
RXD
RTS
CTS
DSR
RI
DCD
DTR
R232
VCC
XI
XO
UD+
UD-
USART1_RX
USART1_TX
USART1_RTS
USART1_CTS
J2
USB_B_Mini
VBUS
D+
D-
ID
Shield
Y1
12MHz
C10
22pF
C11
22pF
C6
10µF
C7
100nF
C8
100nF
C9
1µF
SW1
C12
100nF
R5
4.7k
BOOT0
VREF+
PH0
PH1
NRST
RESET
NMI
INTR
READY
CLK
TEST
MN/MX
RQ/GT0
RQ/GT1
SSO
VBAT
VDD
VDDA
VSS
VSSA
VCAP_1
VCAP_2
C15
2.2µF
C16
2.2µF
Y2
8MHz
C13
22pF
C14
22pF
RXI
TXO
SPI1_NSS
SPI1_SCK
SPI1_MISO
SPI1_MOSI
JTDO
JTRST
AD0
AD1
AD2
AD3
AD4
AD5
AD6
AD7
RD
DEN/S2
DT/R/S1
IO/M/S0
WR/LOCK
INTA
ALE
U3
STM32F405VGT6
J3
Conn_02x20_Odd_Even
A8
A9
A10
A11
A12
A13
A14
A15
A16/S3
A17/S4
A18/S5
A19/S6
TEST
RESET
open hardware
Sheet: /
File: 8088.kicad_sch
Title: 8088 ALIEN PROBE v2
Size: USLetter
Date:
Rev:
KiCad E.D.A. 9.0.8
Id: 1/1