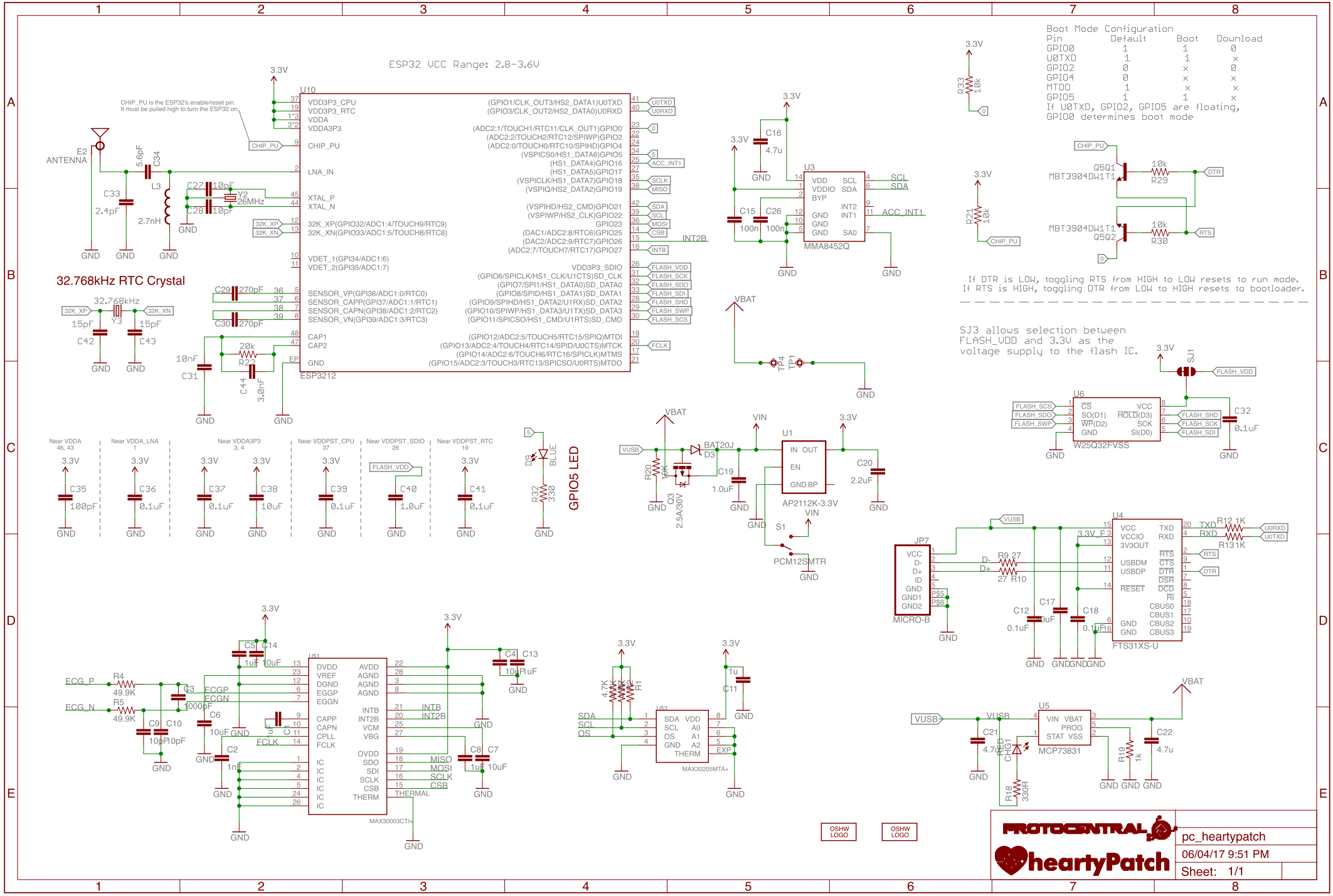
ESP32 VCC Range: 2.8-3.6V
U10
ESP3212
CHIP_PU is the ESP32's enable/reset pin.
It must be pulled high to turn the ESP32 on.
E2
ANTENNA
32.768kHz RTC Crystal
Boot Mode Configuration
Pin Default Boot Download
GPIO0 1 1 0
U0TXD 1 1 x
GPIO2 0 x 0
GPIO4 0 x x
MTDO 1 x x
GPIO5 1 1 x
If U0TXD, GPIO2, GPIO5 are floating,
GPIO0 determines boot mode
If DTR is LOW, toggling RTS from HIGH to LOW resets to run mode.
If RTS is HIGH, toggling DTR from LOW to HIGH resets to bootloader.
SJ3 allows selection between
FLASH_VDD and 3.3V as the
voltage supply to the flash IC.
U3
MMA8452Q
U6
W25Q32FVSS
U1
AP2112K-3.3V
JP7
MICRO-B
U4
FTS31XS-U
U5
MCP73831
U51
MAX30003CTI+
U53
MAX30205MTA+
GPIO5 LED
OSHW LOGO
OSHW LOGO
PROTOCENTRAL
heartyPatch
pc_heartypatch
06/04/17 9:51 PM
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