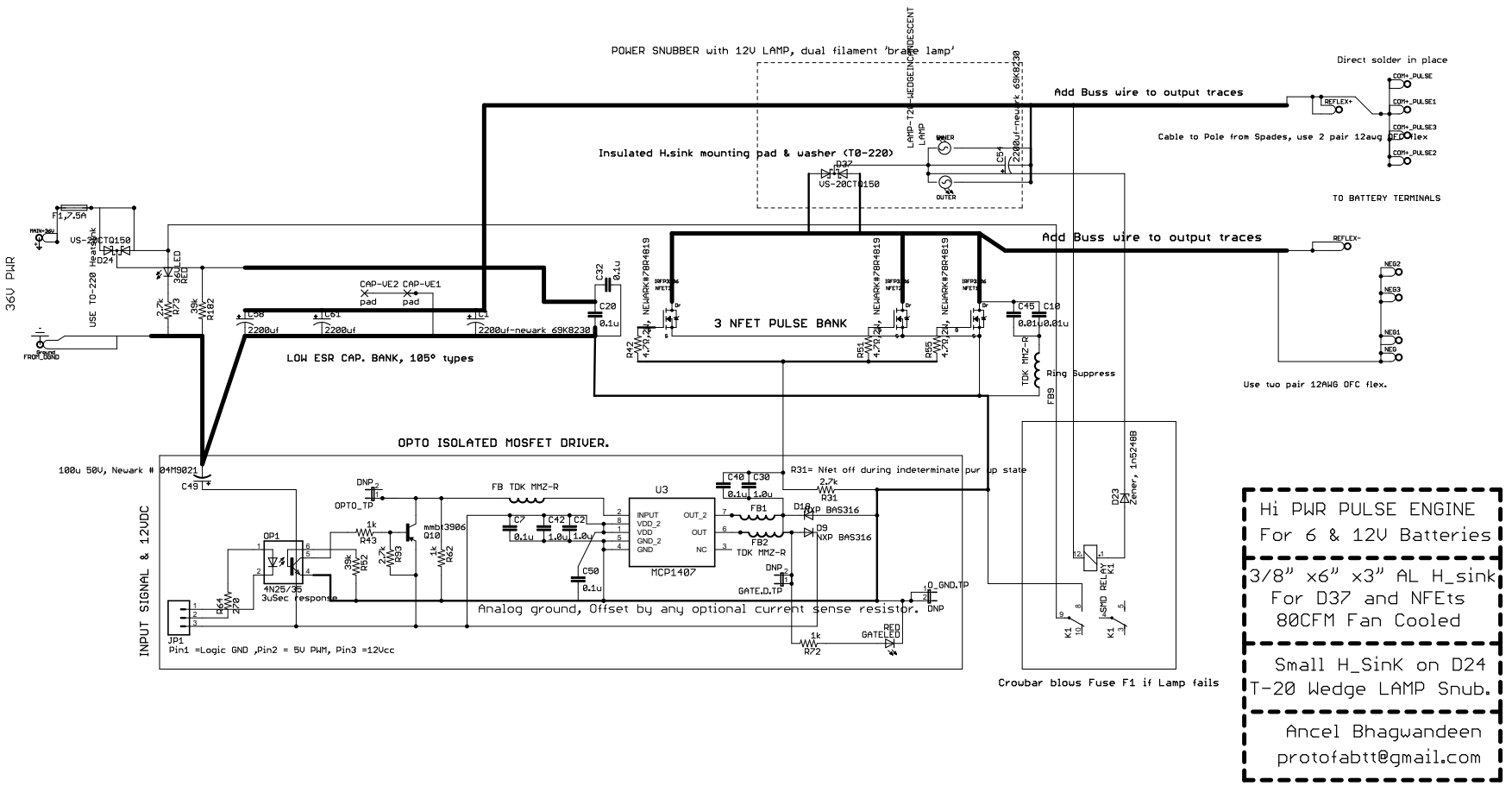
POWER SNUBBER with 12V LAMP, dual filament 'brake lamp'
Add Buss wire to output traces
Direct solder in place
Cable to Pole from Spades, use 2 pair 12awg OFC flex
TO BATTERY TERMINALS
Insulated H.sink mounting pad & washer (T0-220)
LAMP-T20-WEDGE INCANDESCENT LAMP
2200uf-newark 69K8230
VS-20CTQ150
Add Buss wire to output traces
REFLEX+
REFLEX-
COM+_PULSE
COM+_PULSE1
COM+_PULSE3
COM+_PULSE2
NEG2
NEG3
NEG1
NEG
36V PWR
F1 7.5A
VS-20CTQ150
Heatsink
USE TO-220
D24
360LED RED
2.7k R73
39k R182
CAP-VE2 CAP-VE1
pad pad
C38 2200uf
C61 2200uf
C2 2200uf-newark 69K8230
C32 0.1u
C20 0.1u
LOW ESR CAP. BANK, 105° types
3 NFET PULSE BANK
IRFP260N MFET6
NEWARK#78R4819
R42 4.7k 2W
R51 4.7k 2W
R55 4.7k 2W
C45 C10 0.01u 0.01u
TDK MMZ-R
FB3
Ring Suppress
Use two pair 12AWG OFC flex.
OPTO ISOLATED MOSFET DRIVER.
100u 50V, Newark # 01M9021
C49
INPUT SIGNAL & 12VDC
DNP
OPTO_TP
FB TDK MMZ-R
U3
MCP1407
INPUT
VDD_2
VDD
GND_2
GND
OUT_2
OUT
NC
C40 C30 0.1u 1.0u
R31= Nfet off during indeterminate pur up state
2.7k R31
FB1
FB2
TDK MMZ-R
D18 NXP BAS316
D9 NXP BAS316
GATE.D.TP
OP1
4N25/35
3uSec response
1k R43
mmbt3906 Q10
39k R52
2.7k R53
1k R62
R64 270
C7 0.1u
C42 1.0u
C2 1.0u
C50 0.1u
Analog ground, Offset by any optional current sense resistor.
0_GND.TP
DNP
1k R72
RED GATELED
JP1
Pin1 =Logic GND ,Pin2 = 5V PWM, Pin3 =12Vcc
D23 Zener, 1n5240B
K1 SMD RELAY-
Croubar blows Fuse F1 if Lamp fails
Hi PWR PULSE ENGINE
For 6 & 12V Batteries
3/8" x6" x3" AL H_sink
For D37 and NFETs
80CFM Fan Cooled
Small H_SinK on D24
T-20 Wedge LAMP Snub.
Ancel Bhagwandeen
protofabtt@gmail.com