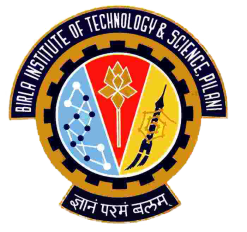
11/26/2017

Into the space



BIRLA INSTITUTE OF TECHNOLOGY & SCIENCE, PILANI

|  |  |
| --- | --- |
| Utsav Jana | 2017H1400173P |

BITS EEEG512 Embedded system Design

# **Flow charts or algorithm used or application description**

Start

End

S/T

P

LEVEL 2

reset