

Quad Single-Ended Line Drivers

The MC3481 and MC3485 are quad single–ended line drivers specifically designed to meet the IBM 360/370 I/O specification (GA22–6974–3).

Output levels are guaranteed over the full range of output load and fault conditions. Compliance with the IBM requirements for fault protection, flagging, and power up/power down protection for the bus make this an ideal line driver for party line operations.

- Separate Enable and Fault Flags MC3481
- Common Enable and Fault Flag MC3485
- Power Up/Down Does Not Disturb Bus
- Schottky Circuitry for High-Speed PNP Inputs
- Internal Bootstraps for Faster Rise Times
- Driver Output Current Foldback Protection
- MC3485 has LS Totem Pole Driver Output

IBM 360/370 QUAD LINE DRIVERS

MC3481 MC3485

SEMICONDUCTOR TECHNICAL DATA



P SUFFIX PLASTIC PACKAGE CASE 648





ORDERING INFORMATION

Device	Operating Temperature Range	Package	
MC3481P	$T_{A} = 0$ to 170° C	Plastic DIP	
MC3485P	$I_{A} = 0.0 + 70.0$		

© Motorola, Inc. 1995

MAXIMUM RATINGS ($T_A = 25^{\circ}C$, unless otherwise noted)

Rating	Symbol	Value	Unit
Power Supply Voltage	V _{CC}	+ 7.0	V
Input Voltage	VI	10	V
Driver Output Voltage	Vo	5.5	V
Power Dissipation (Package Limitation) Derate Above T _A = 25°C	Ρ _D 1/R _{θJA}	962 7.7	mW mW°C
Operating Ambient Temperature Range	ТА	0 to + 70	°C
Junction Temperature	Тј	+ 150	°C
Storage Temperature Range	T _{stg}	65 to + 150	°C

RECOMMENDED OPERATING CONDITIONS

Characteristic	Symbol	Min	Тур	Max	Unit
Power Supply Voltage	V _{CC}	4.5	5.0	5.95	Vdc
High Level Output Current	IОН	-	-	59.3	mA
Operating Ambient Temperature Range	Т _А	0	_	+ 70	°C

SWITCHING CHARACTERISTICS (See Note 1. Unless otherwise noted, these specifications apply over recommended temperature range. I/O Driver characteristics are guaranteed for $V_{CC} = 5.0 V \pm 10 \%$ and Select–Out Driver characteristics are guaranteed for $V_{CC} = 5.25$ to 5.95 V. Typical values measured at T_A = 25 °C and $V_{CC} = 5.0 V$. See Tables 1 and 2, Figures 1 and 2 for load conditions.)

Characteristics	Symbol	Min	Тур	Max	Unit
Propagation Delay Time					ns
High-to-Low-Level, Driver Output					
As I/O Driver	^t PHL(D)	-	18	-	
As Select–Out Driver	^t PHL(DS)	-	19	-	
Low-to-High-Level, Driver Output	· · · ·				
As I/O Driver	^t PLH(D)	-	20	-	
As Select–Out Driv <u>er</u>	^t PLH(DS)	-	21	-	
High-to-Low-Level, Driver Output	. ,				
As I/O Driver	^t PHL(D)	-	25	-	
As Select–Out Driver	^t PHL(DS)	-	26	-	
Low-to-High-Level, Driver Output	. ,				
As I/O Driver	^t PLH(D)	-	25	-	
As Select–Out Driv <u>er</u>	tPLH(DS)	-	26	-	
High–to–Low–Level, Fault Flag – MC3481	. ,				
As I/O Driver	^t PHL(F)	-	45	-	
As Select–Out Driver	^t PHL(FS)	-	47	-	
Low–to–High–Level, Fault Flag – MC3481	. ,				
As I/O Driver	^t PLH(F)	-	40	-	
As Select–Out Driver	^t PLH(FS)	-	42	-	
Ratio of Propagation Delay Times	^t PLH(D)	_	1.0	_	
As I/O Driver	^t PHL(D)				

NOTES: 1. Reference IBM specification GA22-6974-3 for test terminology.

2. The fault protection circuitry of the MC3481 and MC3485 requires relatively clean input voltage waveforms for current operation. Noise pulses which enter the threshold region (0.8 to 2.0 V) may cause the output to enter the fault protect mode. To exit the protect mode, it is necessary to gate an input of the effected driver to the low logic state.

ELECTRICAL CHARACTERISTICS (Unless otherwise noted, these specifications apply over recommended	power supply and
temperature ratings. Typical values measured at $T_A = 25^{\circ}C$ and $V_{CC} = +5.0 \text{ V}$	

		MC3481		MC3485				
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
High-Level Input Voltage Note 2	VIH	2.0	-	-	2.0	-	-	V
Low-Level Input Voltage Note 2	VIL	-	-	0.8	-	-	0.8	V
$\begin{array}{l} \mbox{High-Level Input Current} \\ (V_{CC} = 4.5 \mbox{ V, } V_{IH} = 2.7 \mbox{ V)} & - \mbox{ Input} \\ & \mbox{Enable} \\ (V_{CC} = 4.5 \mbox{ V, } V_{IH} = 5.5 \mbox{ V)} & - \mbox{ Input} \\ & \mbox{Enable} \end{array}$	lιΗ	- - - -	_ _ _ _	20 40 100 200	- - -	- - - -	20 80 100 400	μA
Low–Level Input Current (V _{CC} = 5.95 V, V _{IL} = 0.4 V) – Input Enable	ΙL			-250 -500			- 250 - 1000	μΑ
Input Clamp Voltage (I _{IC} = -18 mA)	VIC	_	_	-1.5	-	-	- 1.5	V
High–Level Driver Output Voltage (V _{CC} = 4.5 V, V _{IH} = 2.0 V, I _{OH} = -59.3 mA) (V _{CC} = 5.25 V, V _{IH} = 2.0 V, I _{OH} = -41 mA)	V _{OH(D)} V _{OH(DS)}	3.11 3.9	3.6 -		3.11 3.9	3.6 -	-	V
Low-Level Driver Output Voltage (V _{CC} = 5.5 V, V _{IL} = 0.8 V, I _{OL} = -240 μ A) (V _{CC} = 5.95 V, V _{IL} = 0.8 V, I _{OL} = -1.0 mA)	V _{OL(D)} V _{OL(DS)}	-	-	+ 0.15 + 0.15	-	-	+ 0.15 + 0.15	V
Driver Output Short Circuit Current (V _{CC} = 5.5 V, V _{IH} = 2.0 V, V _{OS} = 0 V) (V _{CC} = 5.95 V, V _{IH} = 2.0 V, V _{OS} = 0 V)	I _{OS(D)} I _{OS(DS)}			-5.0 -5.0	-		- 5.0 - 5.0	mA
Driver Output Reverse Leakage Current (V _{CC} = 4.5 V, V _{IL} = 0 V, V _O = 3.11 V) (V _{CC} = 0 V, V _{IL} = 0 V, V _O = 3.11 V)	I _{OR1} I _{OR2}		_ _	+100 +200	-	- -	+ 100 + 200	μΑ
High–Level Driver Output Voltage (V _{CC} = 4.5 V, V _{IL} = 0.8 V, I _{OH} = -400 μA)	VOH(D)	-	-	-	2.5	3.0	-	V
Low–Level Driver Output Voltage (V_{CC} = 4.5 V, V_{IH} = 2.0 V, I_{OL} = 8.0 mA)	VOL(D)	-	-	-	_	-	0.5	V
Driver Output Short Circuit Current ($V_{CC} = 5.5 \text{ V}, V_{OS} = 0 \text{ V}$, only one output shorted at a time) ($V_{CC} = 5.95 \text{ V}, V_{OS} = 0 \text{ V}$, only one ouput shorted	I _{OS(D)}	-	_	-	-15 -15	- 60 -	-100 -110	mA
at a time) High–Level Fault Flag Output Voltage (Vcc = 4.5 V lou = -400 µA)	V _{OH(F)}	2 5	3.0	-	-	-	-	V
Low-Level Fault Flag Output Voltage $(V_{CC} = 4.5 \text{ V}, V_{IH} = 2.0 \text{ V}, I_{OL} = 8.0 \text{ mA},$ Driver Output shorted to Ground	V _{OL} (F)	-	_	0.5	_	_	0.5	V
Fault Flag Output Short Circuit Current (V _{CC} = 5.5 V, V _{OS} =0 V, only one output shorted	I _{OS(F)}	- 15	_	-100	_	_	_	mA
$(V_{CC} = 5.95 \text{ V}, V_{OS} = 0 \text{ V}, \text{ only one output shorted})$ at a time)	IOS(FS)	- 15	-	-110	_	-	_	
High–Level Fault Flag Output Current (V _{CC} = 5.95 V, V _{OH} = 5.95 V)	IOH(F)	_	-	-	-	-	+100	μΑ
High–Level Power Supply Current ($V_{CC} = 5.5 \text{ V}$, $V_{IH} = 2.0 \text{ V}$, no output loading) ($V_{CC} = 5.95 \text{ V}$, $V_{IH} = 2.0 \text{ V}$, no output loading)	I _{ССН} Iсснs	-	50 -	70 80	-	55 -	75 85	mA
$\label{eq:loss} \begin{array}{l} \mbox{Low-Level Power Supply Current} \\ (V_{CC} = 5.5 \mbox{ V, } V_{IL} = 0.8 \mbox{ V, no output loading}) \\ (V_{CC} = 5.95 \mbox{ V, } V_{IL} = 0.8 \mbox{ V, no output loading}) \end{array}$	I _{CCL} I _{CCLS}		35 –	55 70	-	35 -	55 70	mA

Figure 1. MC3481 AC Test Circuit and Waveforms







Figure 2. MC3485 AC Test Circuit and Waveforms



Table	Driver Application				
2	I/O	Select-Out			
V _{OH}	3.11 V	3.9 V			
Input Frequency	5 MHz	1 MHz			
Input Pulse Width	100 ns	500 ns			
Input Amplitude	0 V to 4 V	0 V to 4 V			
Input t _{TLH}	≤6 ns	≤6 ns			
Input t _{THL}	≤6 ns	≤6 ns			
Load Resistance (RL)	50	90			



OUTLINE DIMENSIONS



Motorola reserves the right to make changes without further notice to any products herein. Motorola makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Motorola assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters can and do vary in different applications. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. Motorola does not convey any license under its patent rights nor the rights of others. Motorola products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Motorola product could create a situation where personal injury or death may occur. Should Buyer purchase or use Motorola products for any such unintended or unauthorized application, Buyer shall indemnify and hold Motorola and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Motorola was negligent regarding the design or manufacture of the part. Motorola and an eregistered trademarks of Motorola, Inc. Motorola, Inc. is an Equal Opportunity/Affirmative Action Employer.

How to reach us:

USA/EUROPE: Motorola Literature Distribution; P.O. Box 20912; Phoenix, Arizona 85036. 1–800–441–2447 JAPAN: Nippon Motorola Ltd.; Tatsumi–SPD–JLDC, Toshikatsu Otsuki, 6F Seibu–Butsuryu–Center, 3–14–2 Tatsumi Koto–Ku, Tokyo 135, Japan. 03–3521–8315

MFAX: RMFAX0@email.sps.mot.com – TOUCHTONE (602) 244–6609 INTERNET: http://Design-NET.com HONG KONG: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852–26629298



