Demonstrates ternary gates using only MOSFETs. Specific configurations were chosen based on the lowest current usage within acceptable voltage ranges.

Sheet: / File: TernMOSFETLogic copy.sch
Title: Ternary Gates 1–8 Using MOSFETs
Size: US Letter Date: 2016-07-01
KICad EDA, eschema 4.0.0-stable
Id: 1/1